

Module 918: Variable Voltage Supply

Procedure

Build the following circuit. Use the rechargeable NiMH battery to power the Schmitt trigger inverter chip. There are portions of relevant datasheets on the back side of this paper.

Turn on the oscilloscope and press the Default Setup button. Use **only** the horizontal and vertical scale adjustments and the trigger and meas menus to complete today's task to improve your skill set.

Use the oscilloscope to view both voltages V_1 and V_2 as the $2\text{ k}\Omega$ potentiometer is adjusted.

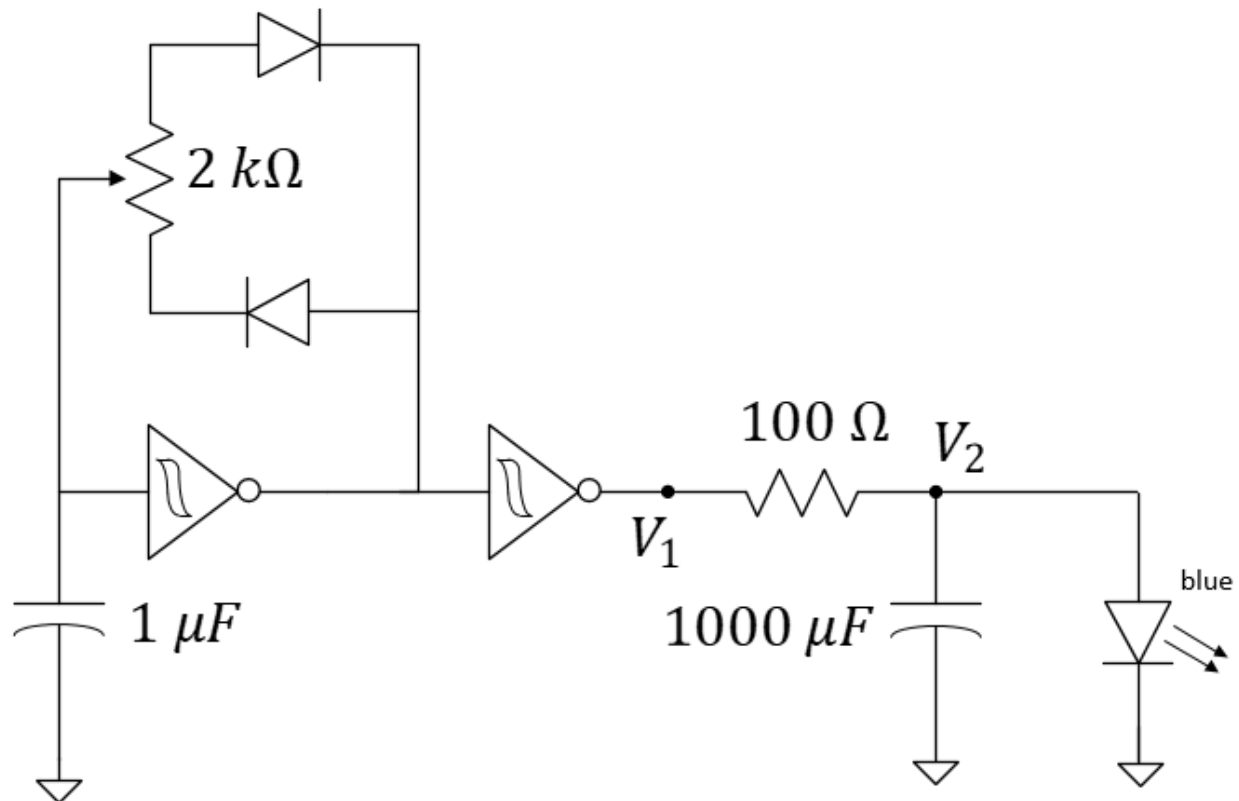


Figure 1: PWM-to-DC voltage circuit.

Verify that adjustment of the $2\text{ k}\Omega$ potentiometer will cause the voltage V_2 to increase and then stop at some value. Use the oscilloscope to measure and record the voltage V_2 (to two digits) at which V_2 significantly slows in its growth. Let your TA know you are ready for evaluation to receive your grade for this module.

Evaluation

- Ability to map a circuit design onto the breadboard in a functional and clean manner.
- Ability to use the oscilloscope.
- Ability to troubleshoot problems that occur during a build.

CD40106 CMOS Hex Schmitt Triggers

Description

CD40106BMS consists of six Schmitt trigger circuits. Each circuit functions as an inverter with Schmitt trigger action on the input. The trigger switches at different points for positive and negative going signals. The difference between the positive going voltage (V_P) and the negative going voltage (V_N) is defined as hysteresis voltage (V_H) (see Figure 17).

Features

- High Voltage Type (20V Rating)
- Schmitt Trigger Action with No External Components
- Hysteresis Voltage (Typ.)
 - 0.9V at $V_{DD} = 5V$
 - 2.3V at $V_{DD} = 10V$
 - 3.5V at $V_{DD} = 15V$
- Noise Immunity Greater than 50%
- No Limit on Input Rise and Fall Times
- Low V_{DD} to V_{SS} Current During Slow Input Ramp
- 100% Tested for Quiescent Current at 20V
- 5V, 10V and 15V Parametric Ratings
- Maximum Input Current of $1\mu A$ at 18V Over Full Package Temperature Range; 100nA at 18V and $+25^\circ C$
- Standardized Symmetrical Output Characteristics
- Meets All Requirements of JEDEC Tentative Standard No. 13B, "Standard Specifications for Description of 'B' Series CMOS Devices"

CD40106BMS
TOP VIEW

