

# Syllabus

## BTCS03CC31 Microprocessors & Microcontrollers

Sr. No	Module/Units	Detailed Topic wise Syllabus (In bullet points)	Total Hours (L +T+P+ S)
1	<b>Microprocessor Architecture</b>	<ul style="list-style-type: none"> <li>Microprocessors, Microcontrollers</li> <li>Application of microprocessor and Microcontroller</li> <li>Overview of 8085 microprocessor. <ul style="list-style-type: none"> <li>Pins and architecture of 8085 microprocessor.</li> </ul> </li> <li>Addressing modes and Instruction sets</li> </ul>	Lecture/Theory Duration (hh.mm): 09.00  Practical Duration (hh.mm):06:00  Skill Duration (hh.mm): 06.00
2	<b>Programming and interfacing</b>	<ul style="list-style-type: none"> <li>Assembly Language Programming: <ul style="list-style-type: none"> <li>Assembler directives,</li> <li>simple examples; Subroutines.</li> </ul> </li> <li>Interfacing: Interfacing of memory chips, <ul style="list-style-type: none"> <li>address allocation technique and decoding; <ul style="list-style-type: none"> <li>Interfacing of I/O devices,</li> </ul> </li> </ul> </li> <li>LEDs and toggle-switches as examples,</li> <li>memory mapped and isolated I/O structure.</li> </ul>	Lecture/Theory Duration (hh.mm): 14.00  Practical Duration (hh.mm):06:00  Skill Duration (hh.mm): 06.00
3	<b>PIC Microcontroller</b>	<ul style="list-style-type: none"> <li>Harvard Vs Von-Neumann Architecture; <ul style="list-style-type: none"> <li>RISC Vs CISC.</li> <li>CPU architecture,</li> <li>Registers and Memory</li> <li>Addressing modes <ul style="list-style-type: none"> <li>instruction sets</li> </ul> </li> <li>programming examples</li> </ul> </li> </ul>	Lecture/Theory Duration (hh.mm): 09.00  Practical Duration (hh.mm):06:00  Skill Duration (hh.mm): 06.00
4	<b>Interfacing with PIC</b>	<ul style="list-style-type: none"> <li>Timer/counter,</li> <li>interrupt programming,</li> <li>Interfacing and programming of: ADC &amp; DAC <ul style="list-style-type: none"> <li>Interfacing with sensors,</li> </ul> </li> <li>Interfacing with keyboard, <ul style="list-style-type: none"> <li>Relays, LED,</li> </ul> </li> <li>Seven segment display</li> <li>LCD interfacing.</li> </ul>	Lecture/Theory Duration (hh.mm): 09.00  Practical Duration (hh.mm):06:00  Skill Duration (hh.mm): 06.00
5	<b>ARM Cortex M-3 Microcontroller</b>	<ul style="list-style-type: none"> <li>Introduction of cortex M3 and its applications</li> <li>Architecture of cortex-M3,</li> </ul>	Lecture/Theory Duration (hh.mm): 04.00

		<ul style="list-style-type: none"> <li>Registers, special purpose registers <ul style="list-style-type: none"> <li>Memory map,</li> <li>Instruction sets,</li> </ul> </li> <li>Interfacing and Programming examples</li> </ul>	Practical Duration (hh.mm):06:00  Skill Duration (hh.mm): 06.00
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## Learning Resources

Sr . No	Module / Unit	Text Books	Reference Book / Paper / Article / Online Resource (link) / Other
1	Microprocessor Architecture	Microprocessor Architecture, Programming, and Applications with the 8085 by R Gaonker 4 th Edition, Wiley,2012	<a href="http://nptel.ac.in/courses/108107029/">http://nptel.ac.in/courses/108107029/</a>
2	Programming and interfacing	Microprocessor Architecture, Programming, and Applications with the 8085 by R Gaonker 4 th Edition, Wiley,2012	<a href="http://nptel.ac.in/courses/108107029/">http://nptel.ac.in/courses/108107029/</a>
3	PIC Microcontroller	The PIC Microcontroller and Embedded Systems using Assembly and C by Mazidi, Mazidi & McKinlay 2 nd Edition PHI	<a href="http://nptel.ac.in/courses/Webcourse-contents/IIT-KANPUR/microcontrollers/pic.pdf">http://nptel.ac.in/courses/Webcourse-contents/IIT-KANPUR/microcontrollers/pic.pdf</a>
4	Interfacing with PIC	The PIC Microcontroller and Embedded Systems using Assembly and C by Mazidi, Mazidi & McKinlay 2 nd Edition PHI	<a href="http://nptel.ac.in/courses/Webcourse-contents/IIT-KANPUR/microcontrollers/micro/ui/Course_home3_16.htm">http://nptel.ac.in/courses/Webcourse-contents/IIT-KANPUR/microcontrollers/micro/ui/Course_home3_16.htm</a>
5	ARM Cortex M-3 Microcontroller	The definitive guide to ARM Cortex M-3 by Joseph Yiu 2nd Edition, Elsevier	<a href="https://www.arm.com/ja/files/pdf/IntroToCortex-M3.pdf">https://www.arm.com/ja/files/pdf/IntroToCortex-M3.pdf</a>