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MILANO 1863

DIPARTIMENTO DI ELETTRONICA
INFORMAZIONE E BIOINGEGNERIA

Packers & Evasion

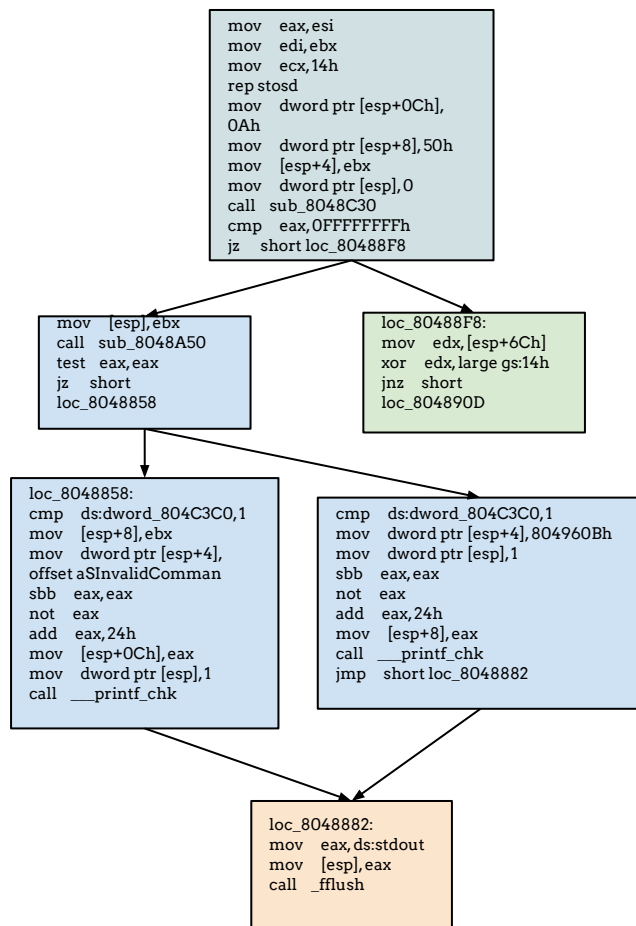
Mario Polino,

Malware Analysis

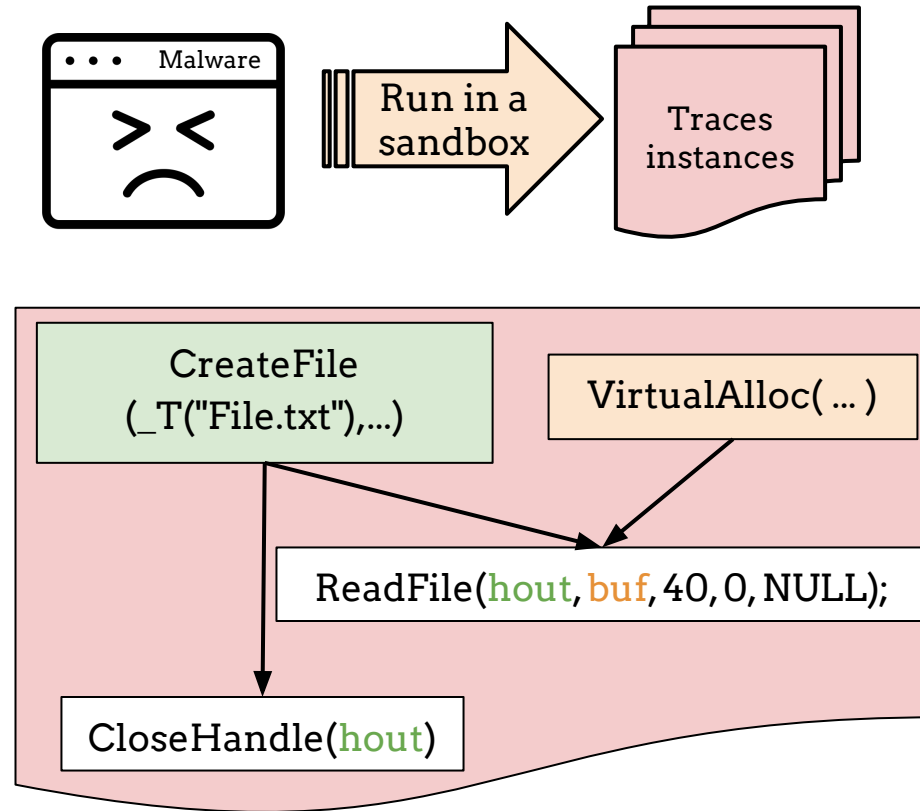


Malware Analysis

Static



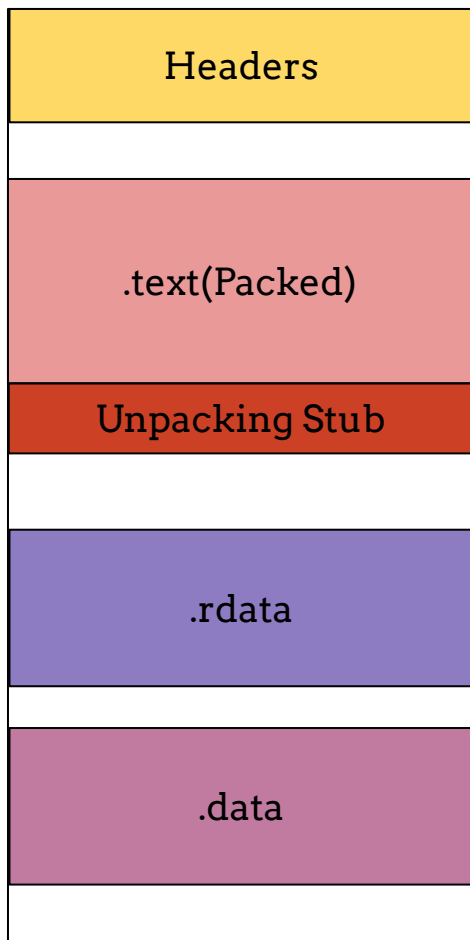
Dynamic





Static & Dynamic Issues

Packers



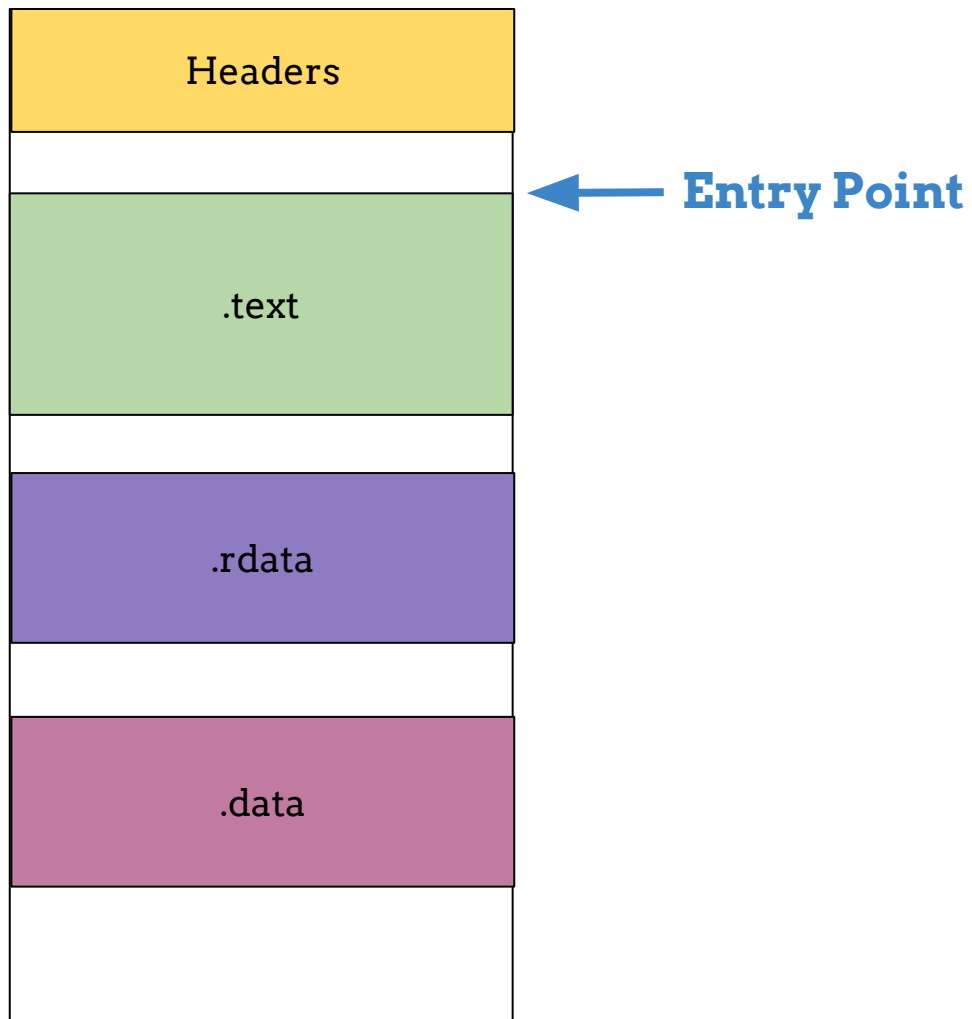
Evasive Malware

```
If (amIUnderAnalysis())  
{  
    die();  
}  
else  
{  
    beMalicious();  
}
```



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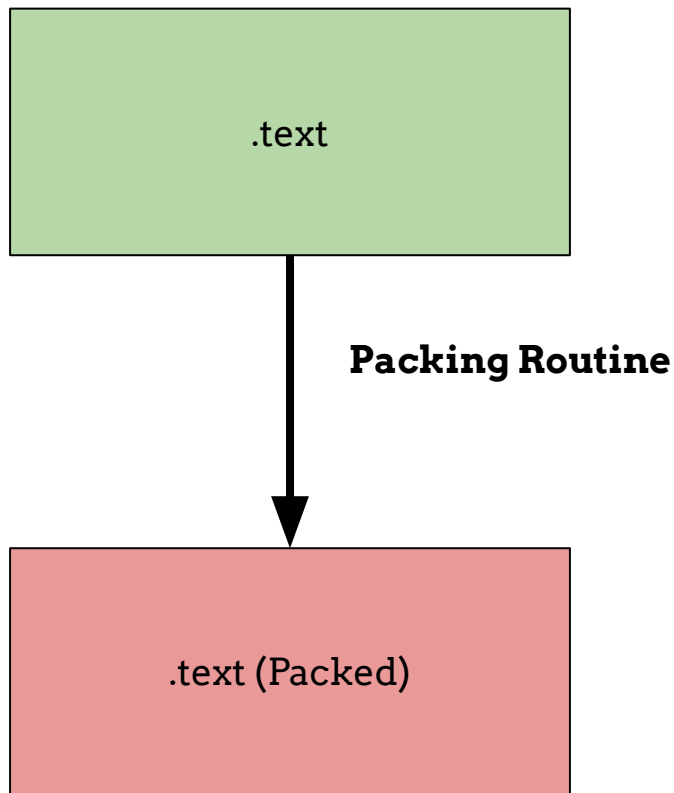
How does a packed malware work?





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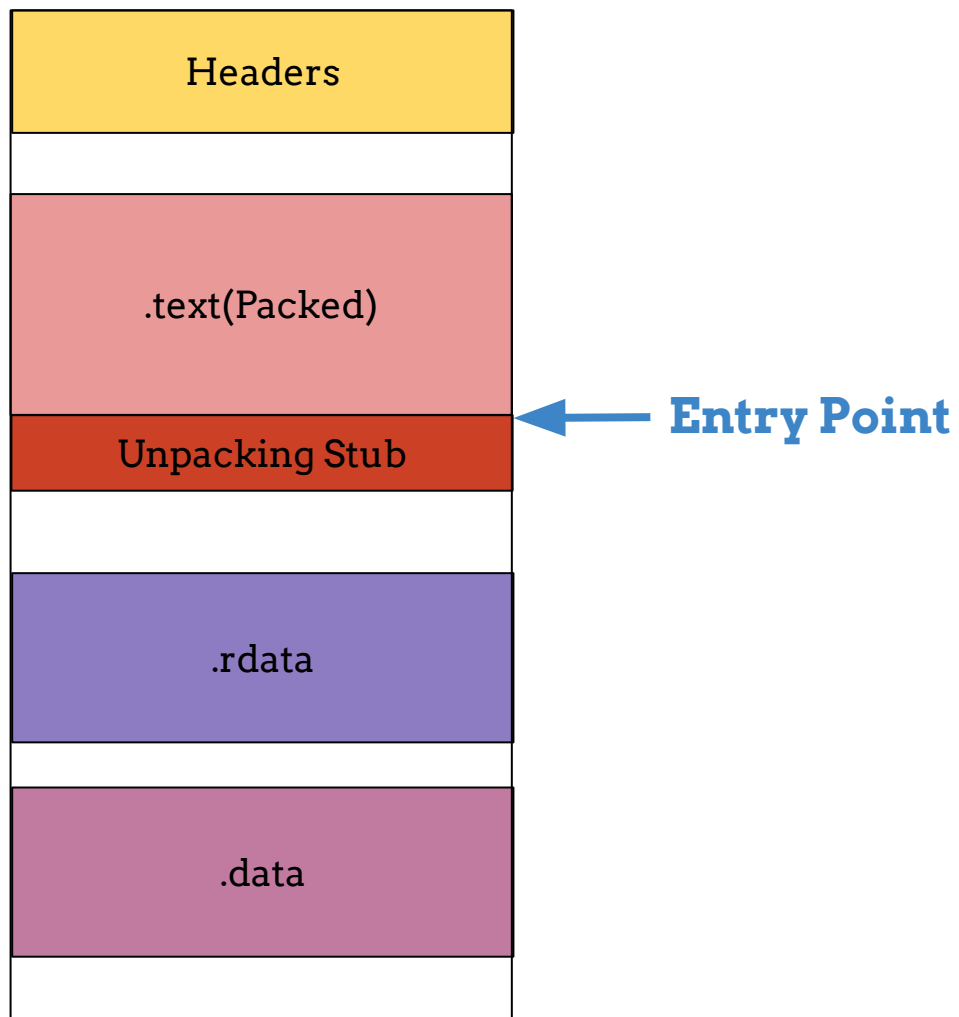
How does a packed malware work?





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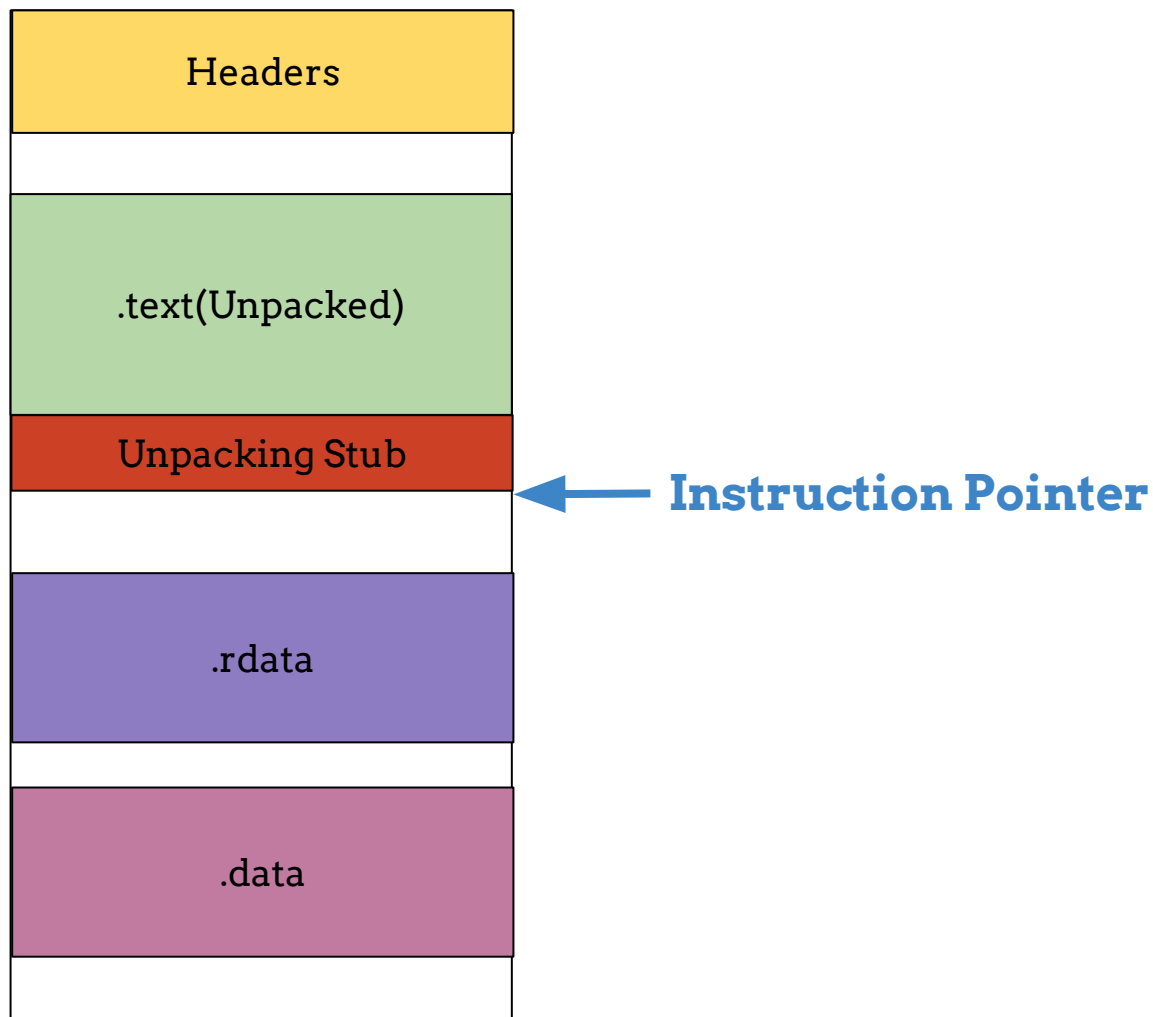
How does a packed malware work?

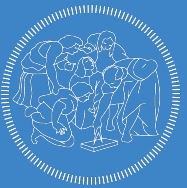




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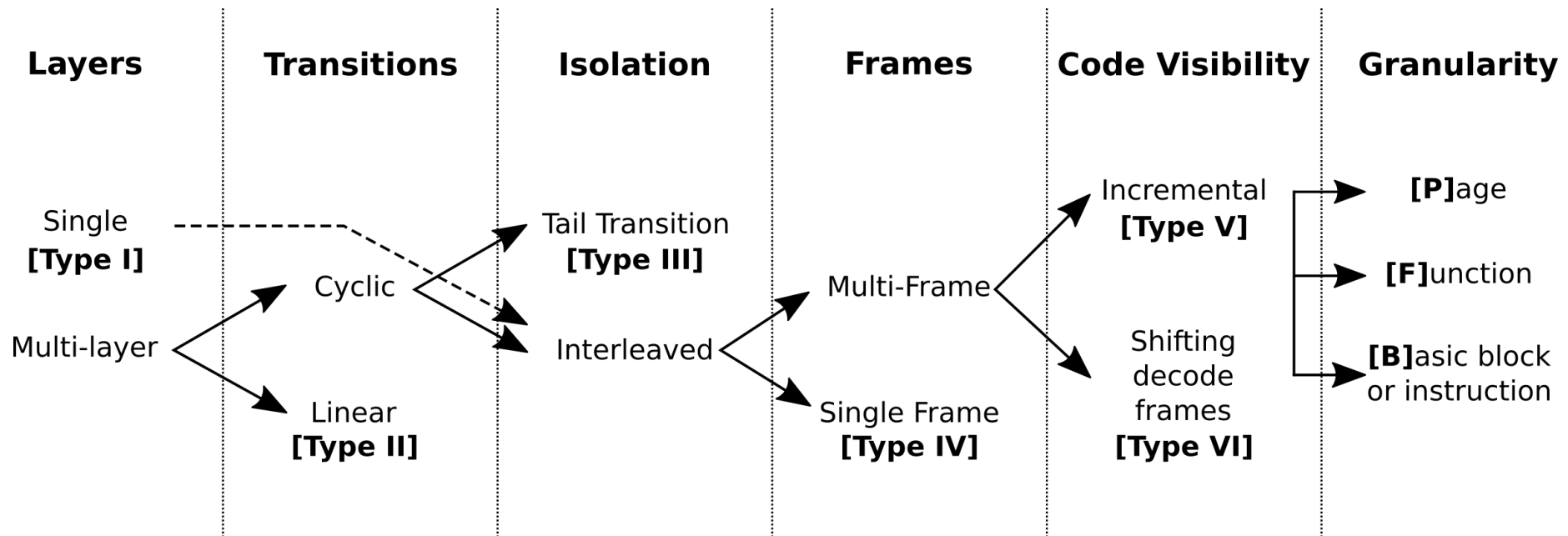
How does a packed malware work?





Packers Complexity

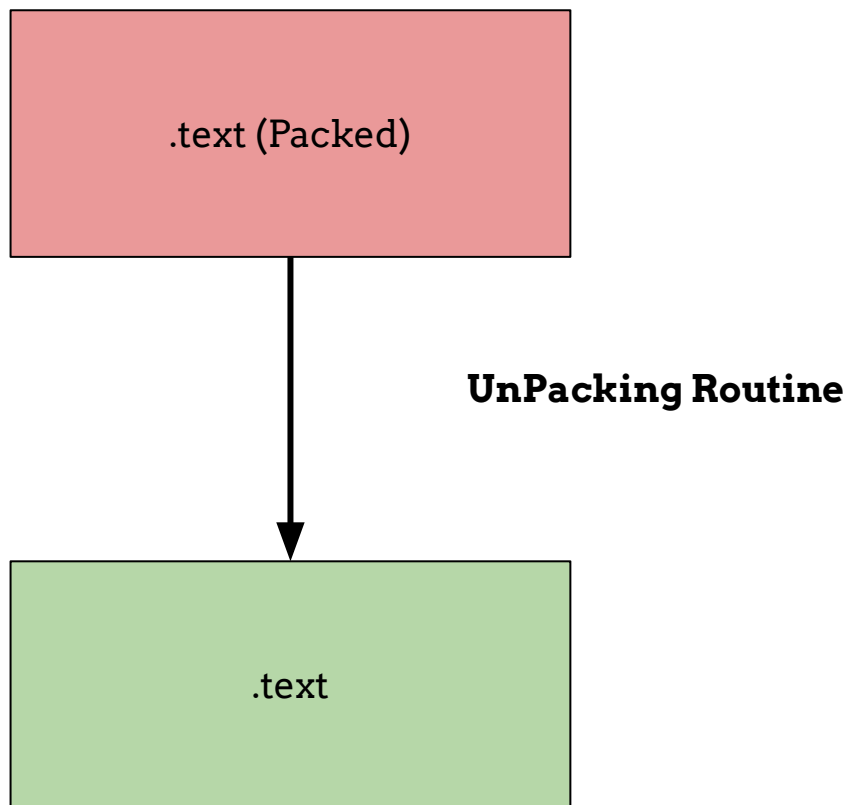
From **Unpacked** to **Custom VM** there is a huge **gray** area



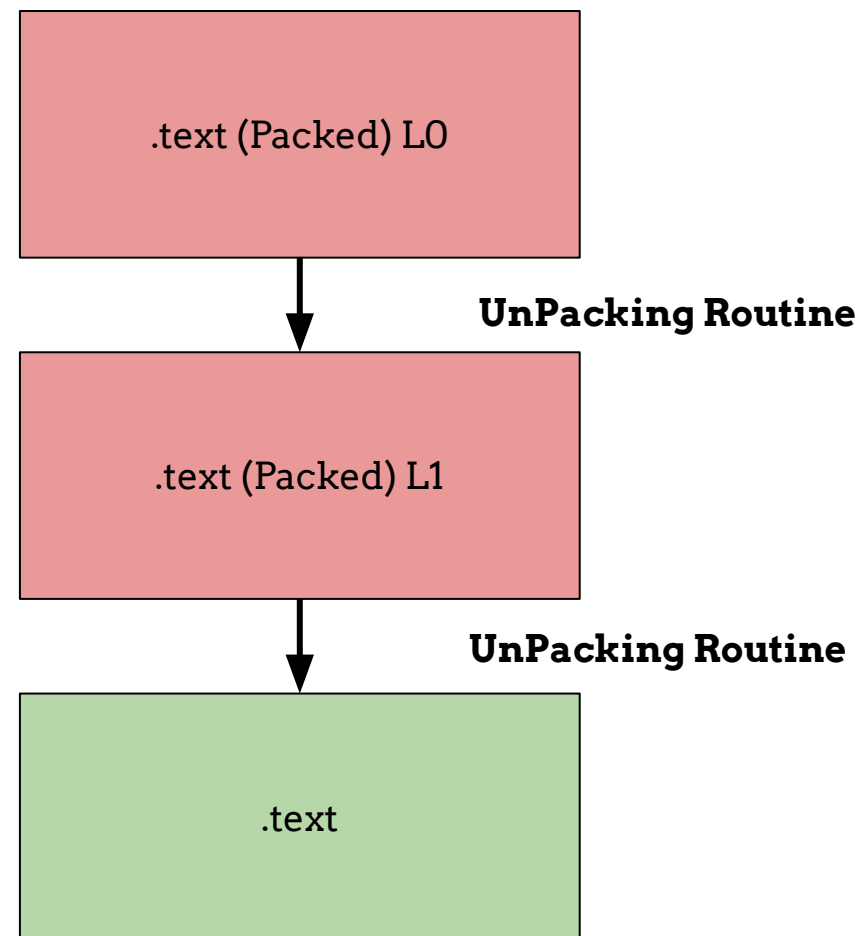


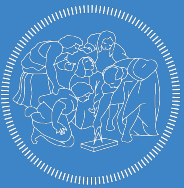
Layers

Type I



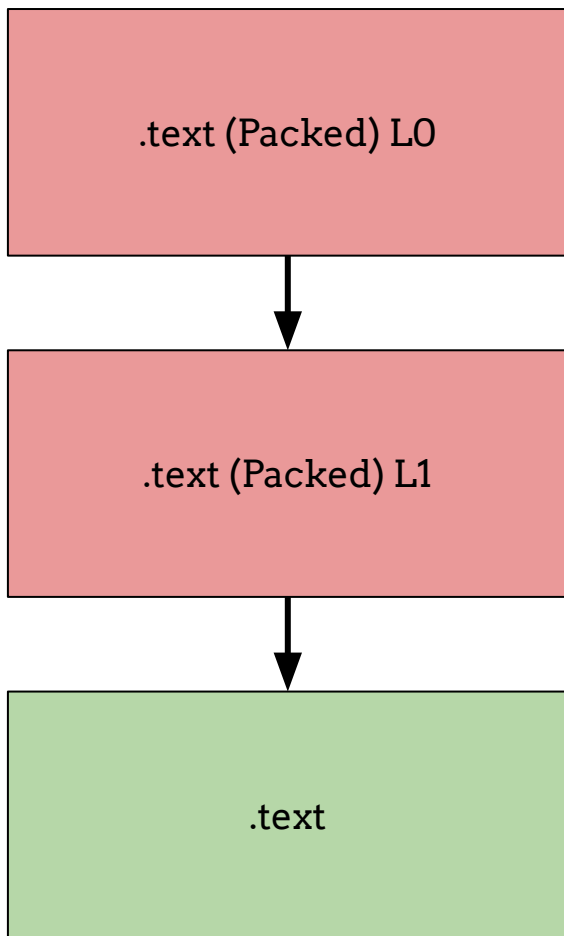
Type \geq II



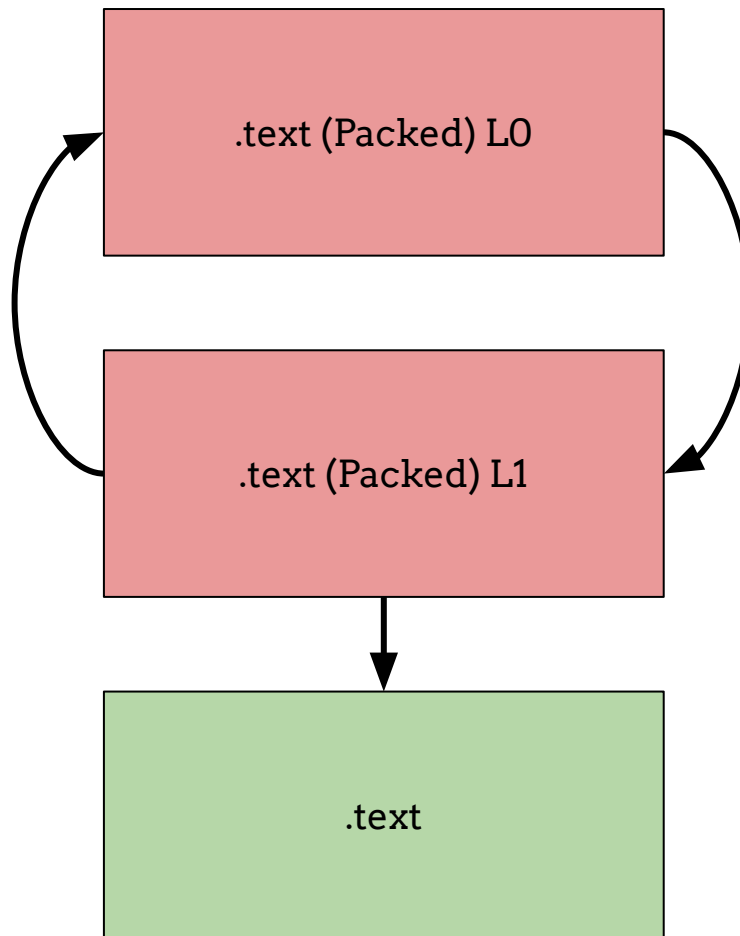


Transaction & Isolation

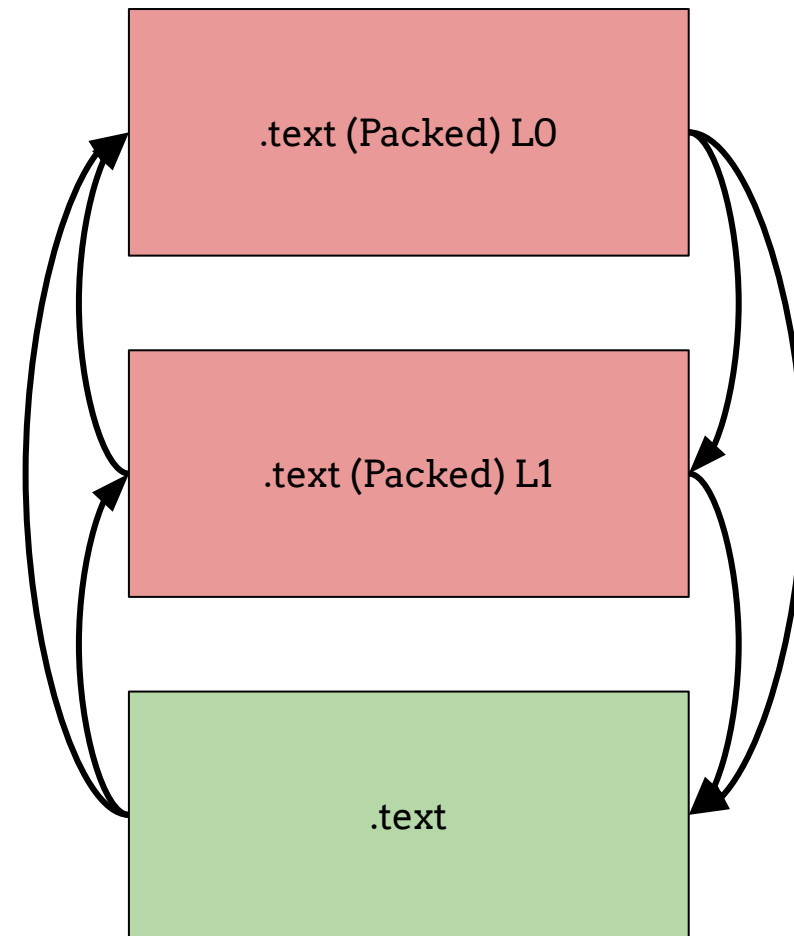
Linear: Type II



Cyclic: Type III



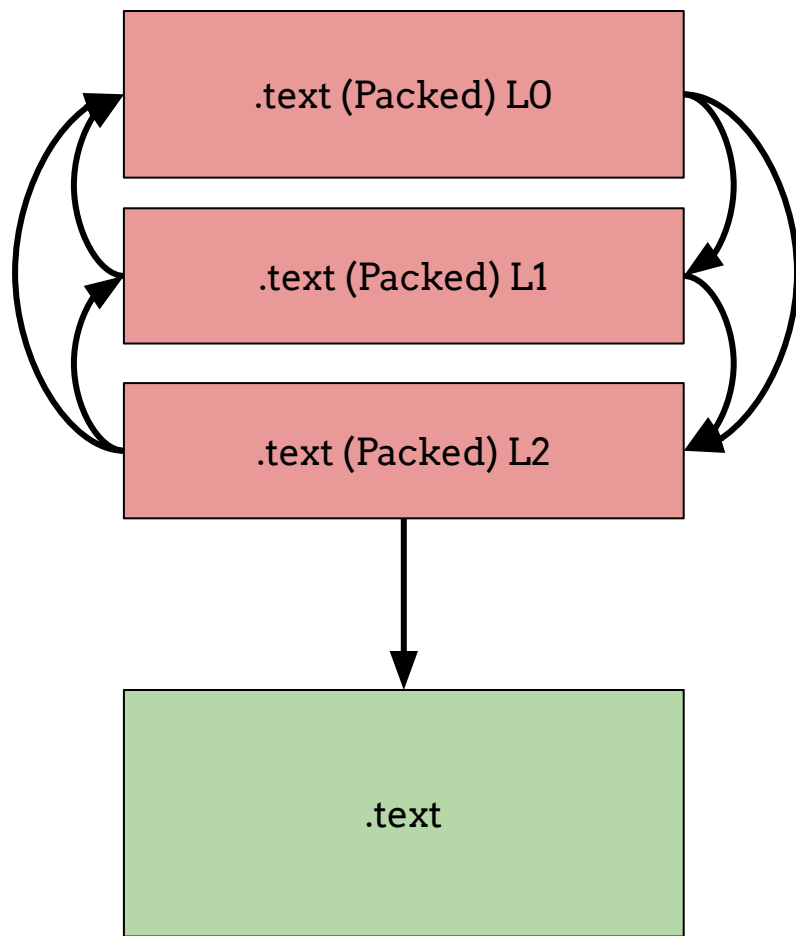
Interleaved: Type \geq IV



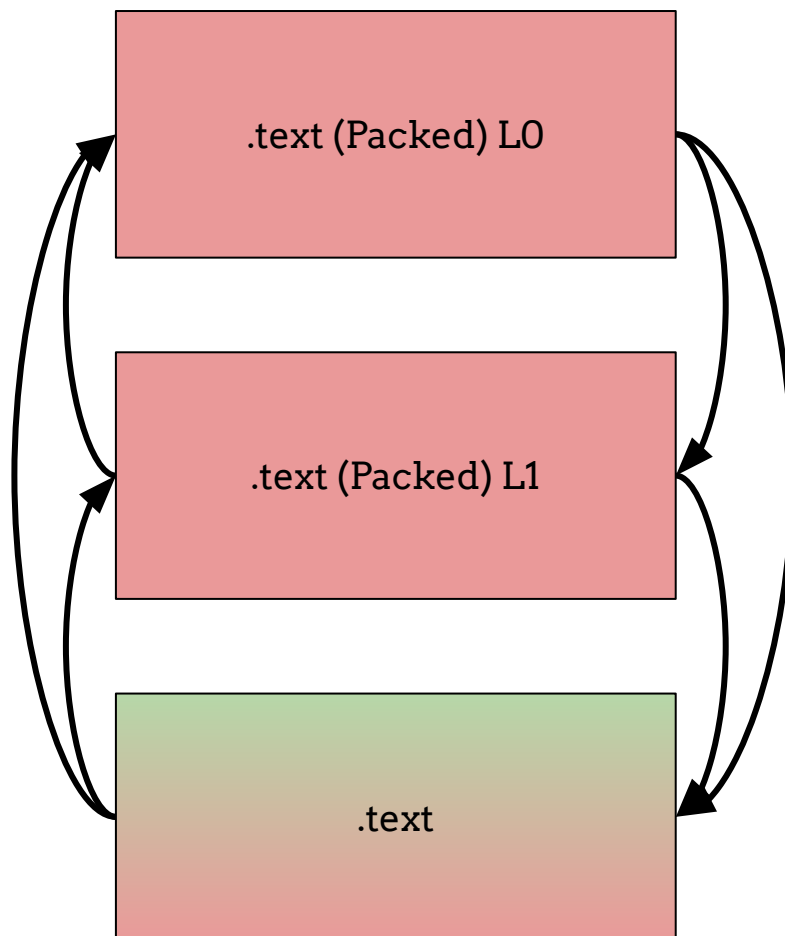


Code Visibility

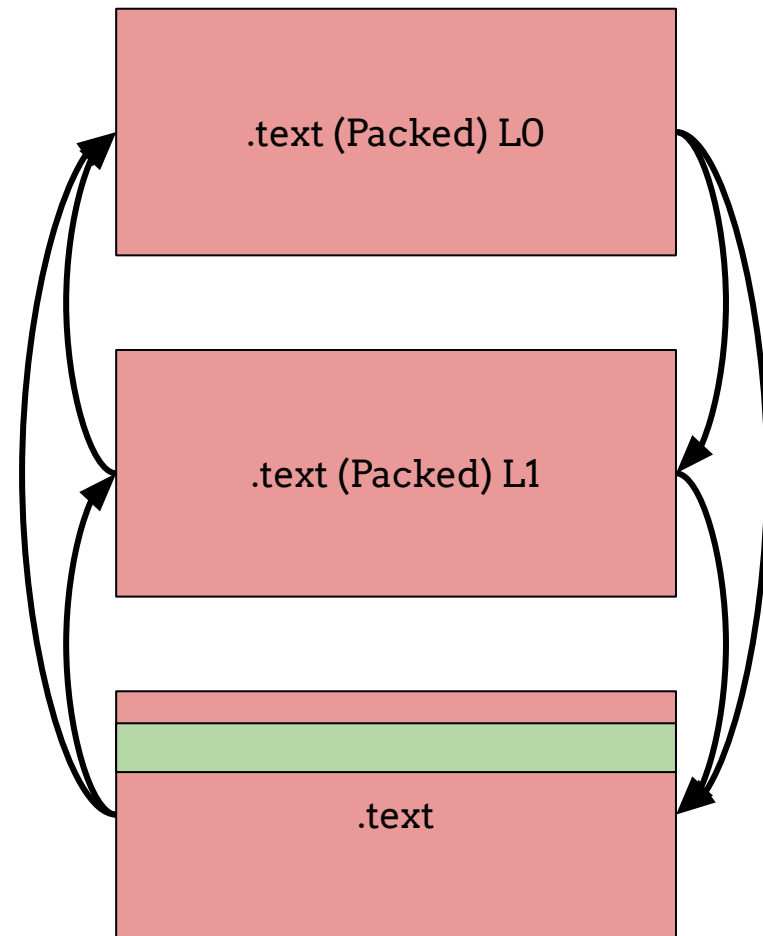
Full Code: Type IV

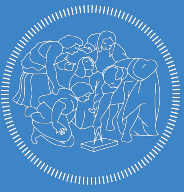


Incremental: Type V



Shift Frame: Type V

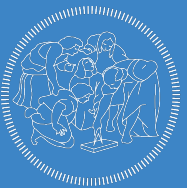




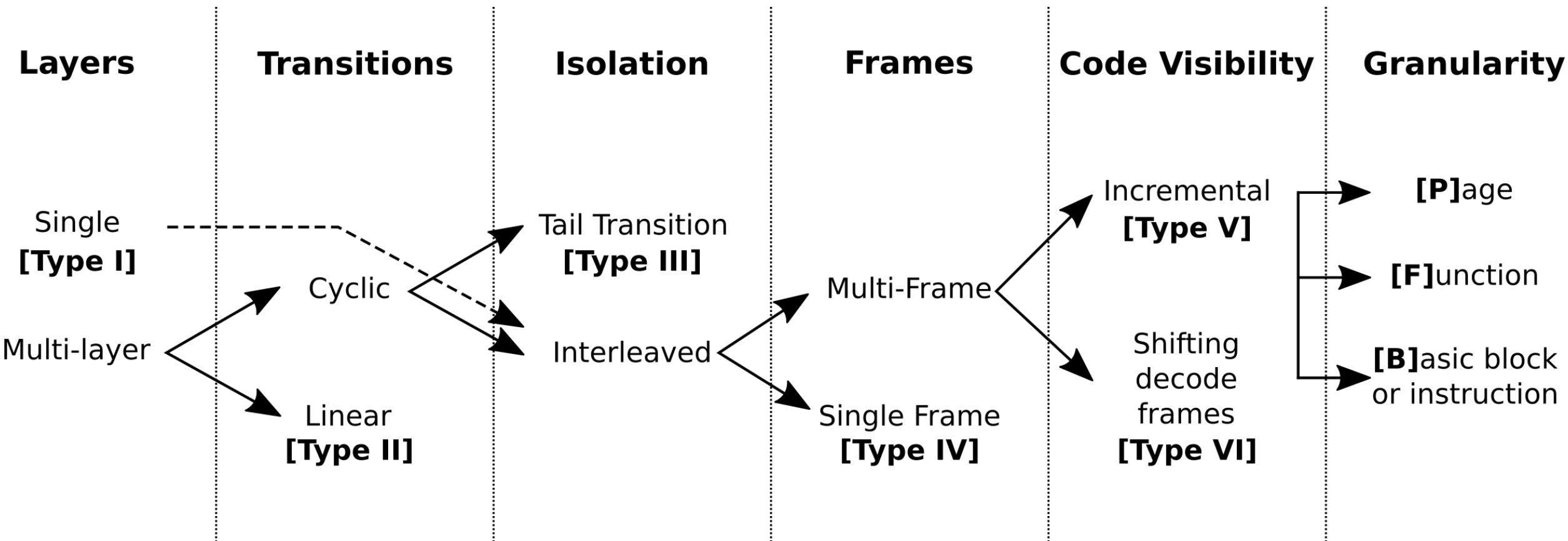
Granularity

- **Pages**
- **Functions**
- **Basic Blocks**
- **Instructions**





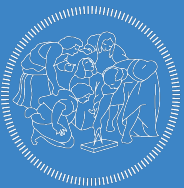
Packers Complexity





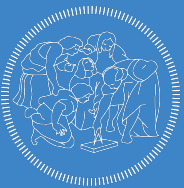
Examples of Packers

Type		Packer
I	Single Layer	UPX
II	Linear Multi Layer	Custom Packer
III	Cyclic Multi Layer	Themida
IV	Interleaved Single Frame	Upack
V	Incremental Multi-Frame	Beria
VI	Shifting Frames	Armadillo



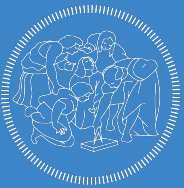
Usage of Packers

Type	Off-the-Shelf	Custom Packer
I	25.3 % (173)	7.3% (443)
II	8.2 % (56)	12.4% (752)
III	51.4 % (352)	65.6 % (3993)
IV	12.6% (86)	13.8% (843)
V	0.9% (6)	0.8% (46)
VI	1.8% (12)	0.2% (11)

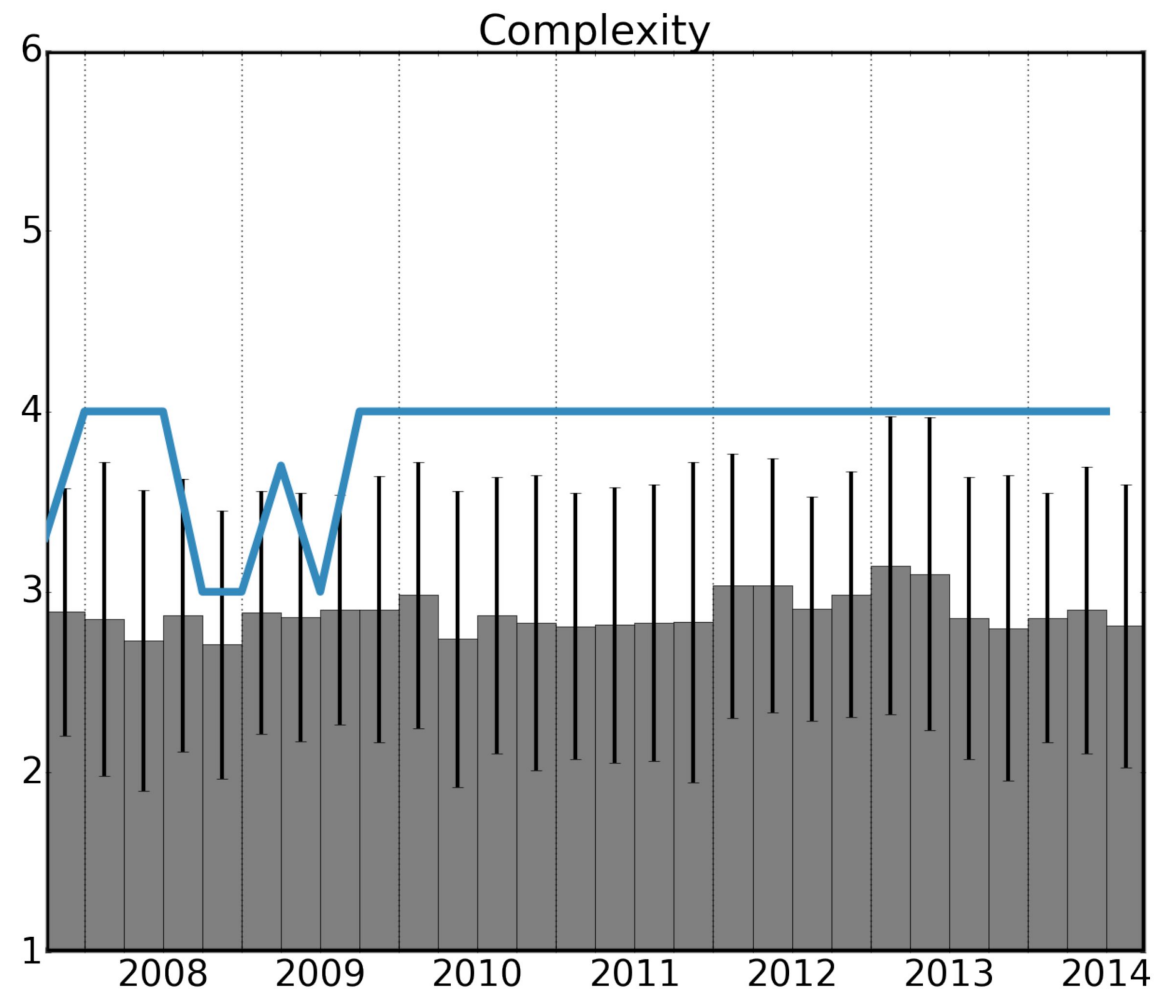


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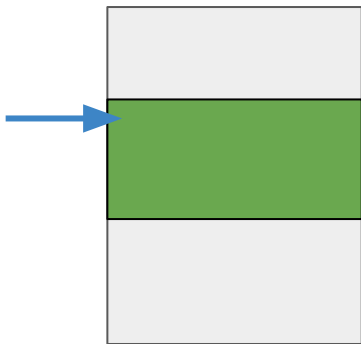
Packers Timeline



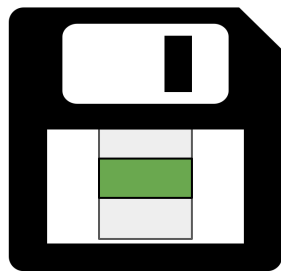


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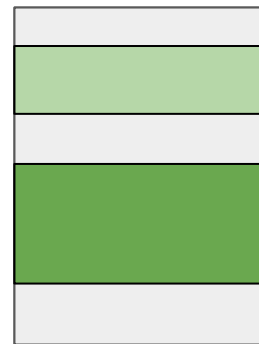
Unpacking Approach



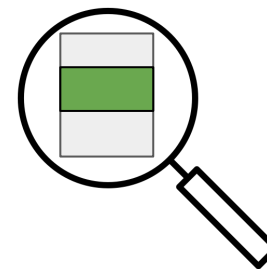
**Detect W and
X memory
regions**



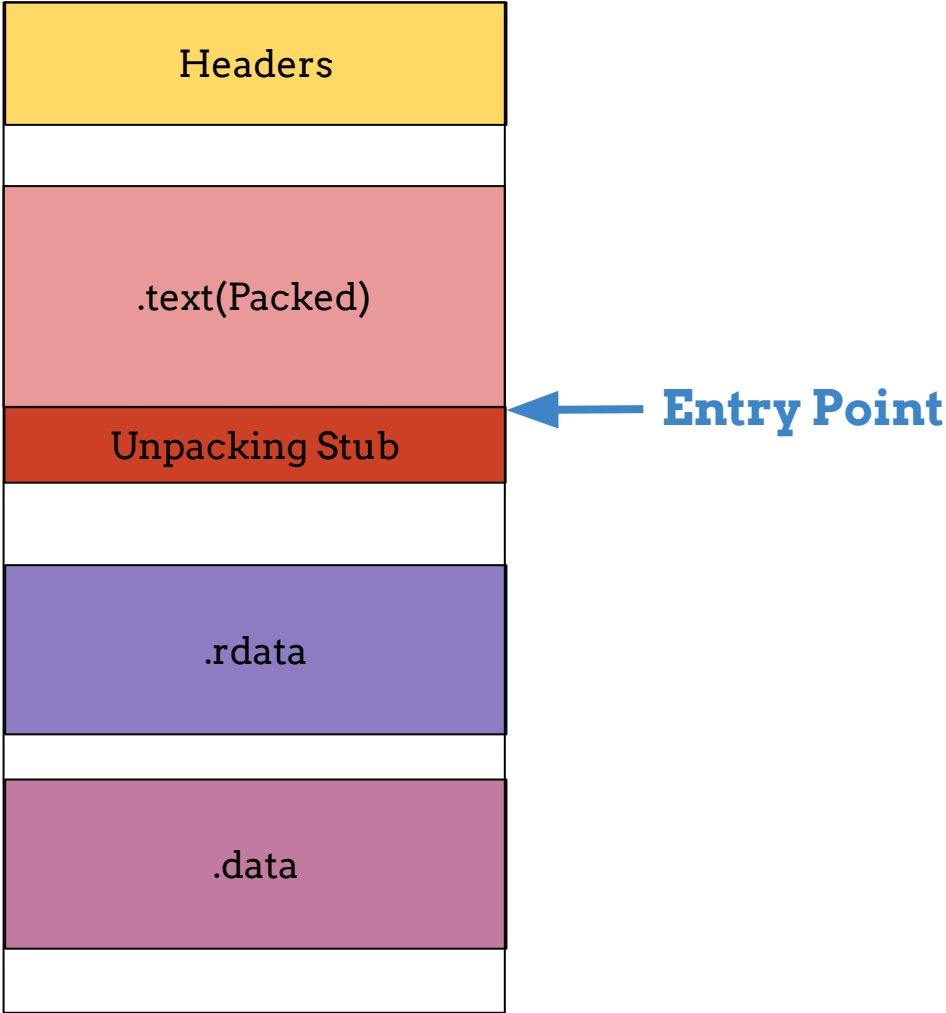
**Dump
the
Program**



**Deobfuscate
the Import
Address
Table**

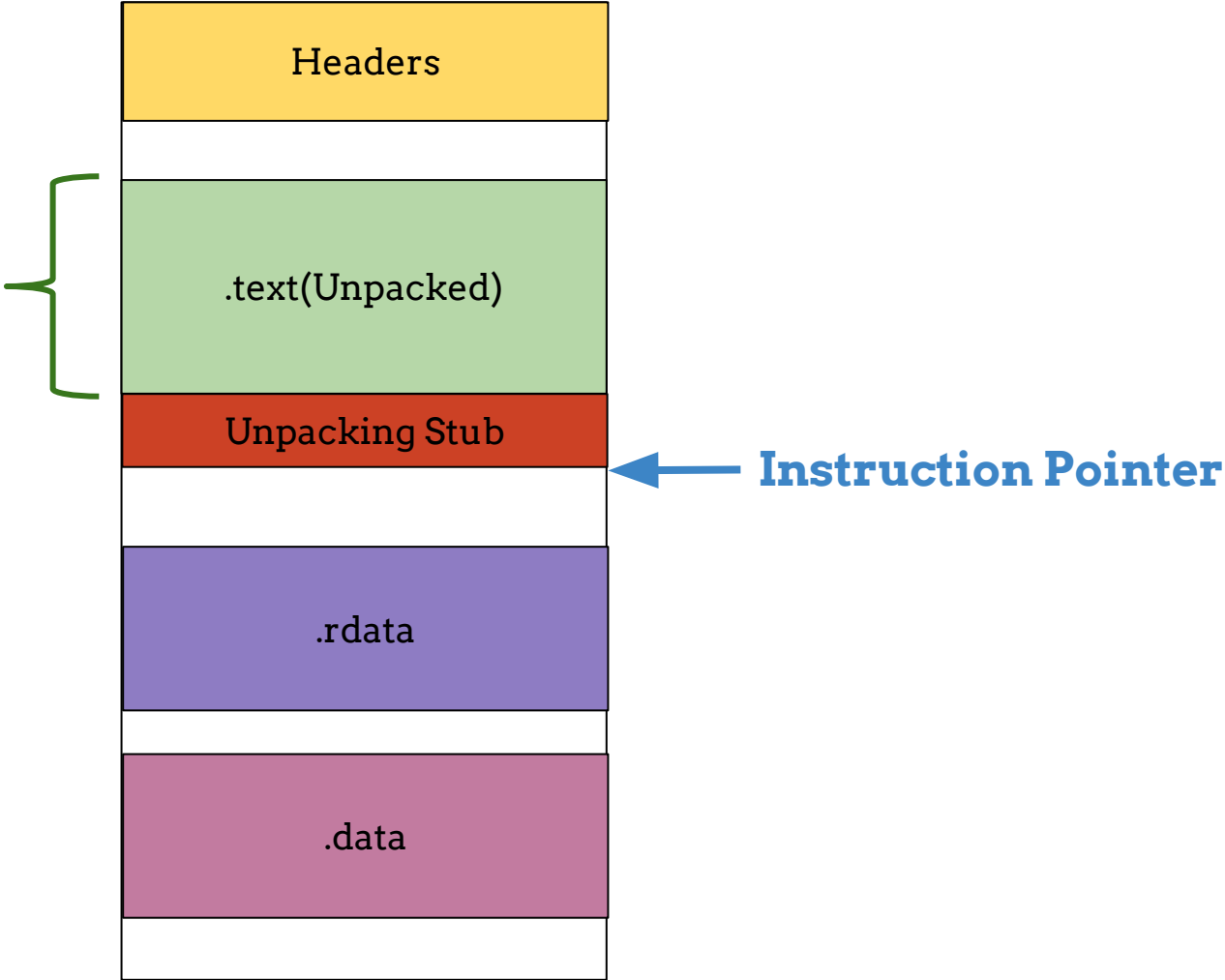


**Recognize
the correct
dump**



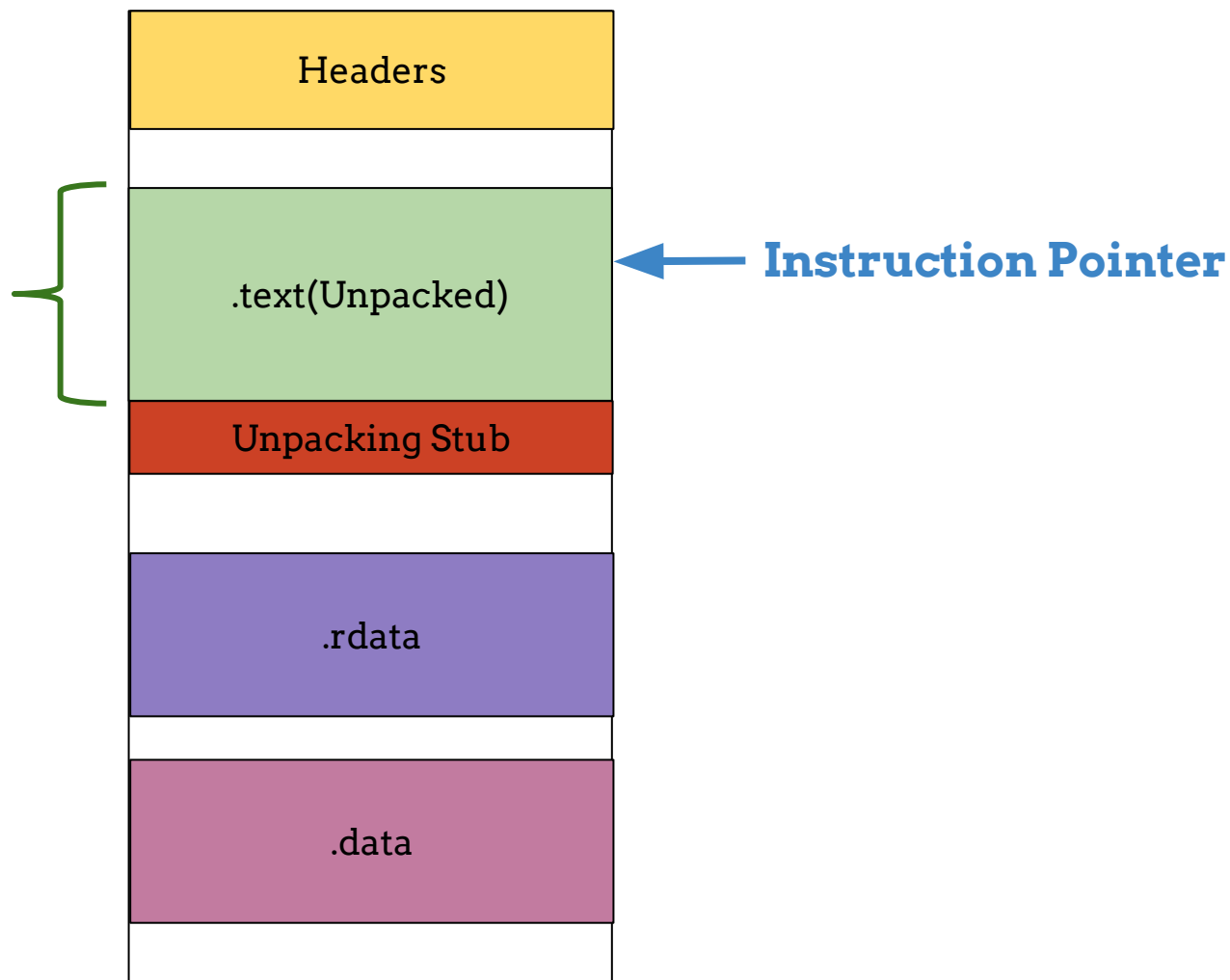


Written Data





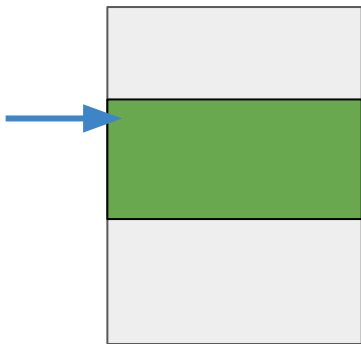
Written Data



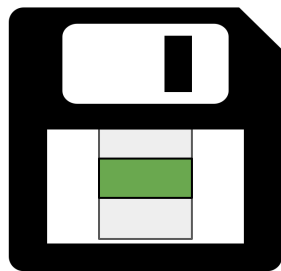


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Unpacking Approach



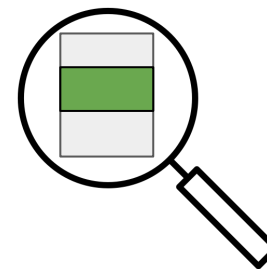
**Detect W and
X memory
regions**



**Dump
the
Program**



**Deobfuscate
the Import
Address
Table**



**Recognize
the correct
dump**



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Experiment 1: known packers

	Upx	FSG	Mew	mpress	PeCompact	Obsidium	ExePacker	ezip
MessageBox.exe	✓	✓	✓	✓	✓	!	✓	✓
WinRAR.exe	✓	✓	✓	✓	✓	!	✓	✓

	Xcomp	PElock	ASProtect	ASPack	eXpressor	exe32packer	beropacker	Hyperion
MessageBox.exe	✓	!	!	✓	!	✓	✓	✓
WinRAR.exe	✓	!	!	✓	!	✓	✓	✓



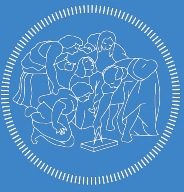
Original code dumped but Import directory not reconstructed



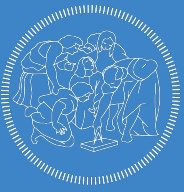
Experiment 2: wild samples

Number of packed (checked manually) samples **1096**

	N°	%
Unpacked and working	669	63
Unpacked but not executable	139	13
Not unpacked	258	24



- **String Obfuscation**
- **IAT Obfuscation**
- **Dynamic Loading**
- **etc.**



Malware Evasive

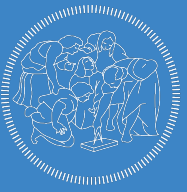
```
If (amIUnderAnalysis ())  
{  
    die ();  
}  
else  
{  
    beMalicious ();  
}
```



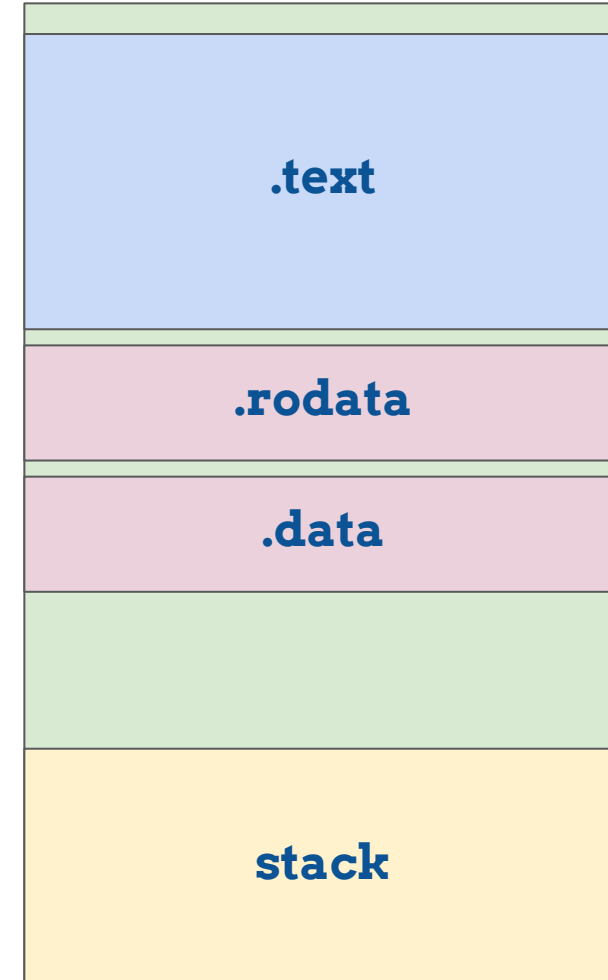
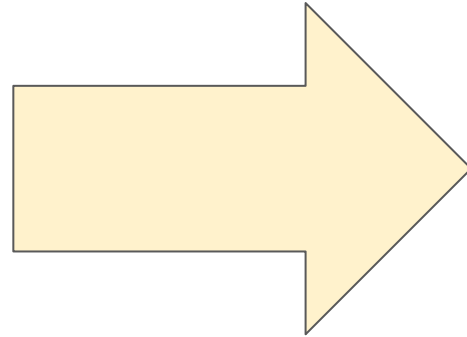
Artifacts

```
If (amIUnderAnalysis ())  
{  
    die ();  
}  
else  
{  
    beMalicious () ;  
}
```

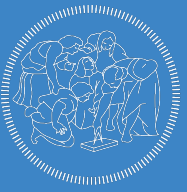
Dynamic Binary Instrumentation



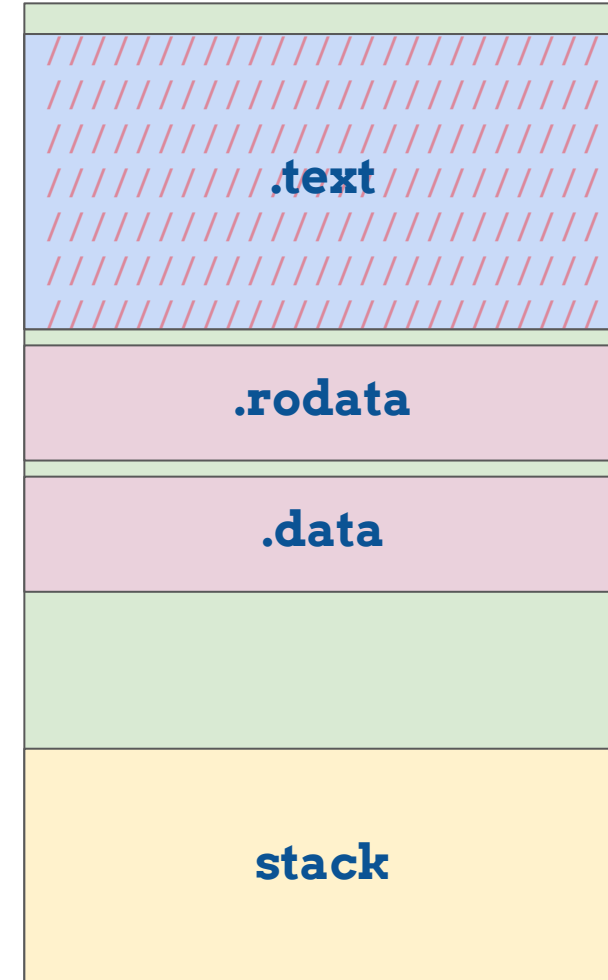
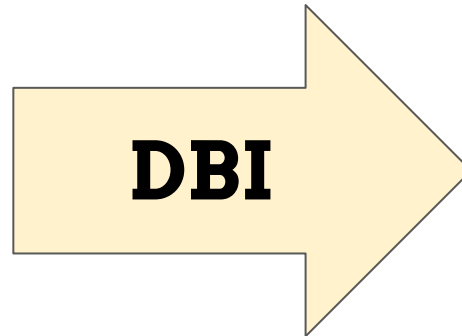
What is a DBI Tool?



Memory



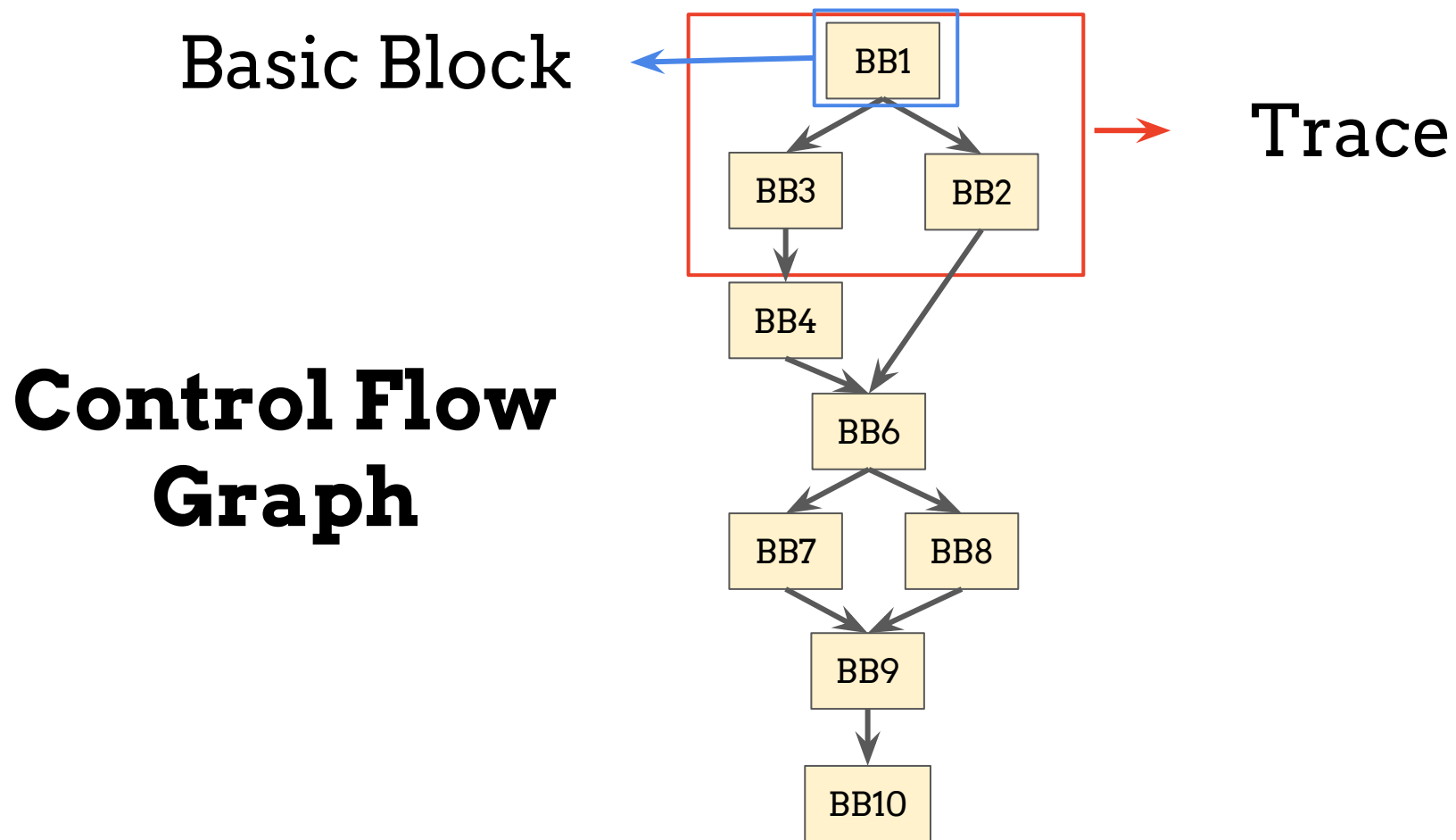
What is a DBI Tool?

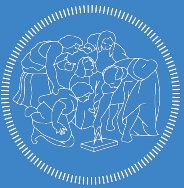


Memory

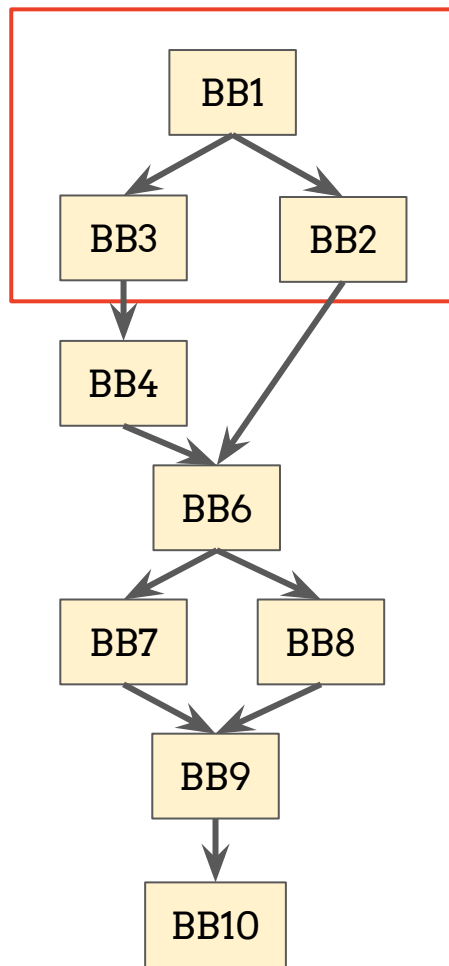


What is a DBI Tool?

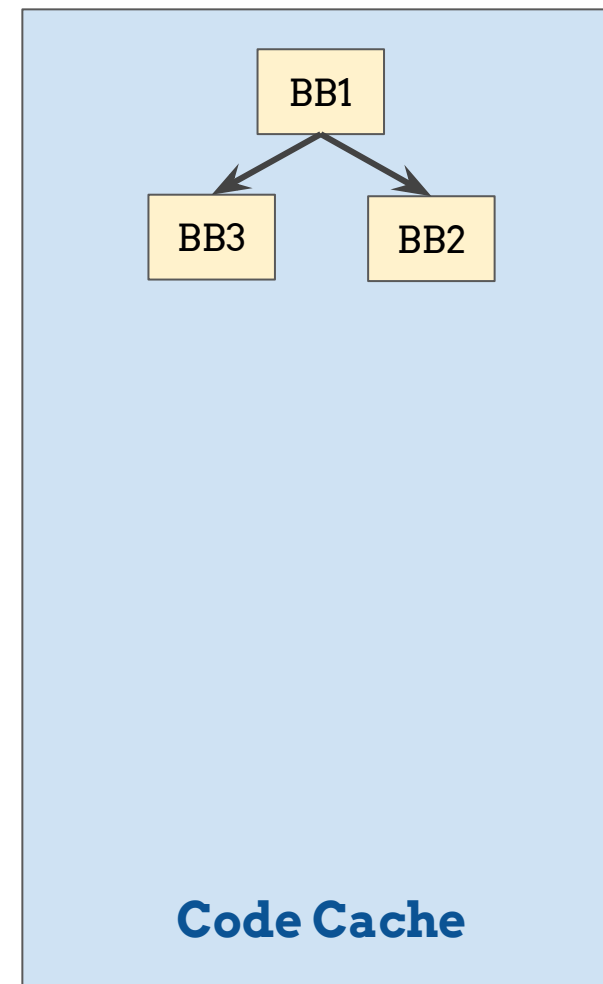




What is a DBI Tool?

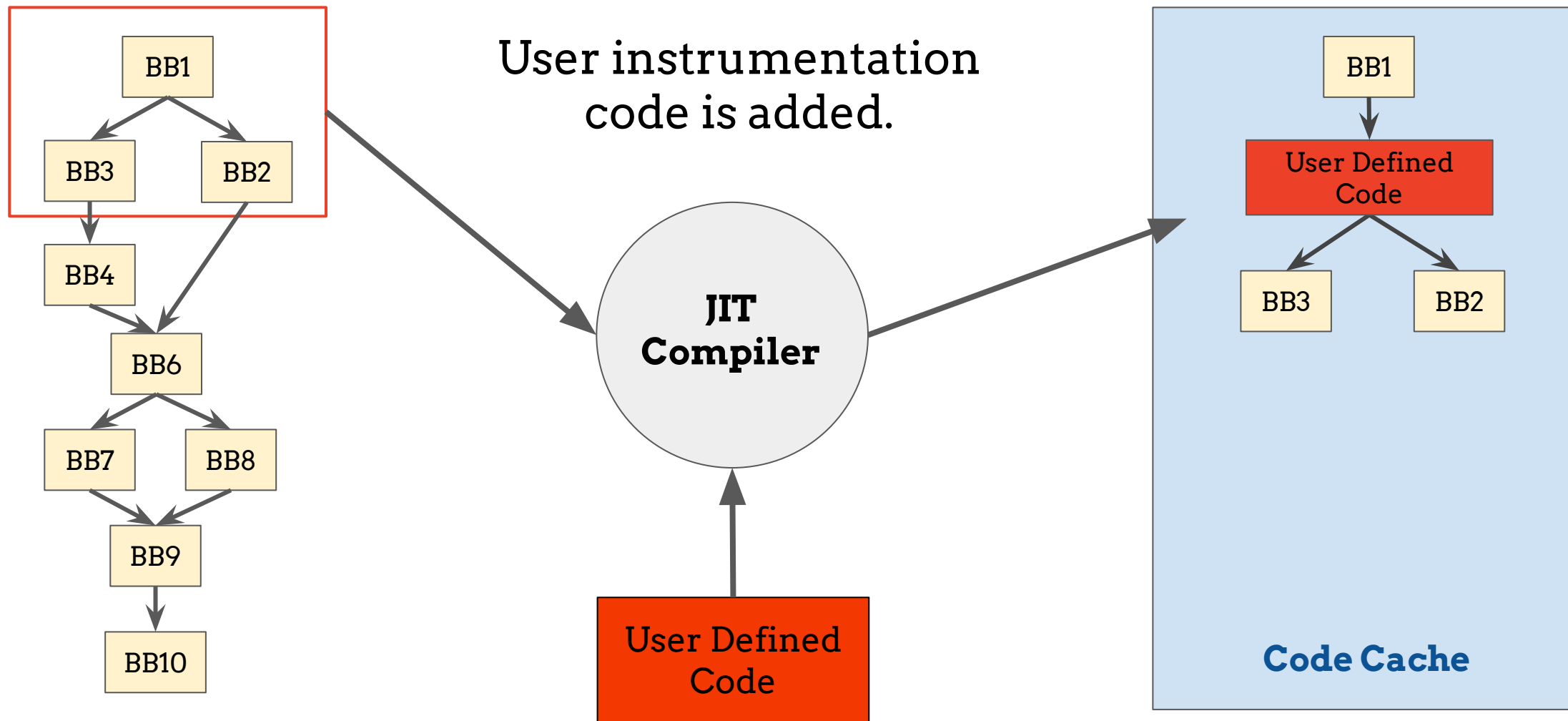


Trace is
copied in the
code cache





What is a DBI Tool?





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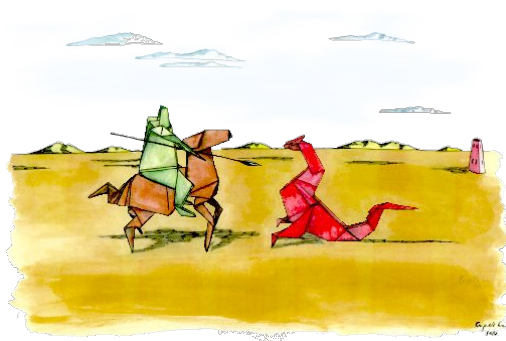
DBI - Evasive Malware



Intel Pin Tools



DynamoRIO



Valgrind

rev.ng

rev.ng



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DBI - Evasive Malware



Intel Pin Tools



The DR. is in.

DynamoRIO



Valgrind

rev.ng

rev.ng

Artifacts

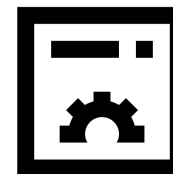


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DBI - Evasive Malware



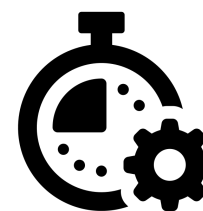
**Code Cache
Artifacts**



**JIT Compiler
Detection**



**Environment
Artifact**



**Overhead
Detection**



Arancino

Fake Memory Handler Modules

Fake Read Handler
Module

Fake Write Handler
Module

Fake Free Handler
Module

**Pattern Matching
Module**

**Self Modifying Code
Module**

Process Information Module

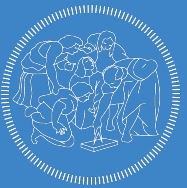
Hooking Module

Hooking Function Module

Hooking Syscall Module

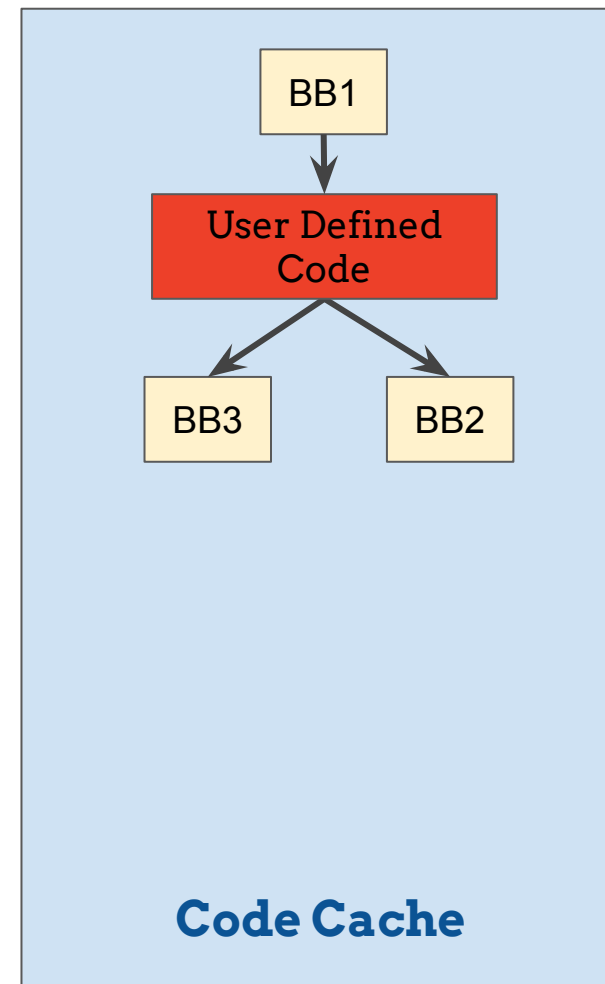


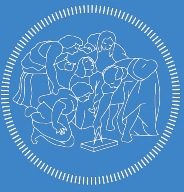
Code Cache Artifacts



All those artifacts caused by having a Code Cache

- **IP Detection**
- **Self-Modifying Code**



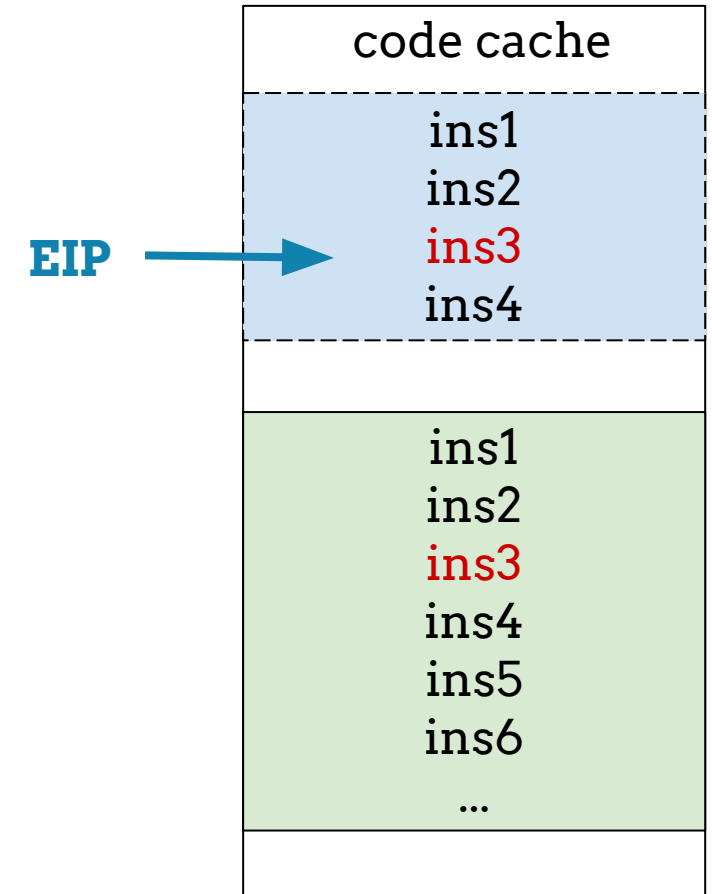


Nt Sycall (EIP -> EDX)

`int 2e`

Floating Point Context on the Stack

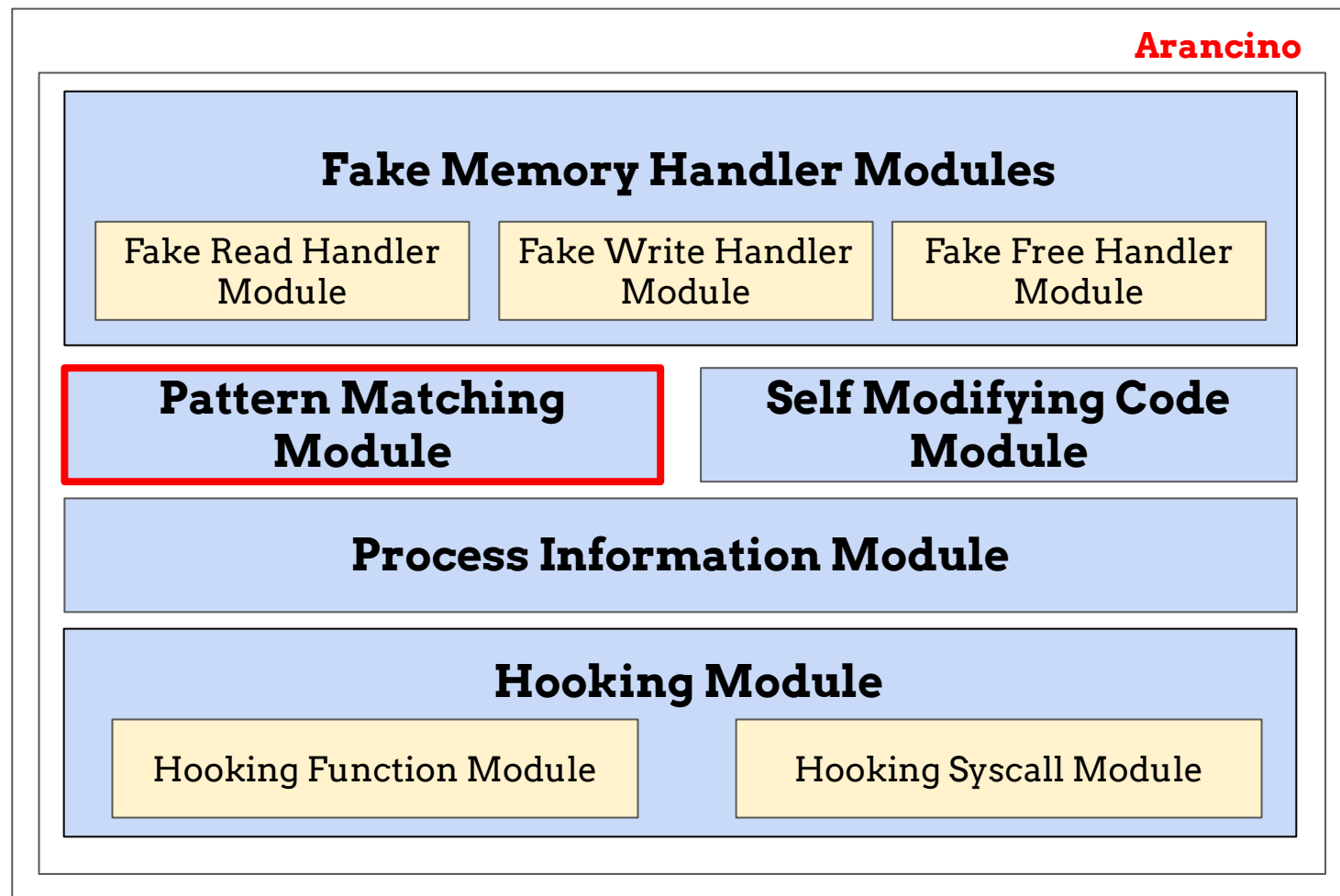
`fsave/ fxsave/ fstenv`





Arancino - Pattern Matching Module

- **PatchMap**: List of instructions and func pointers
- **PatchDispatcher**: check and add patch to instructions during trace building.



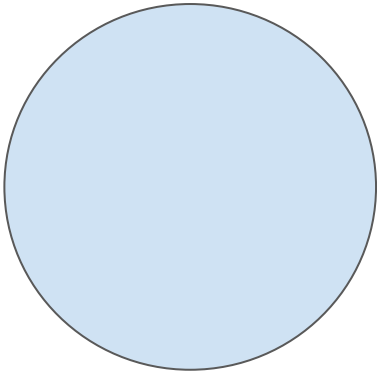


CCA - IP Detection

TRACE

```
add eax,4  
int 2e  
jmp 0x0804856c
```

PATCH DISPATCHER



int 2e
fsave
fxsave

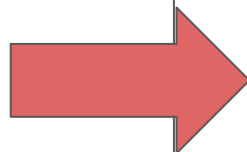
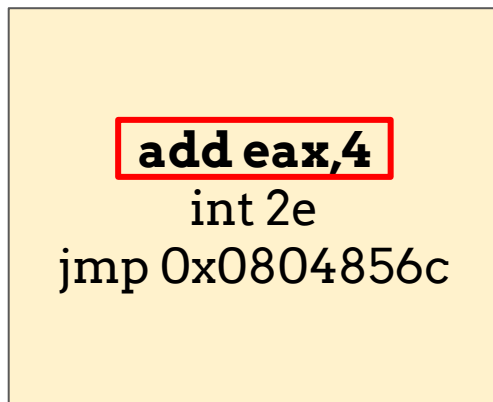
PATCHED TRACE



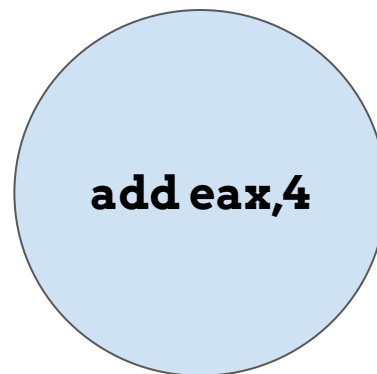


CCA - IP Detection

TRACE



PATCH DISPATCHER



int 2e

fsave

fxsave

PATCHED TRACE



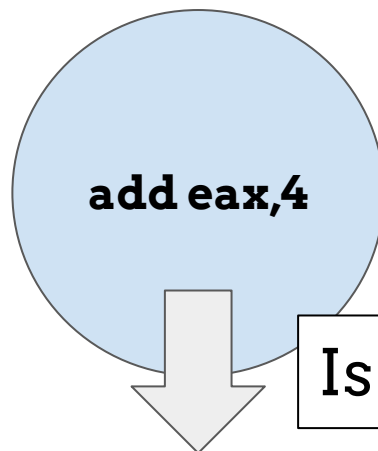


CCA - IP Detection

TRACE

```
add eax,4  
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jmp 0x0804856c
```

PATCH DISPATCHER



int 2e

fsave

fxsave

Is it in the list?

PATCHED TRACE



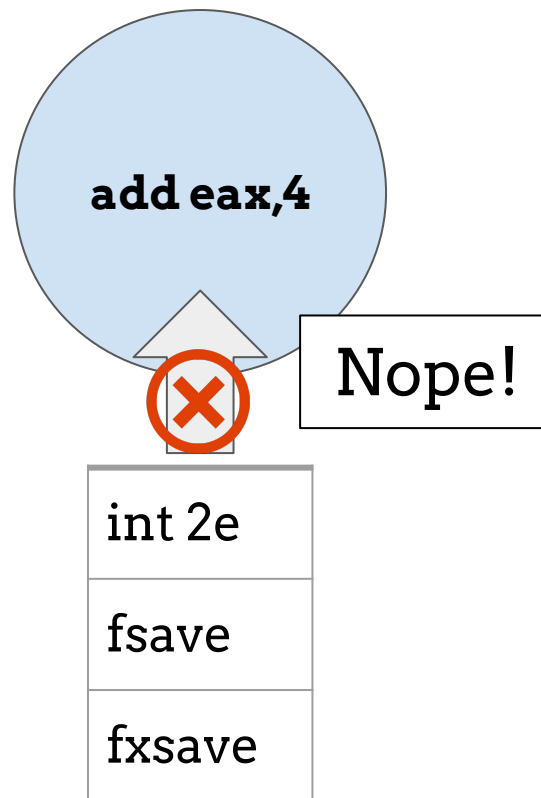


CCA - IP Detection

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PATCH DISPATCHER



PATCHED TRACE





CCA - IP Detection

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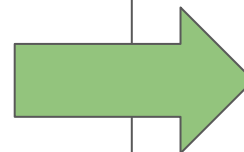
PATCH DISPATCHER

add eax,4

int 2e

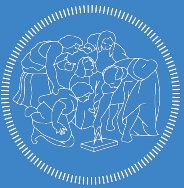
fsave

fxsave



PATCHED TRACE

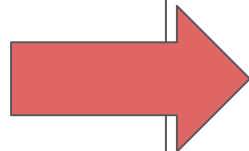
```
add eax,4
```

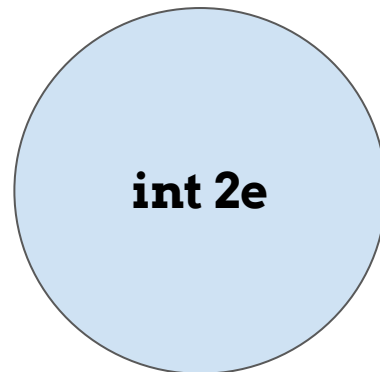
CCA - IP Detection

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jmp 0x0804856c
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PATCH DISPATCHER



int 2e

fsave

fxsave

PATCHED TRACE

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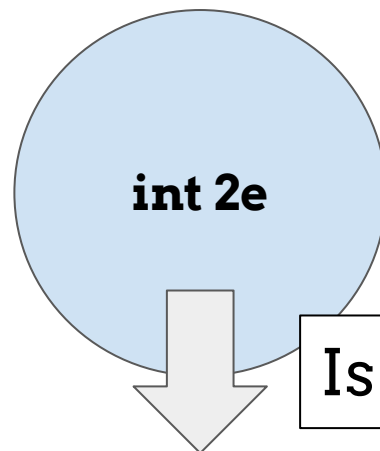


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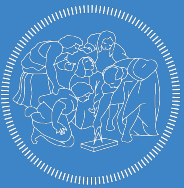
PATCH DISPATCHER



Is it in the list?

PATCHED TRACE

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add eax,4
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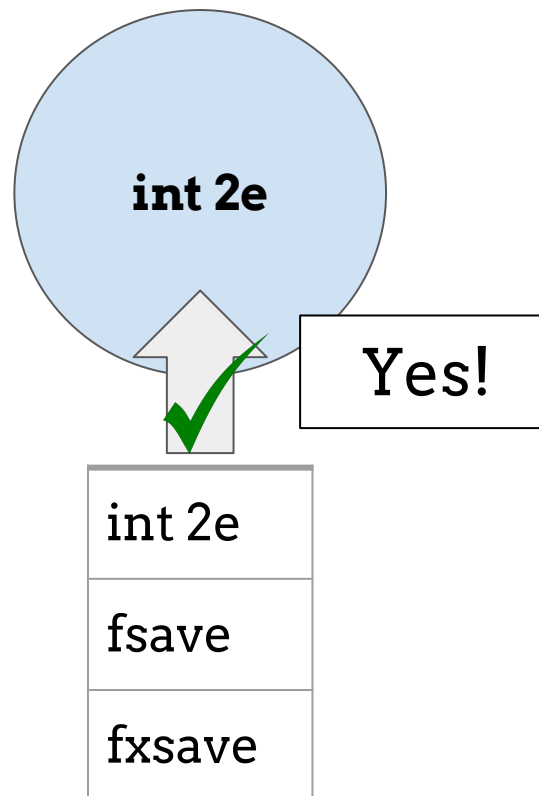


CCA - IP Detection

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PATCH DISPATCHER



PATCHED TRACE

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```



CCA - IP Detection

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PATCH DISPATCHER

int 2e

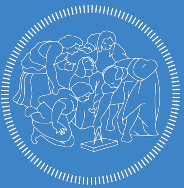
int 2e

fsave

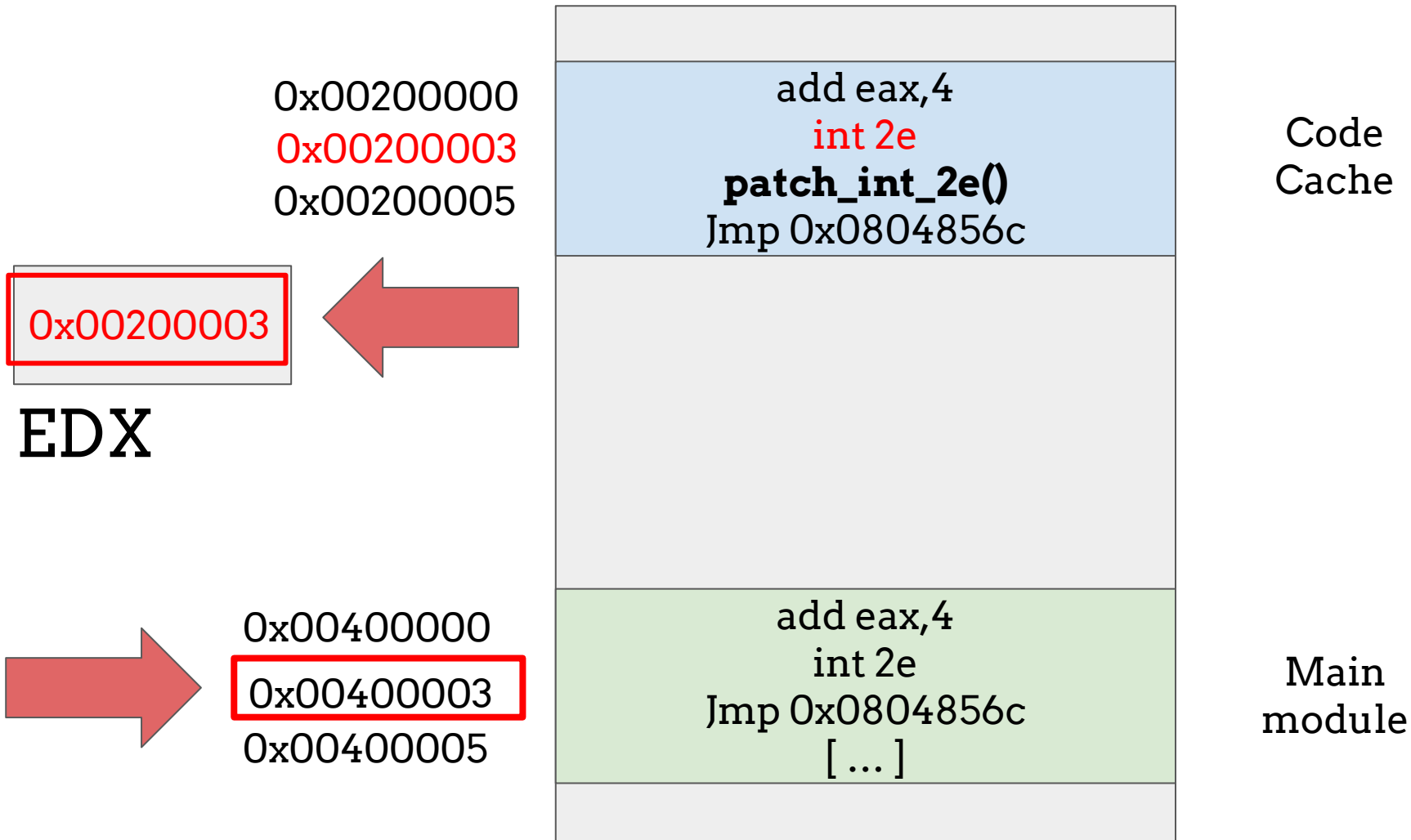
fxsave

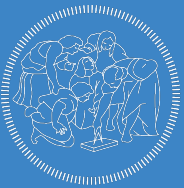
PATCHED TRACE

```
add eax,4  
int 2e  
patch_int2e()
```

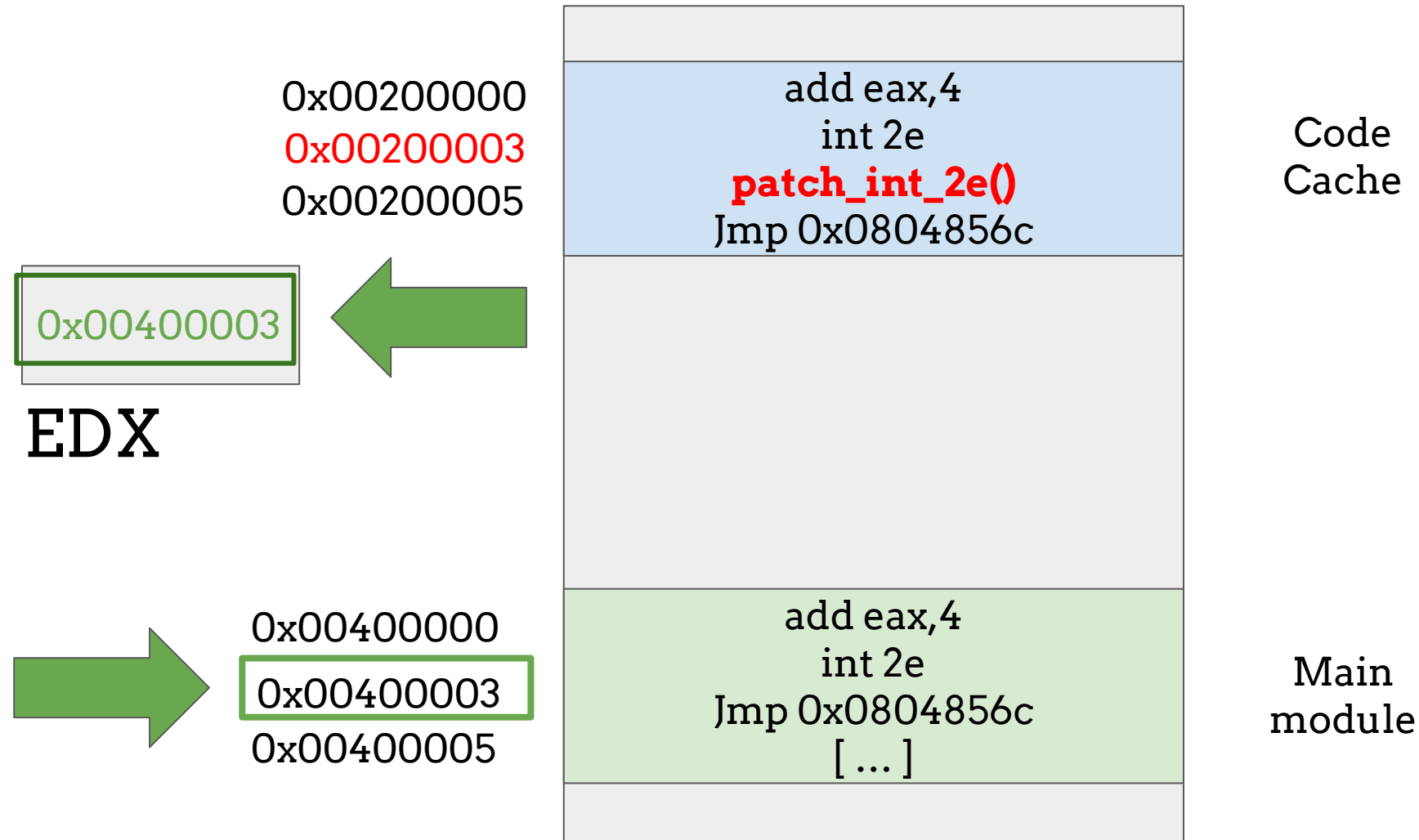


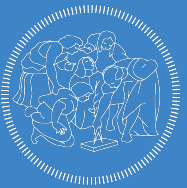
CCA - IP Detection RT





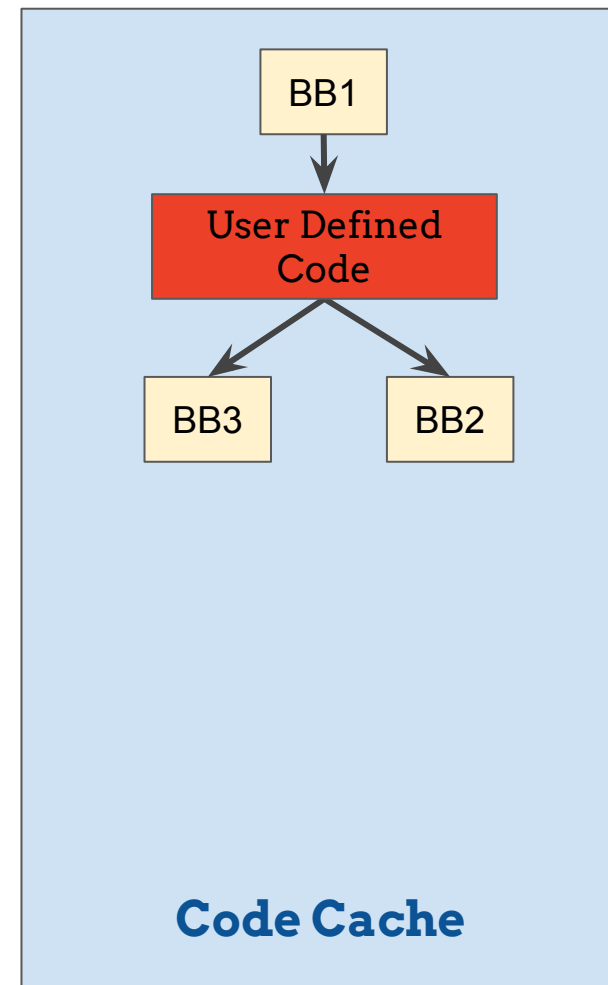
CCA - IP Detection RT





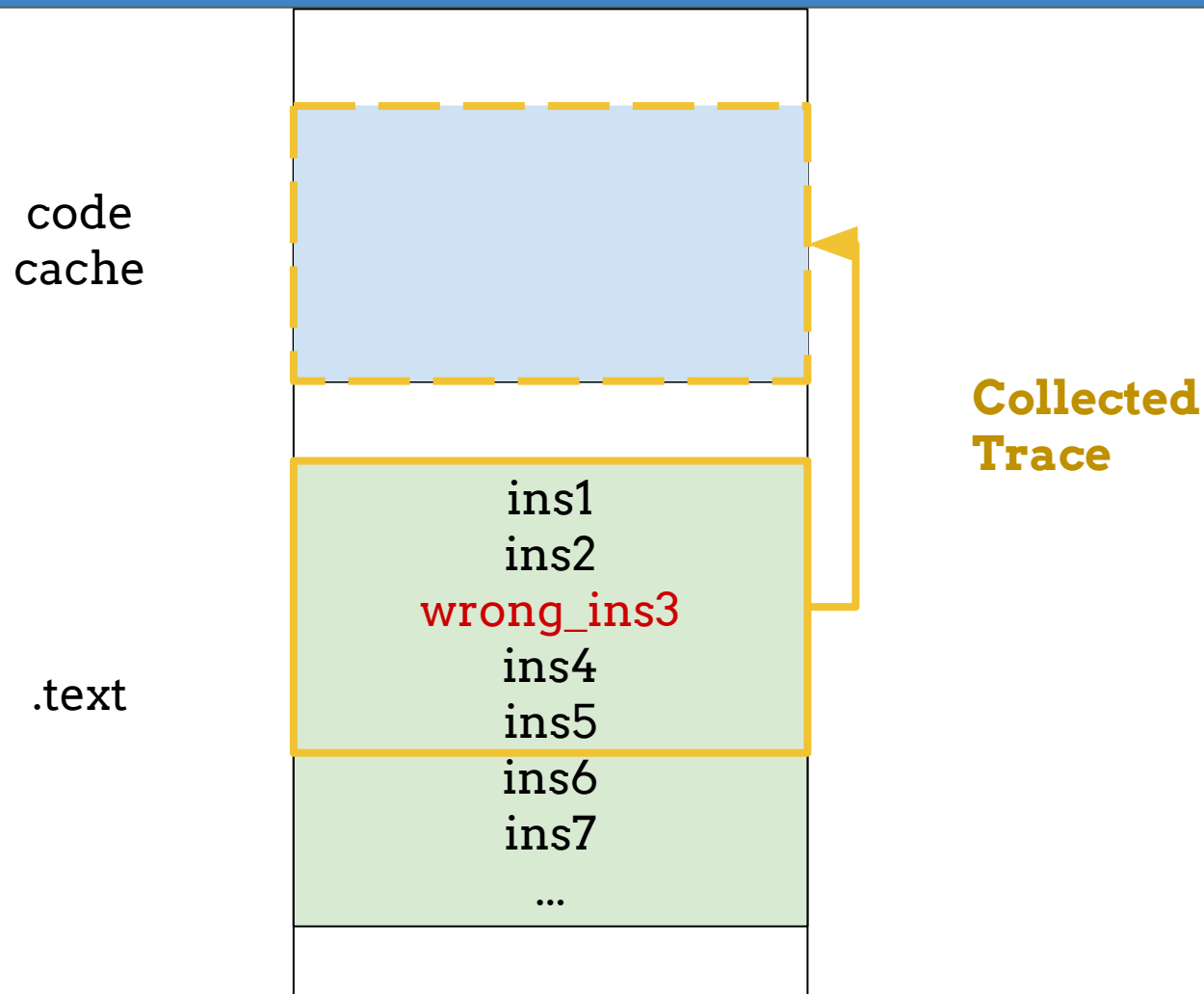
All those artifacts caused by having a Code Cache

- **IP Detection**
- **Self-Modifying Code**



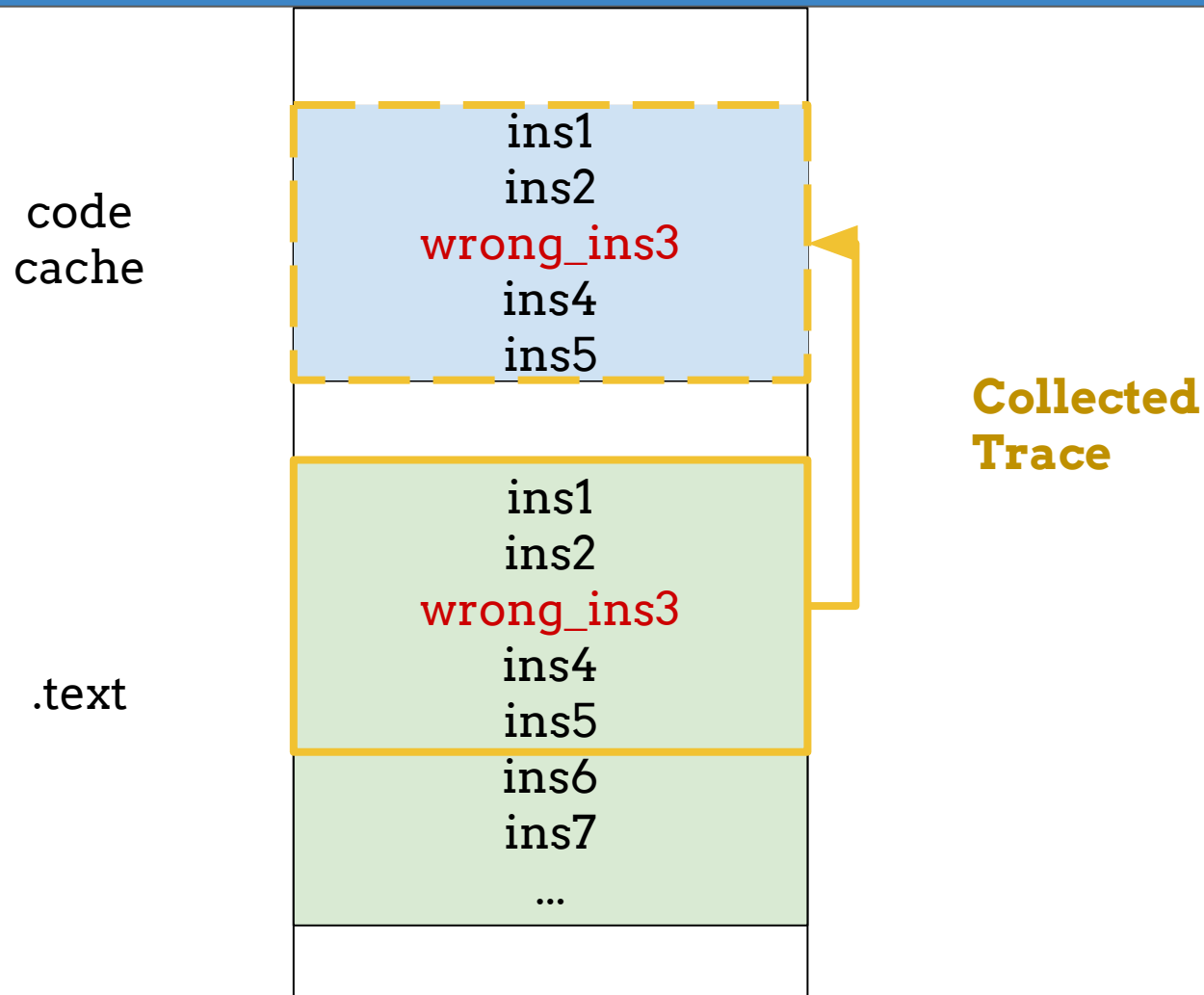


CCA - Self Modifying Code



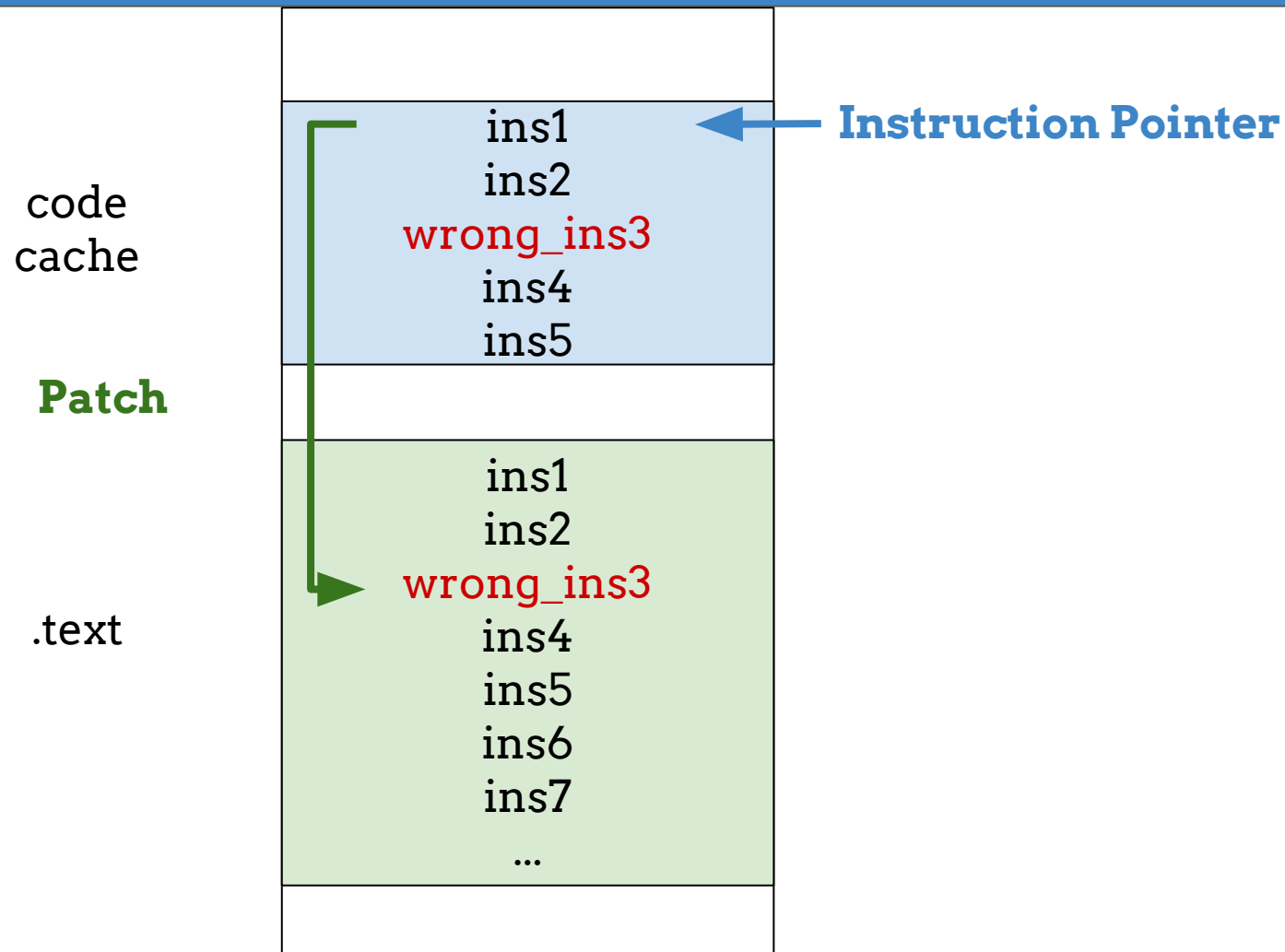


CCA - Self Modifying Code



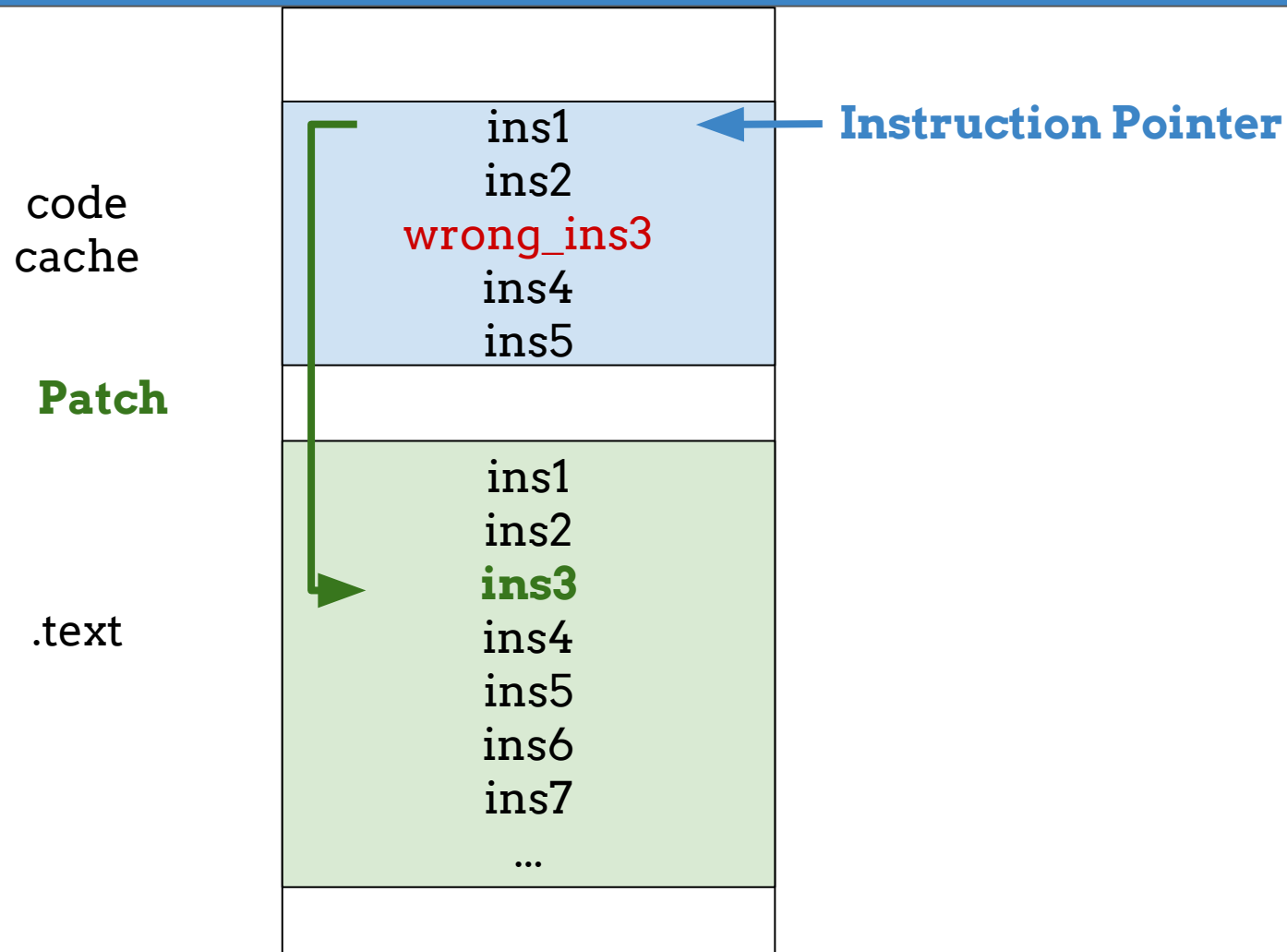


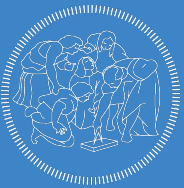
CCA - Self Modifying Code





CCA - Self Modifying Code





CCA - Self Modifying Code

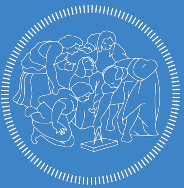
code
cache

ins1
ins2
wrong_ins3
ins4
ins5

← **Instruction Pointer**

.text

ins1
ins2
ins3
ins4
ins5
ins6
ins7
...



CCA - Self Modifying Code

code
cache

ins1
ins2
wrong_ins3
ins4
ins5

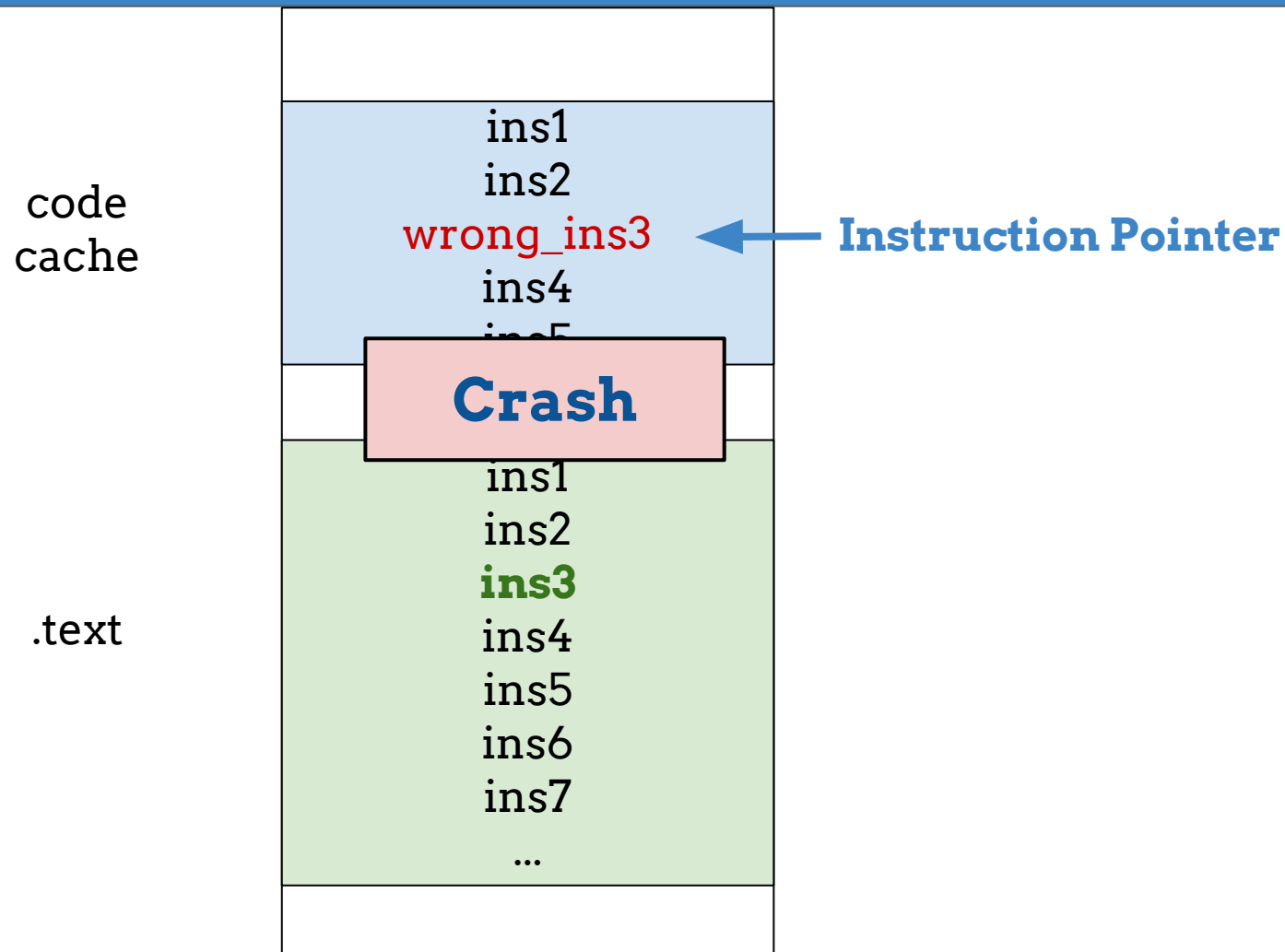
← **Instruction Pointer**

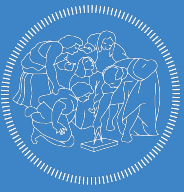
.text

ins1
ins2
ins3
ins4
ins5
ins6
ins7
...



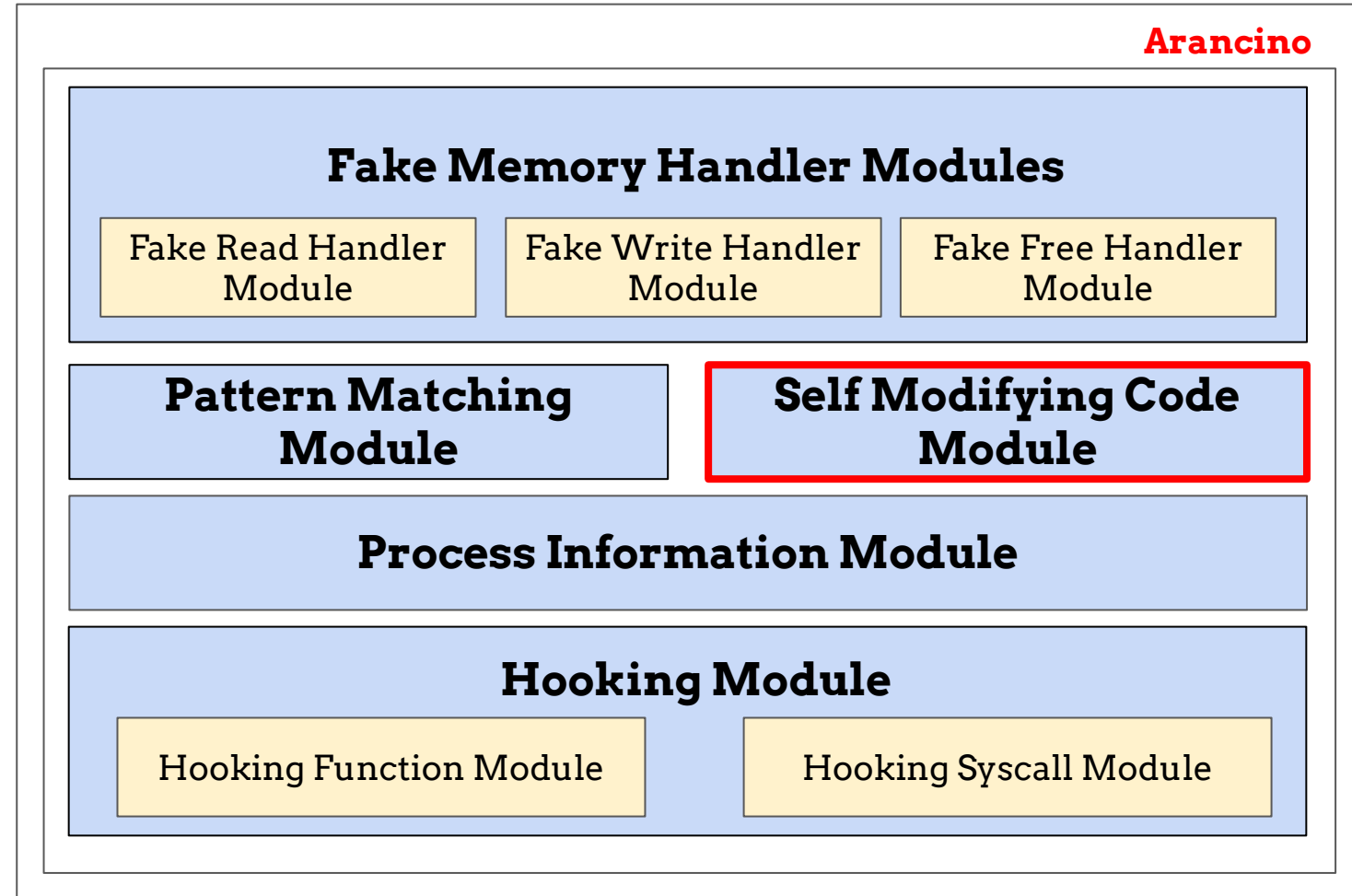
CCA - Self Modifying Code





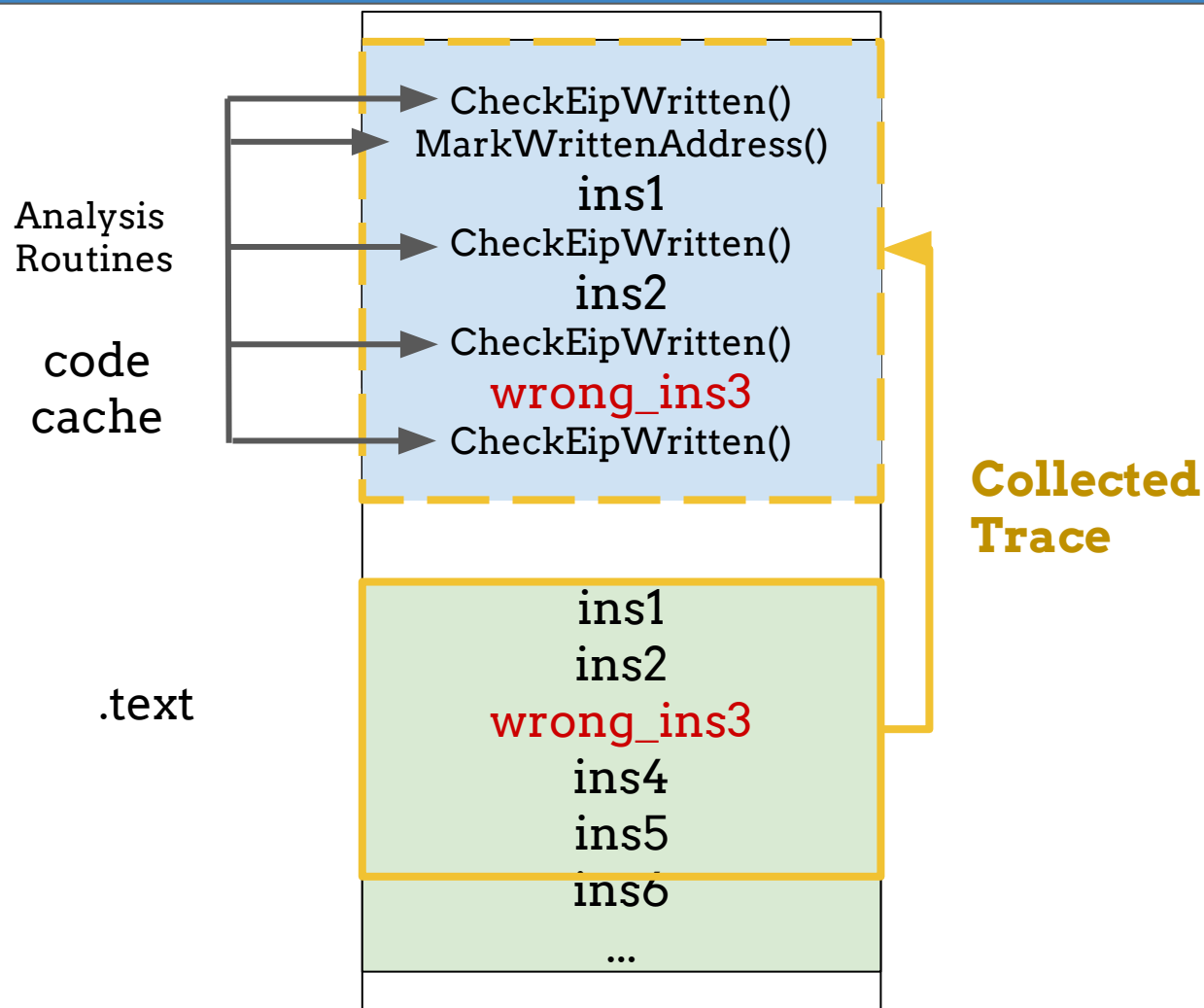
Arancino - Self Modifying Code Module

- **MarkWrittenAddress:**
store which address has been overwritten
- **CheckEIPWritten:**
check if next instruction has been overwritten.





CCA - Self Modifying Code





CCA - Self Modifying Code

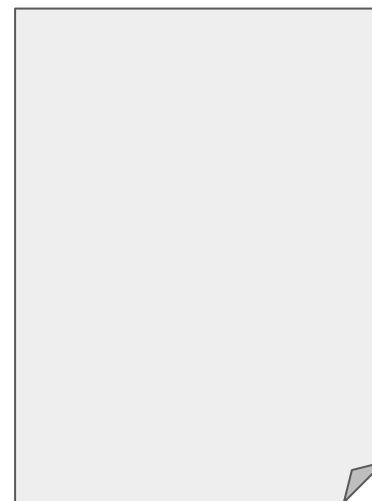
code
cache

```
CheckEipWritten()
MarkWrittenAddress()
ins1
CheckEipWritten()
ins2
CheckEipWritten()
wrong_ins3
CheckEipWritten()
```

Instruction Pointer

.text

```
ins1
ins2
wrong_ins3
ins4
ins5
ins6
...
```





CCA - Self Modifying Code

code
cache

```
CheckEipWritten()
MarkWrittenAddress()
  ins1
CheckEipWritten()
  ins2
CheckEipWritten()
  wrong_ins3
CheckEipWritten()
```

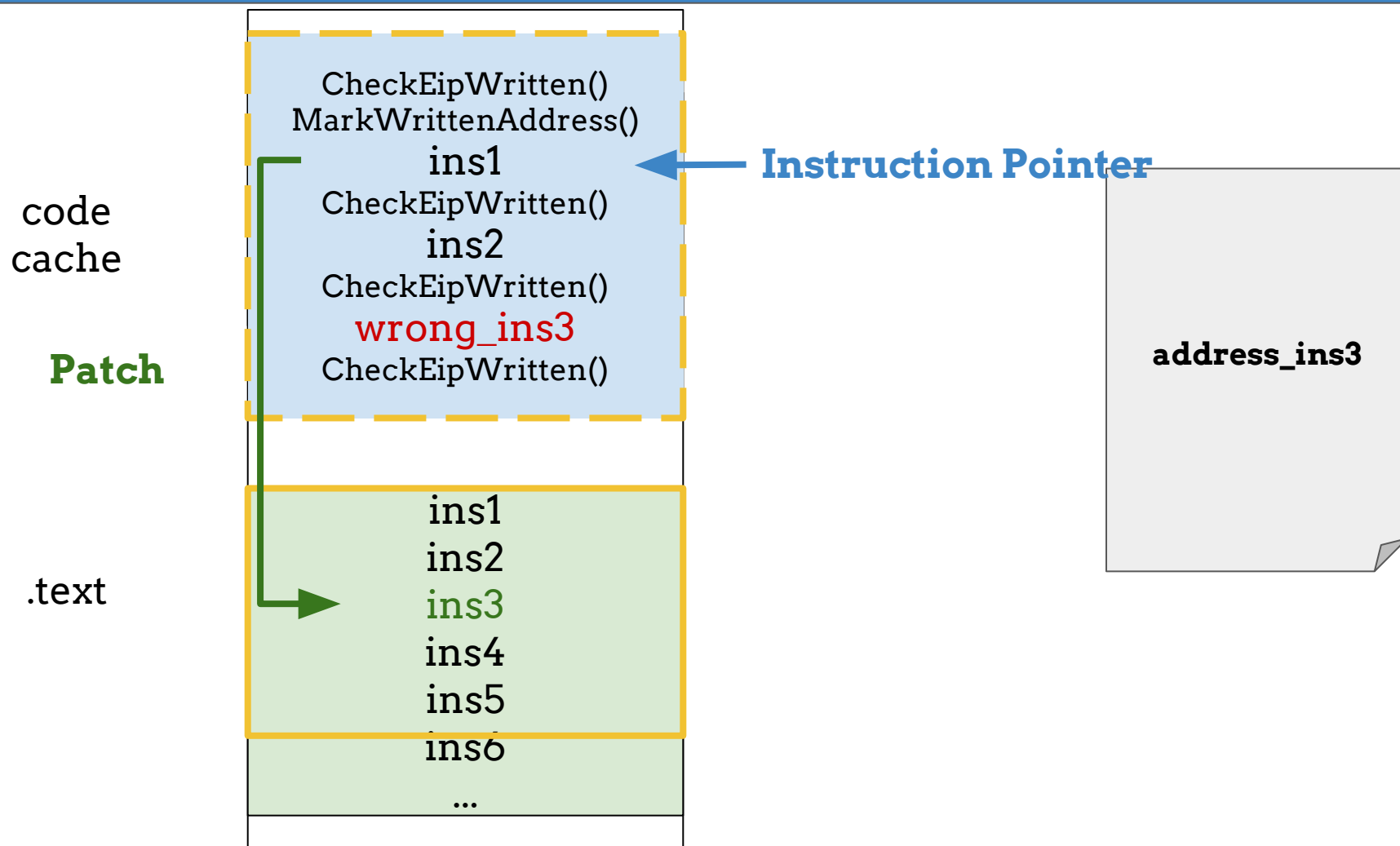
.text

```
ins1
ins2
wrong_ins3
ins4
ins5
ins6
...
```

address_ins3



CCA - Self Modifying Code





CCA - Self Modifying Code

code
cache

```
CheckEipWritten()
MarkWrittenAddress()
ins1
CheckEipWritten()
ins2
CheckEipWritten()
wrong_ins3
CheckEipWritten()
```

← Instruction Pointer

address_ins3

.text

```
ins1
ins2
ins3
ins4
ins5
ins6
...
```



CCA - Self Modifying Code

code
cache

```
CheckEipWritten()
MarkWrittenAddress()
  ins1
CheckEipWritten()
  ins2
CheckEipWritten()
  wrong_ins3
CheckEipWritten()
```

.text

```
ins1
ins2
ins3
ins4
ins5
ins6
...
```

address_ins3



CCA - Self Modifying Code

code
cache

```
CheckEipWritten()
MarkWrittenAddress()
ins1
CheckEipWritten()
ins2
CheckEipWritten()
wrong_ins3
CheckEipWritten()
```

Instruction Pointer

address_ins3

.text

```
ins1
ins2
ins3
ins4
ins5
ins6
...
```



CCA - Self Modifying Code

code
cache

```
CheckEipWritten()
MarkWrittenAddress()
  ins1
CheckEipWritten()
  ins2
CheckEipWritten()
  wrong_ins3
CheckEipWritten()
```

Instruction Pointer

address_ins3

.text

```
ins1
ins2
ins3
ins4
ins5
ins6
...
```



CCA - Self Modifying Code

code
cache

```
CheckEipWritten()
MarkWrittenAddress()
  ins1
CheckEipWritten()
  ins2
CheckEipWritten()
  wrong_ins3
CheckEipWritten()
```

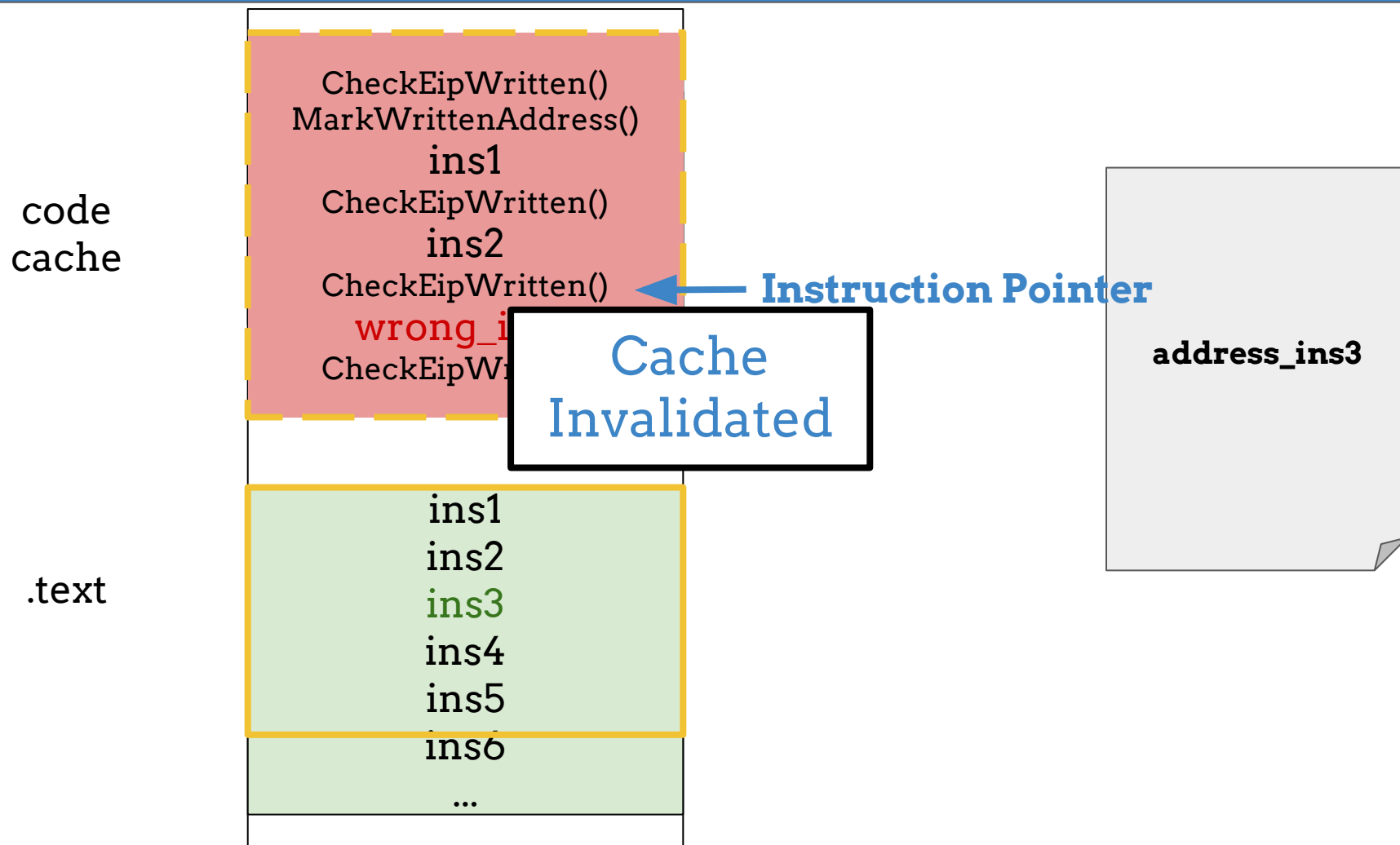
.text

```
ins1
ins2
ins3
ins4
ins5
ins6
...
```

address_ins3

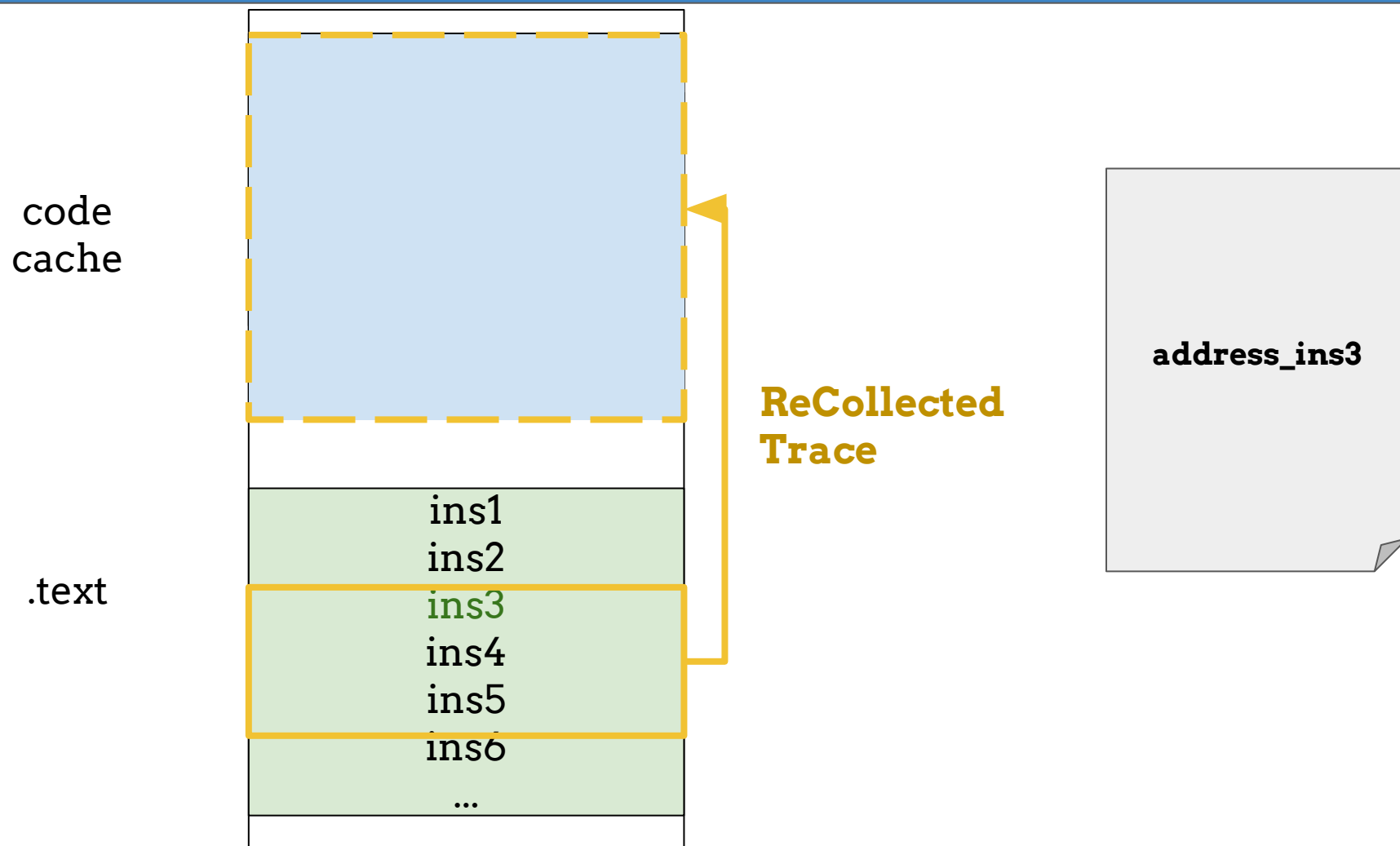


CCA - Self Modifying Code





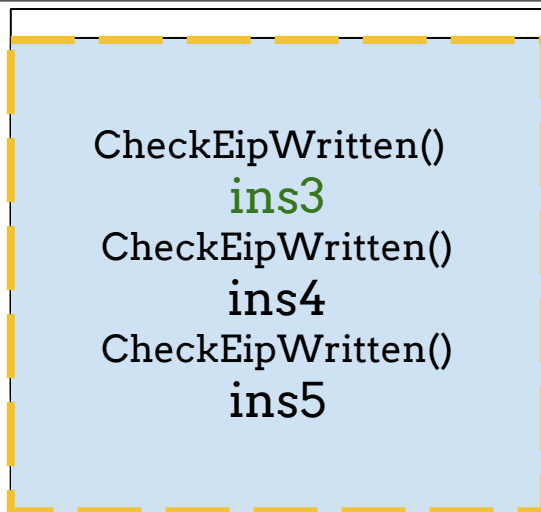
CCA - Self Modifying Code



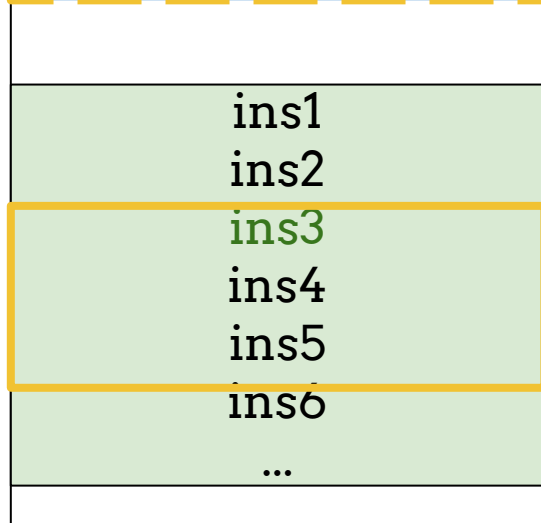


CCA - Self Modifying Code

code
cache

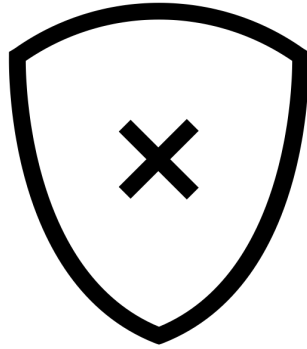


.text

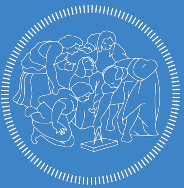


**ReCollected
Trace**





Environment Artifacts

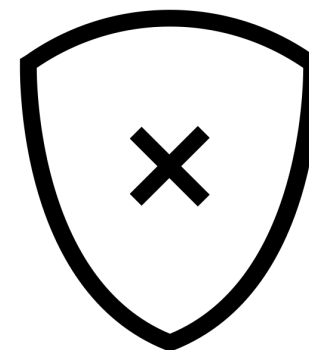


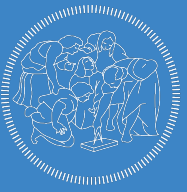
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MILANO 1863

Environment Artifacts

- **Parent Detection**

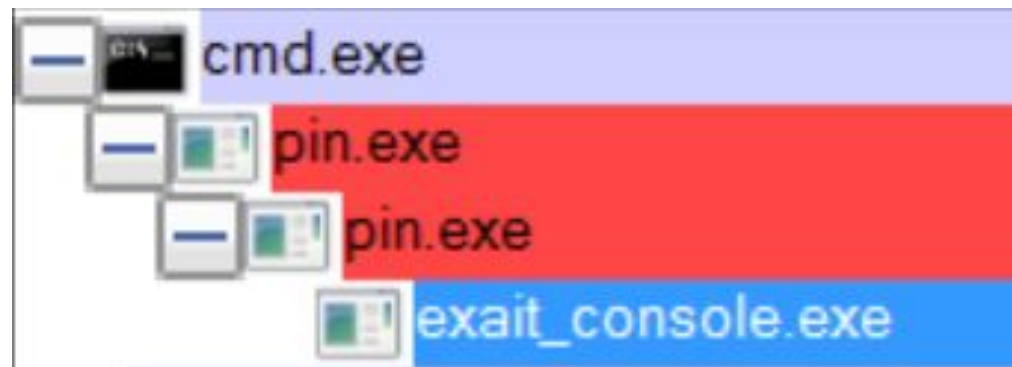
- **Memory Fingerprinting**

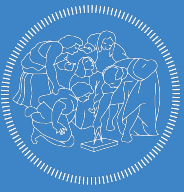




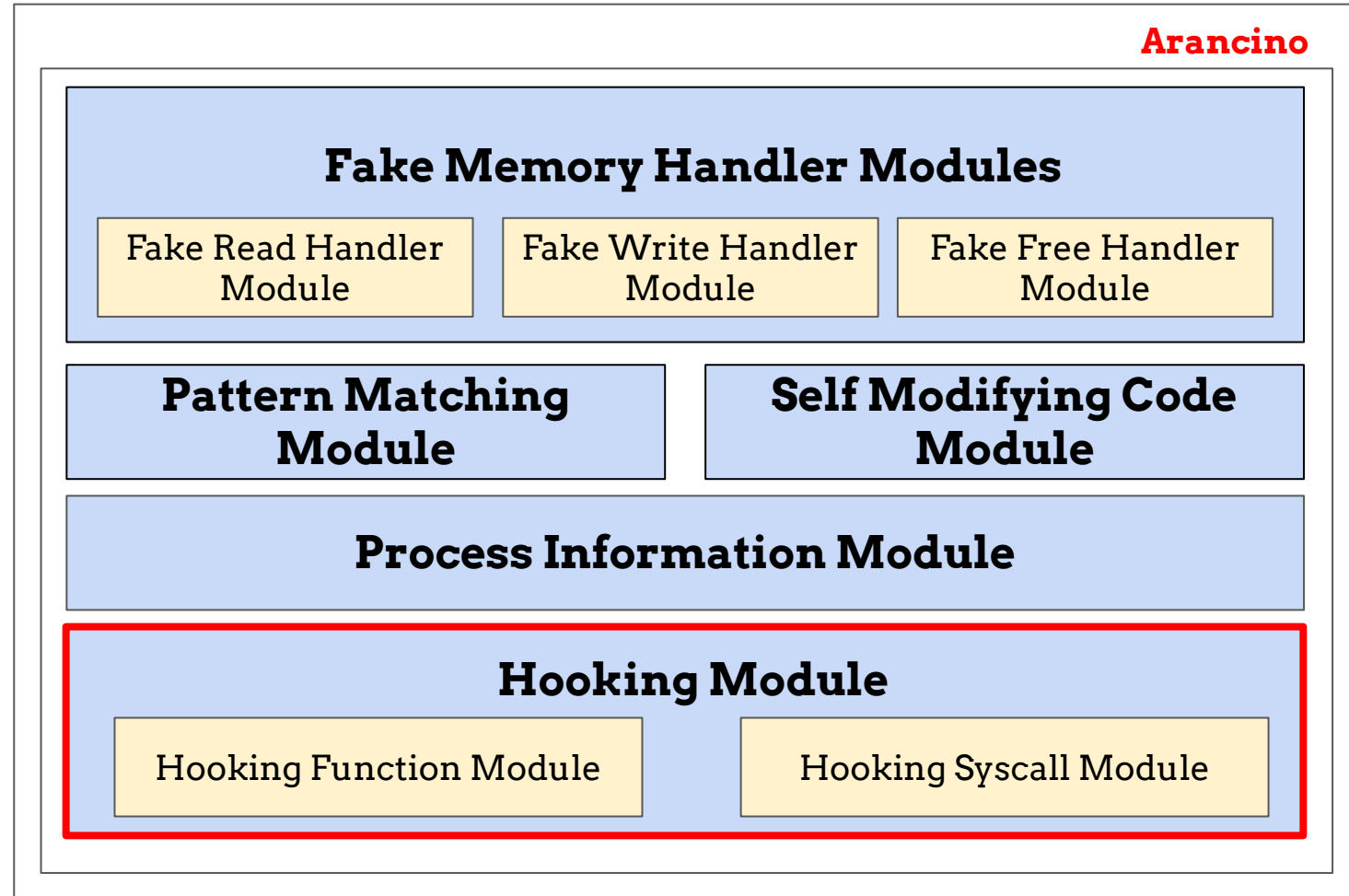
Malware can check which is the process father.

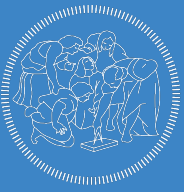
- NtQuerySystemInformation
- CSRSS.exe



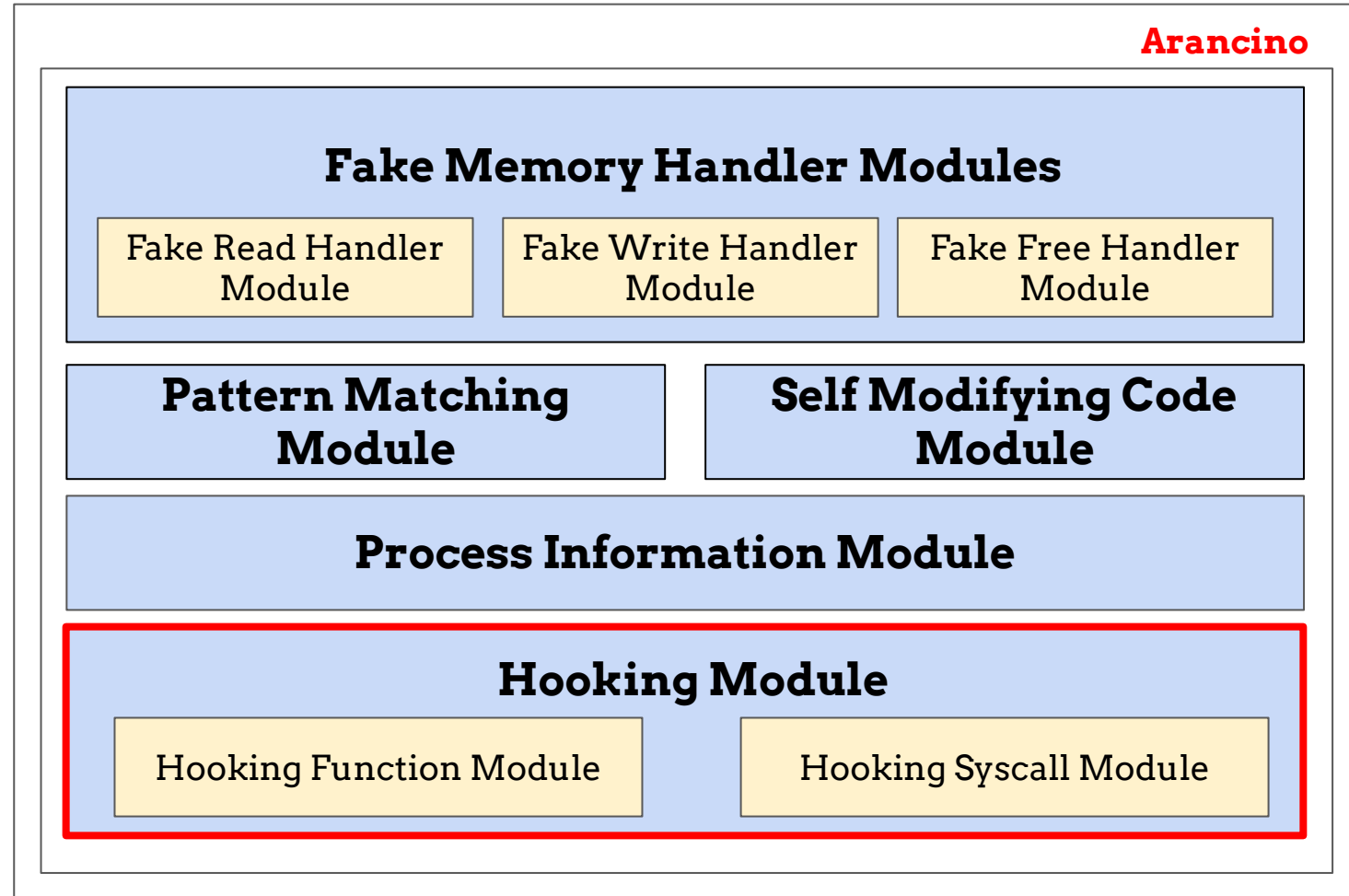


- **Hooking Function Module:** Install an Hook on dll's Functions
- **Hooking Syscall Module:** Install an Hook on dll's Functions





- **Hooking Function Module:** Install an Hook on dll's Functions
- **Hooking Syscall Module:** Install an Hook on dll's Functions





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Arancino - Hook Functions

ImageLoad



Memory



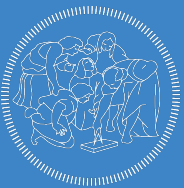
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Arancino - Hook Functions

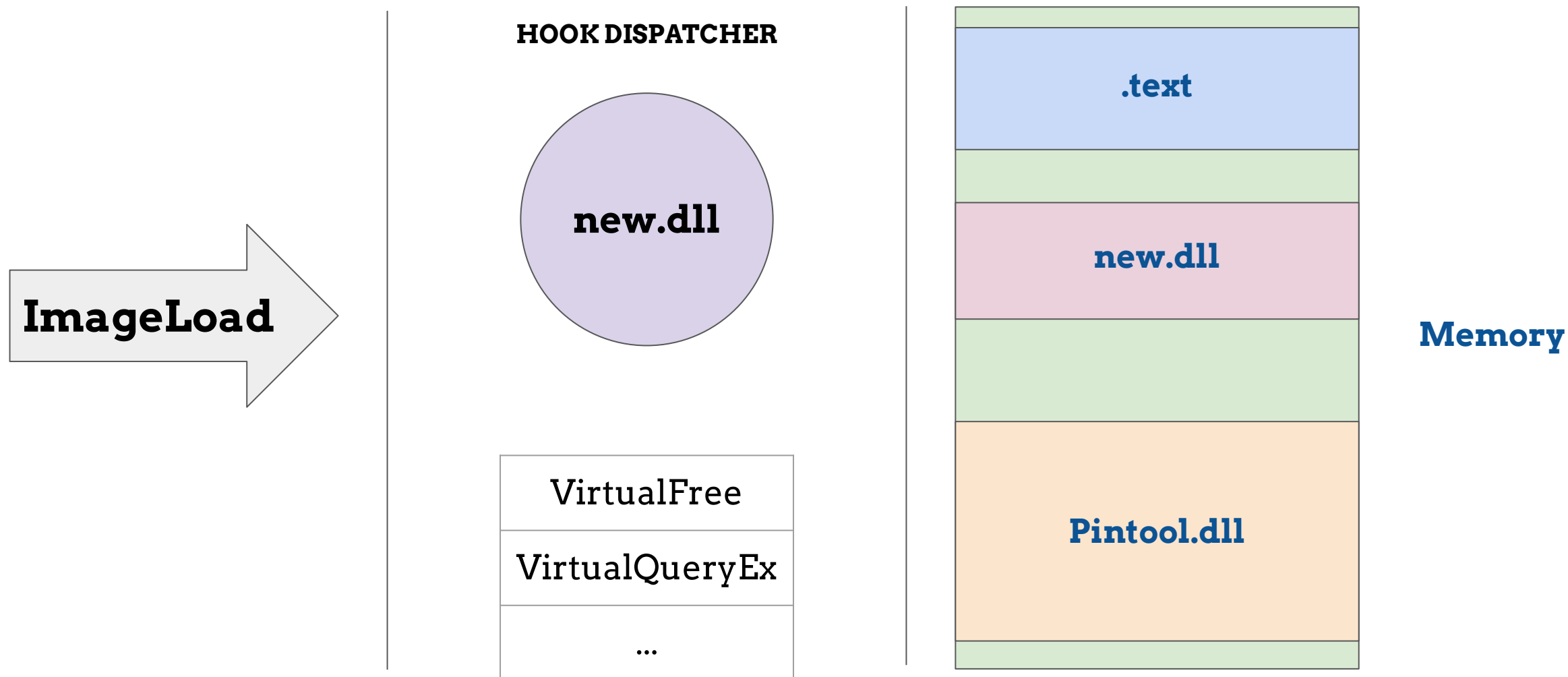
ImageLoad



Memory

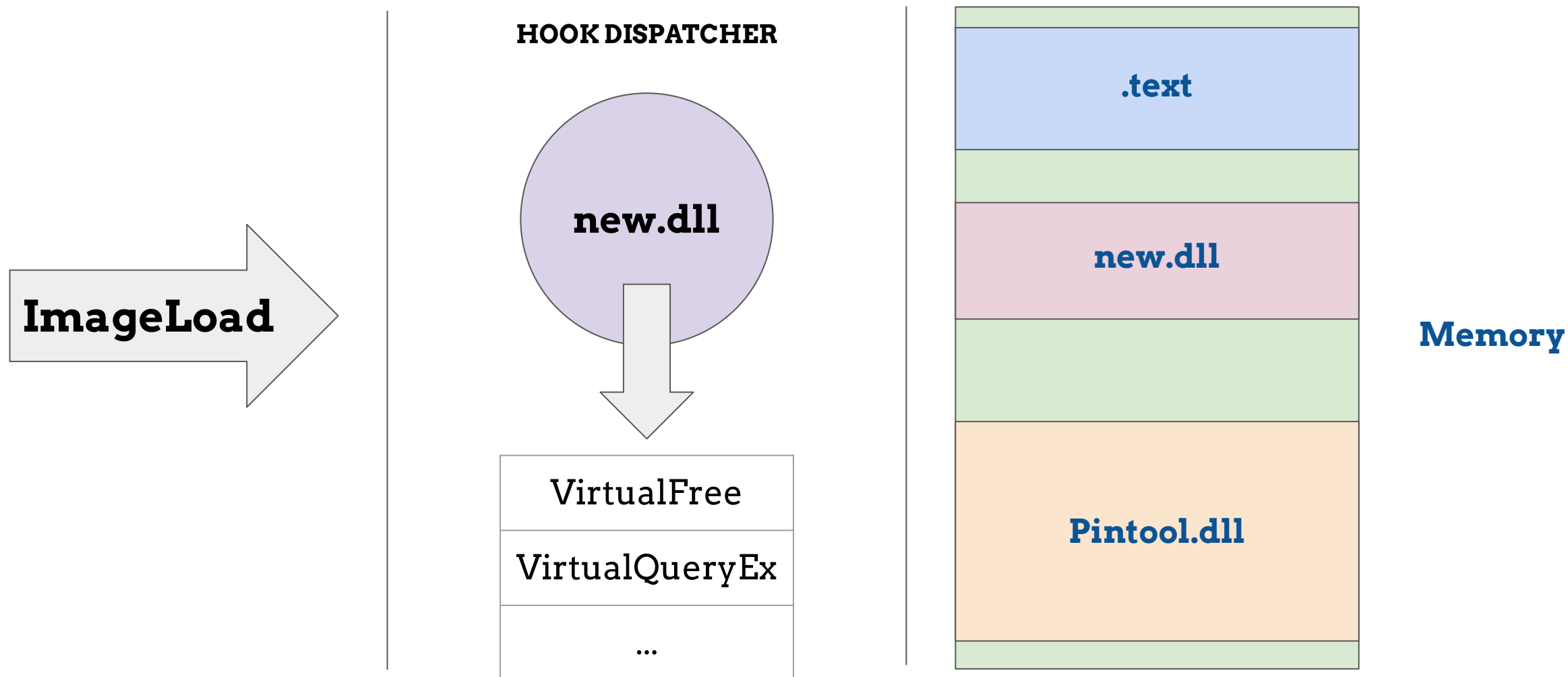


Arancino - Hook Functions



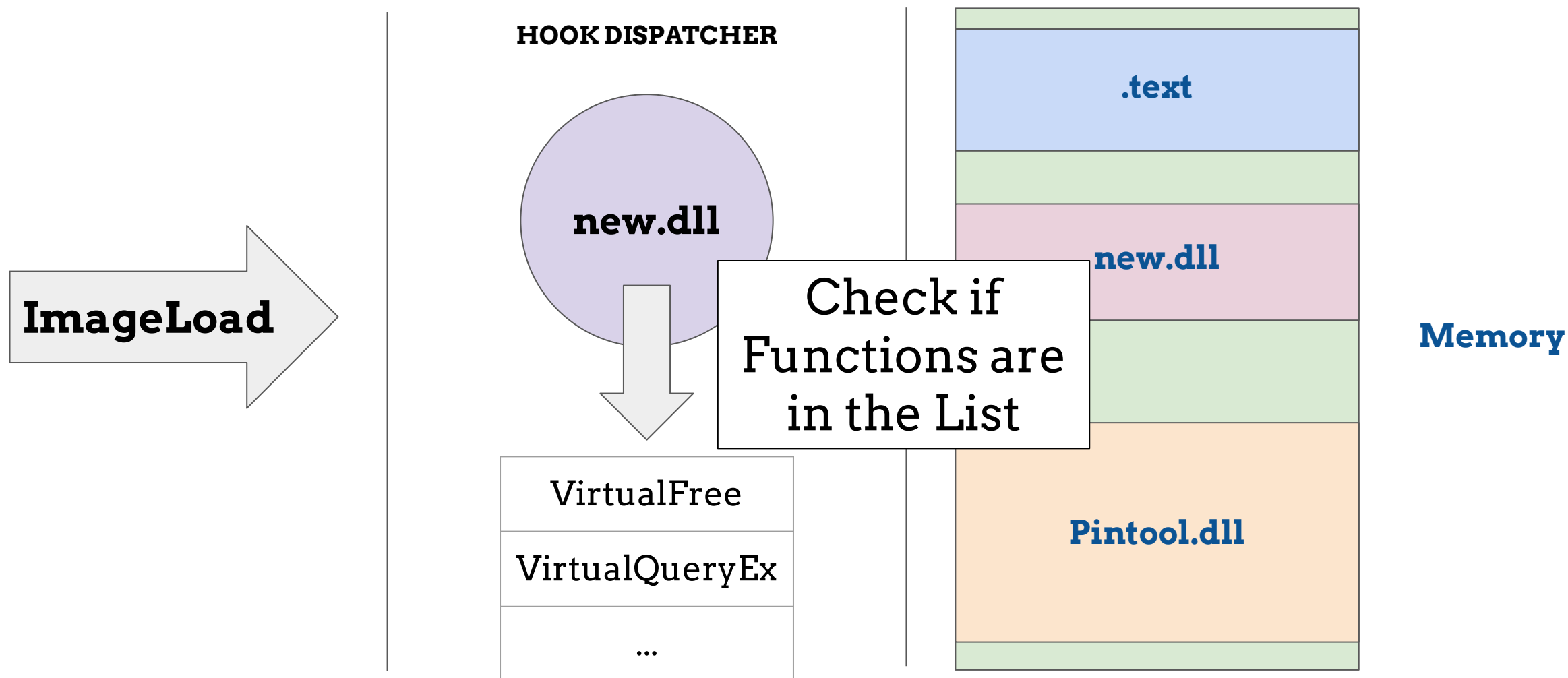


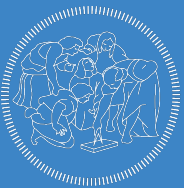
Arancino - Hook Functions



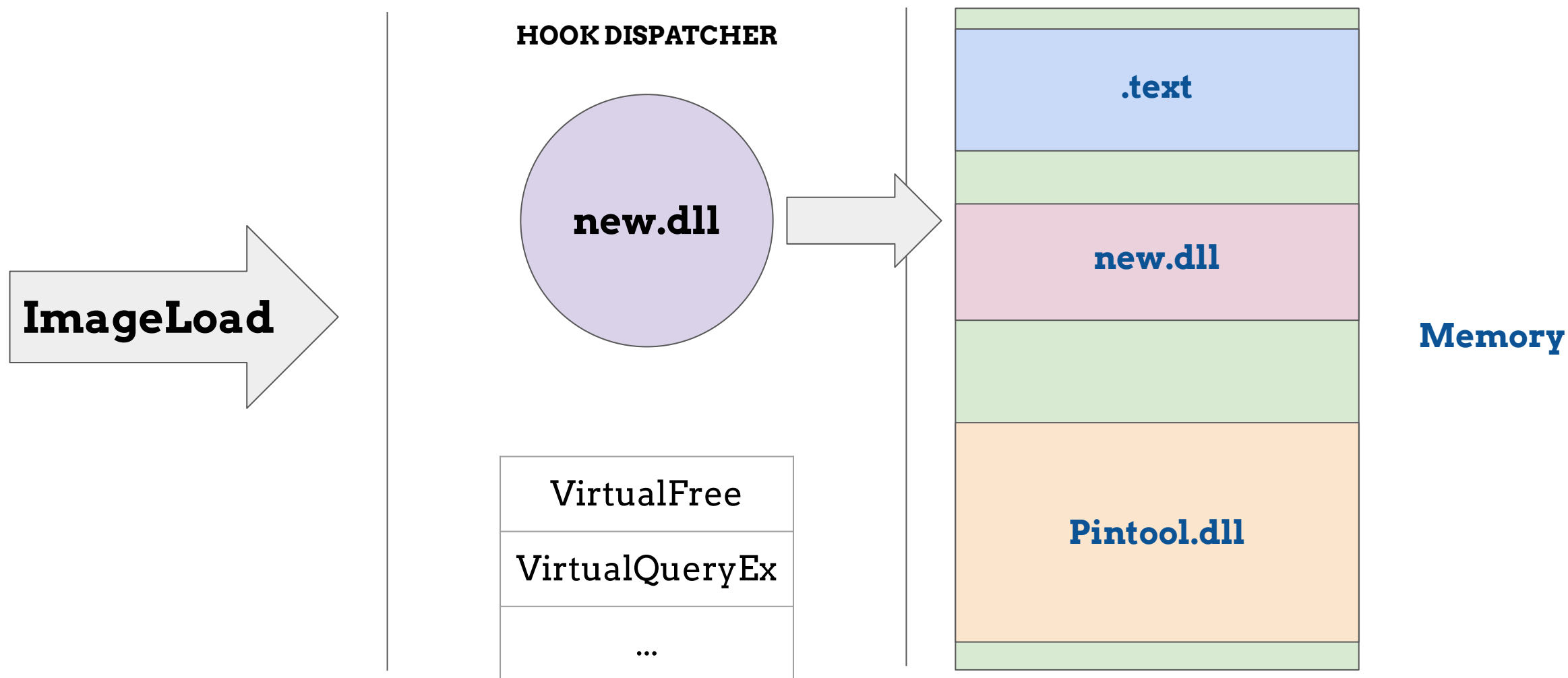


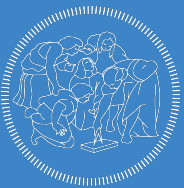
Arancino - Hook Functions



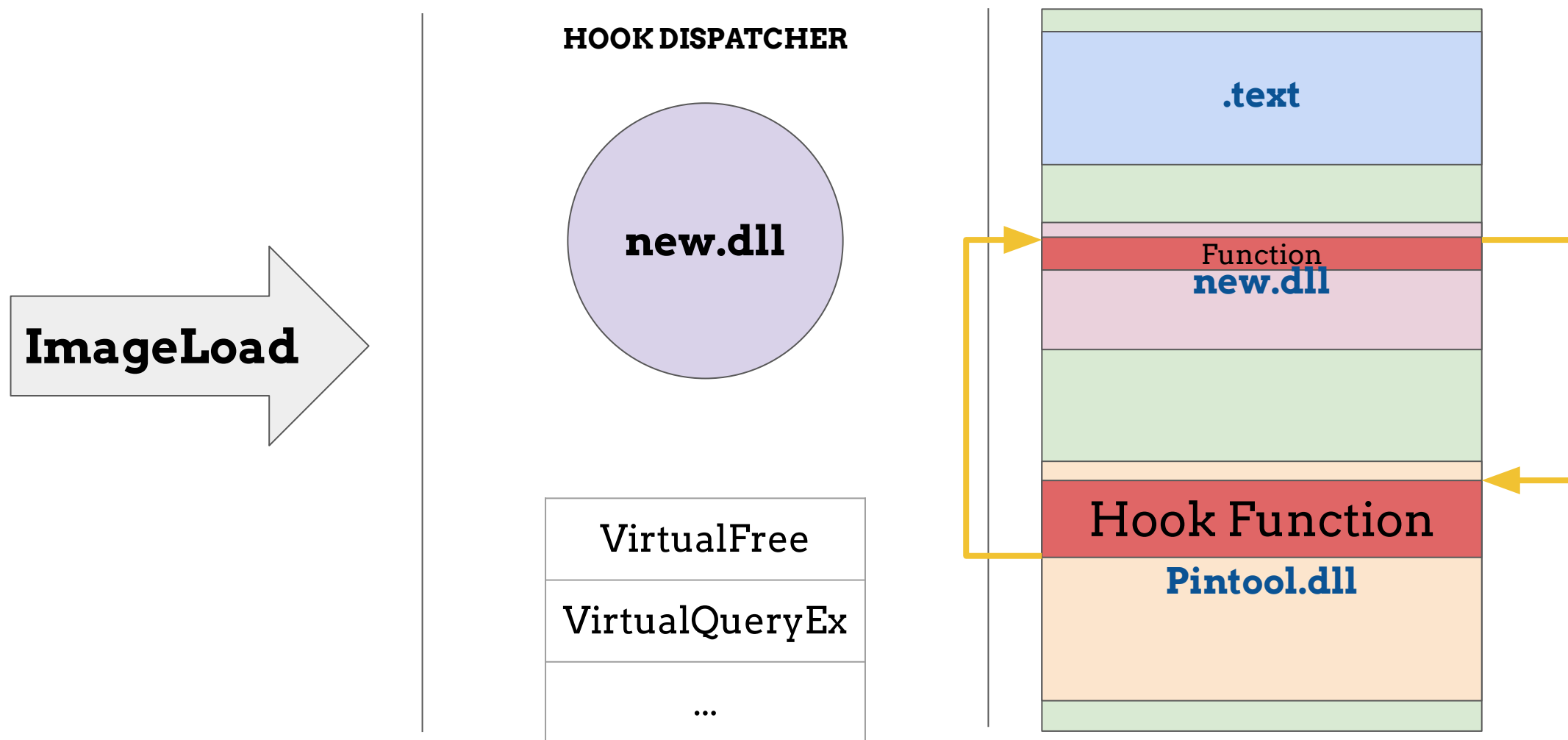


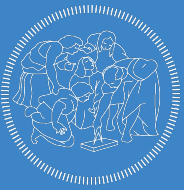
Arancino - Hook Functions





Arancino - Hook Functions



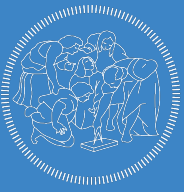


Hooked NtQuerySystemInformation

pin.exe -> cmd.exe

Hooked NtOpenProcess

to deny access to CSRSS.exe



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Environment Artifacts

- **Parent Detection**
- **Memory Fingerprinting**





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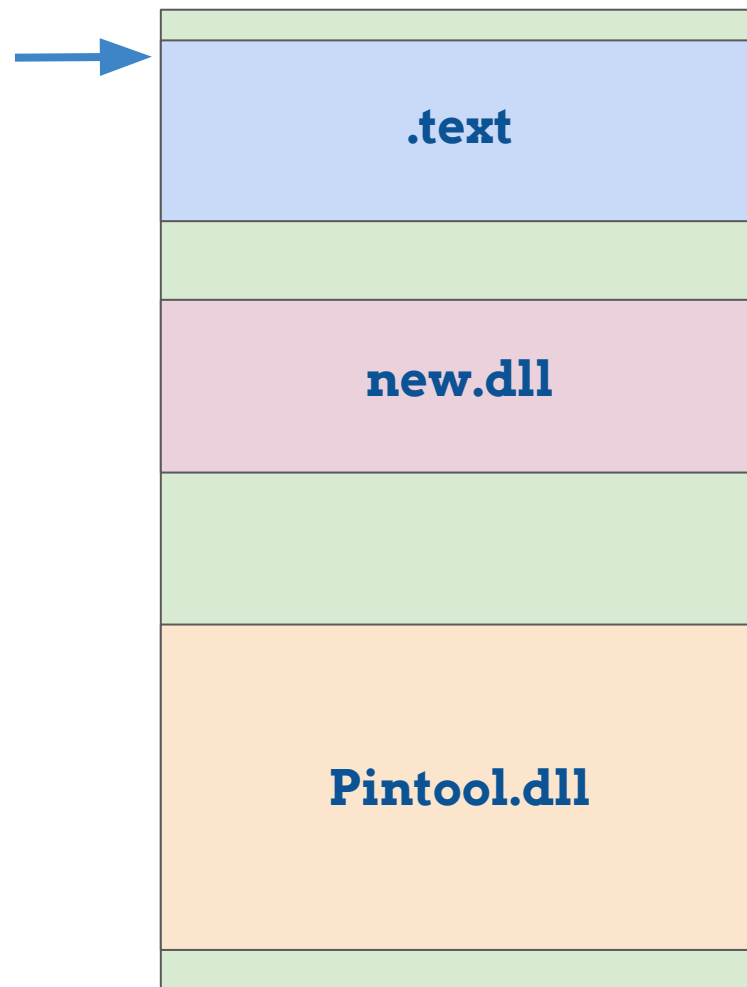
EA - Memory Fingerprinting

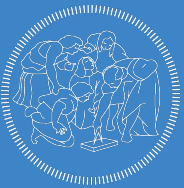




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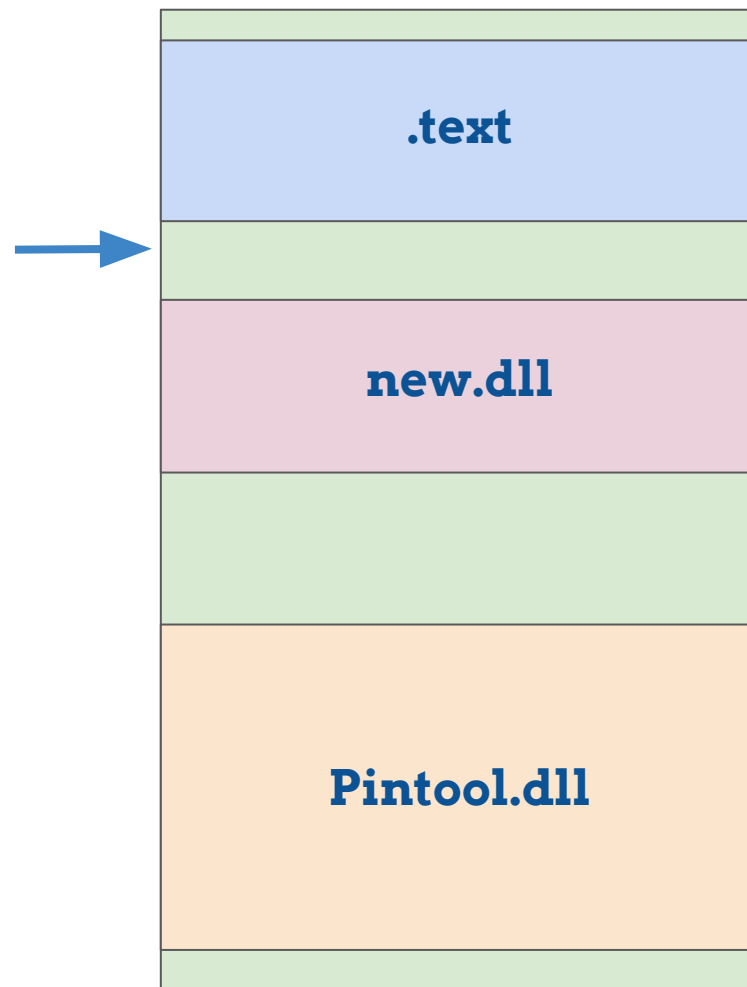
EA - Memory Fingerprinting





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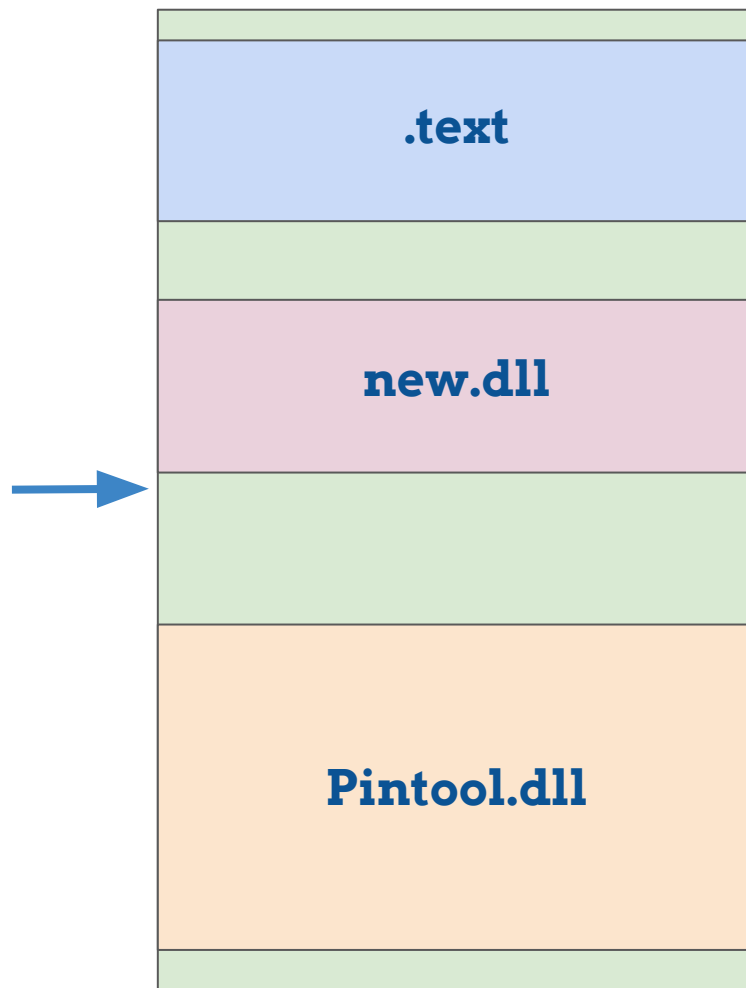
EA - Memory Fingerprinting

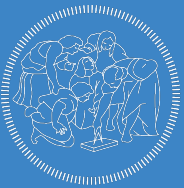




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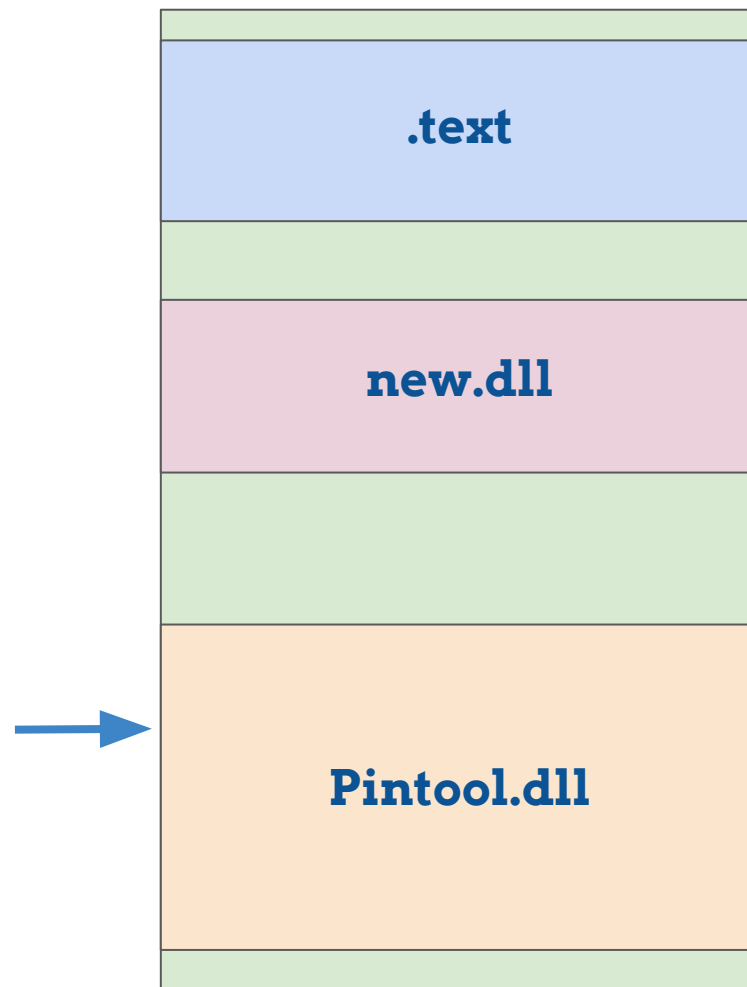
EA - Memory Fingerprinting





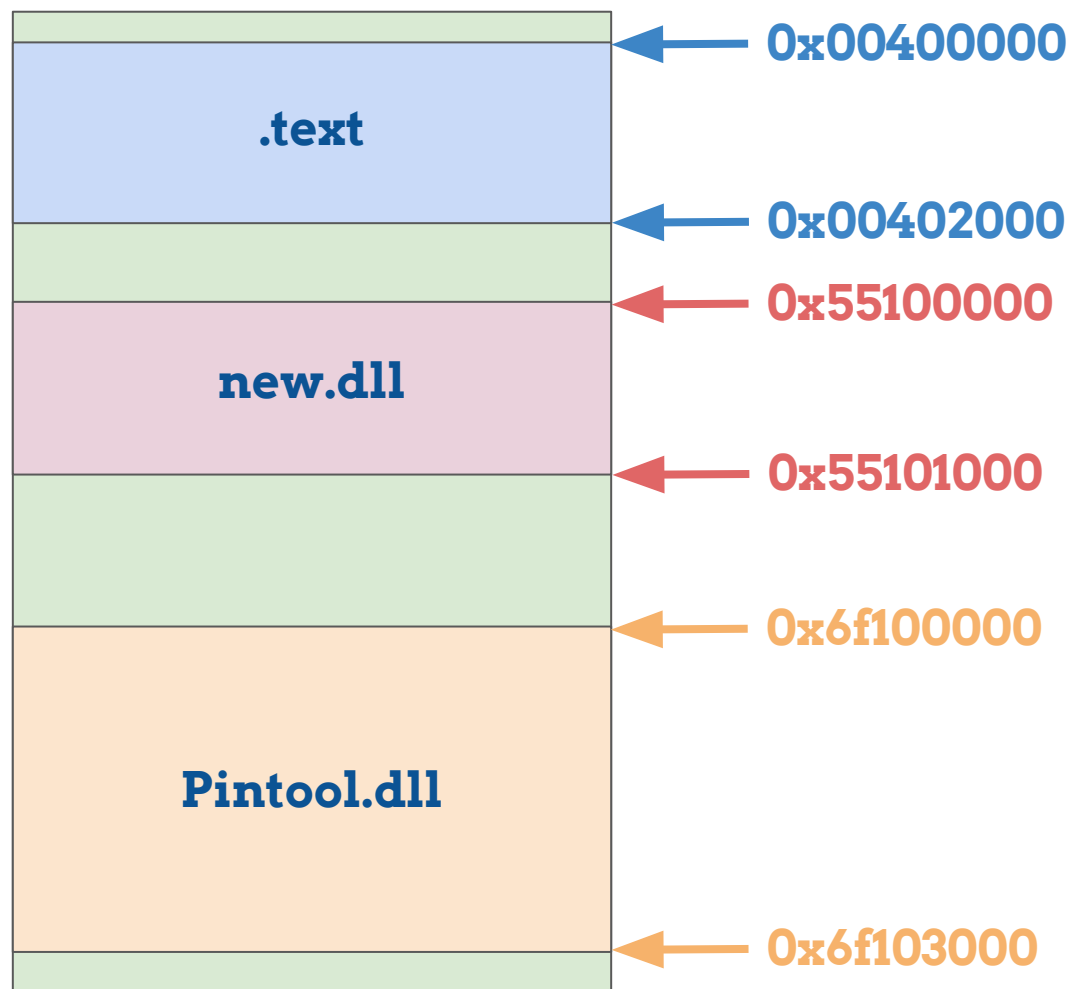
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EA - Memory Fingerprinting





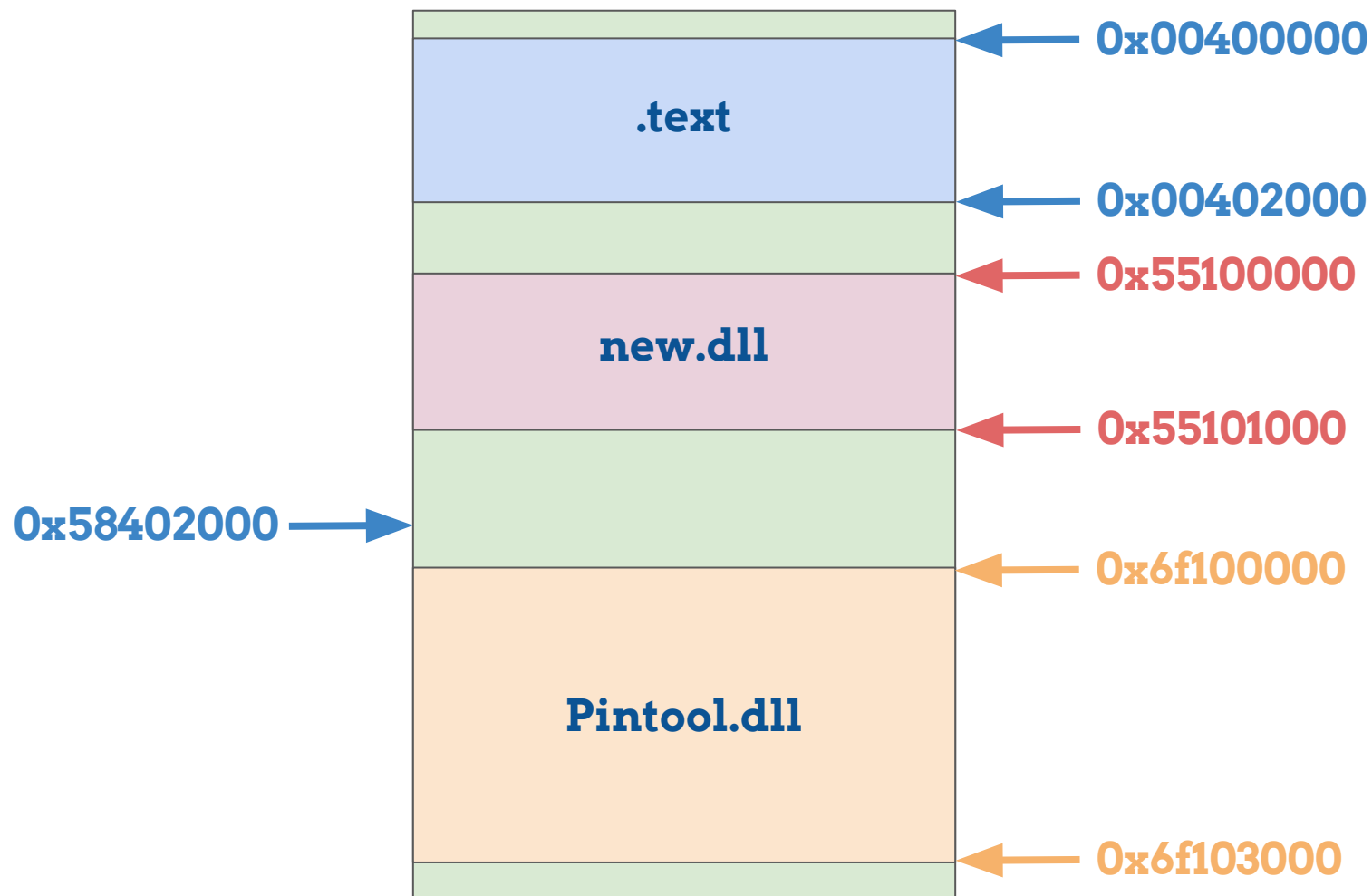
EA - Memory Fingerprinting





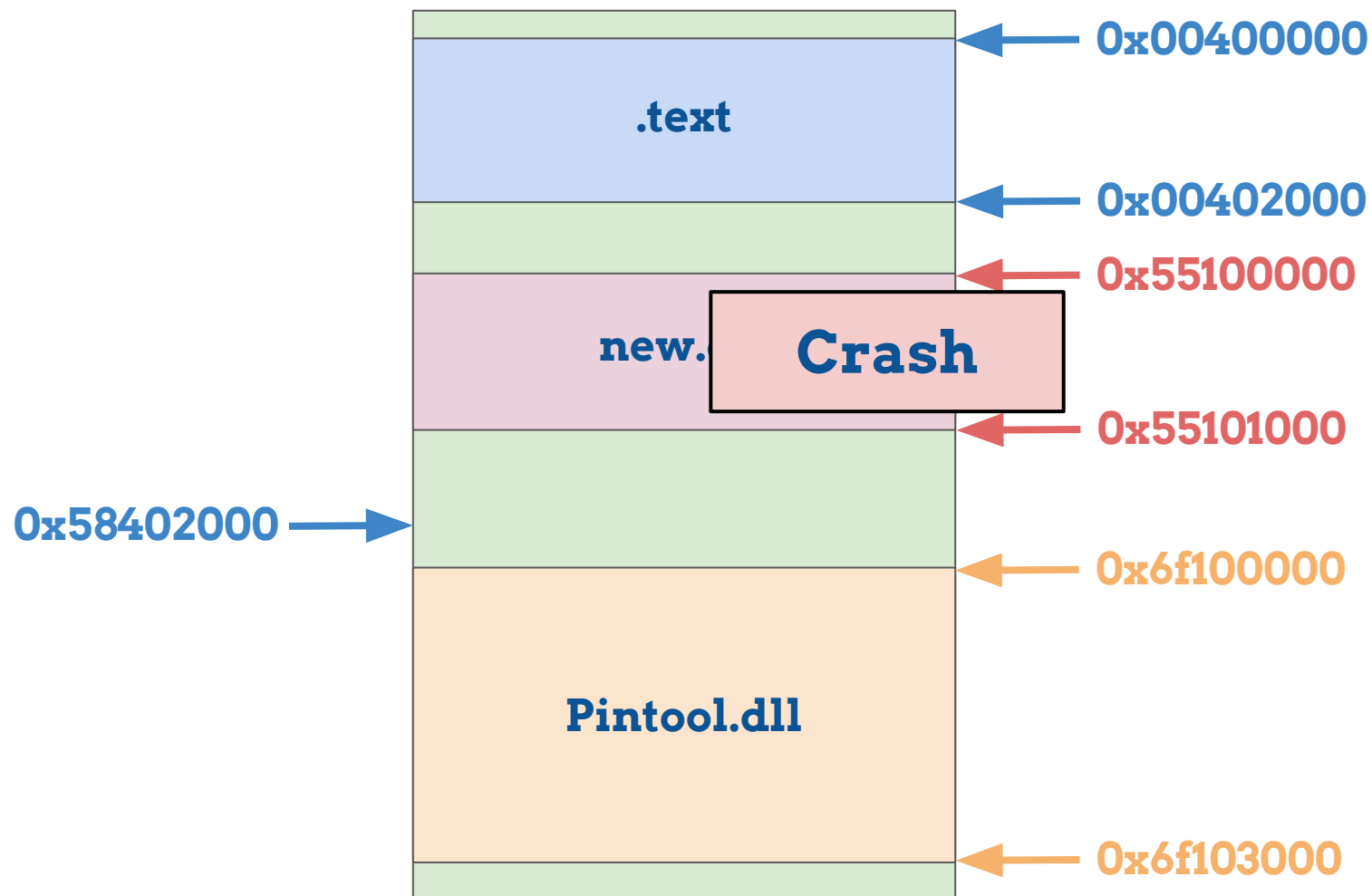
POLITECNICO
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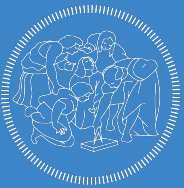
EA - Memory Fingerprinting





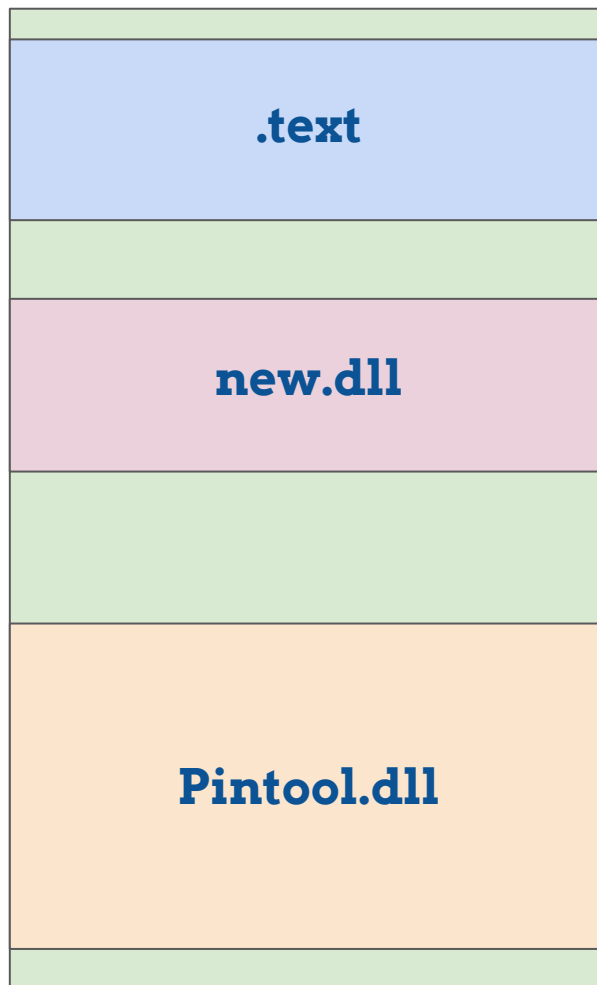
EA - Memory Fingerprinting



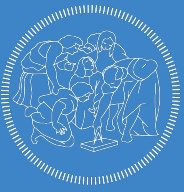


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EA - Memory Fingerprinting



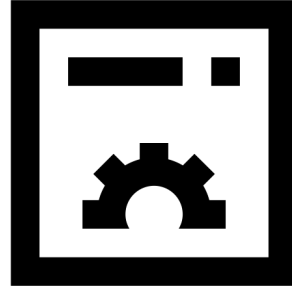
VirtualQuery



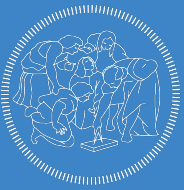
We Hook **NtQueryVirtualMemory**

We create a **Whitelist** of accessible memory regions updated at runtime.

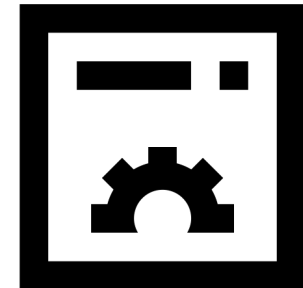
- **Main Module**
- **Libraries**
- **Heap and Stack**
- **PEB, TEB, etc.**
- **Mapped files**

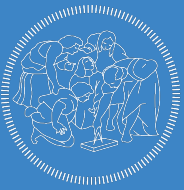


JIT Compiler Detection

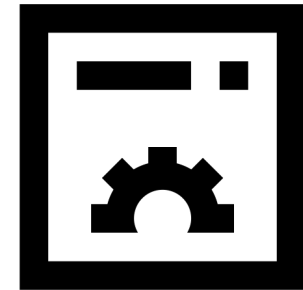


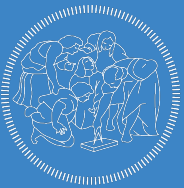
- **Memory Page Permissions**
 - Checks if there are **WX pages**
- **DLL Hook Detection**
- **Memory Allocations**





- **Memory Page Permissions**
 - Checks if there are **WX pages**
- **DLL Hook Detection**
- **Memory Allocations**





JITC Detection - DLL Hook

A process can search through memory for discrepancy caused by Hooks.

```
77C76F58 8D8424 DC020000 LEA EAX,DWORD PTR SS:[ESP+2DC]
77C76F5F 64:8B0D 00000000 MOV ECX,DWORD PTR FS:[0]
77C76F66 BA 406EC777 MOV EDX,ntdll.77C76F40
77C76F6B 8908 MOV DWORD PTR DS:[EAX],ECX
```

KiUserApcDispatcher - normal execution

```
77C76F58 E9 839CA0E3 JMP 5B680BE0
77C76F5D 0000 ADD BYTE PTR DS:[EAX],AL
77C76F5F 64:8B0D 00000000 MOV ECX,DWORD PTR FS:[0]
77C76F66 BA 406EC777 MOV EDX,ntdll.77C76F40
```

KiUserApcDispatcher - Instrumented execution



Arancino

Fake Memory Handler Modules

**Fake Read Handler
Module**

**Fake Write Handler
Module**

**Fake Free Handler
Module**

**Pattern Matching
Module**

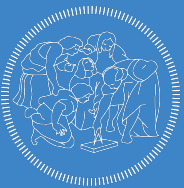
**Self Modifying Code
Module**

Process Information Module

Hooking Module

Hooking Function Module

Hooking Syscall Module

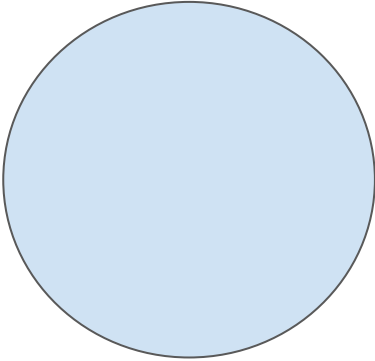


JITC Detection - DLL Hook

TRACE

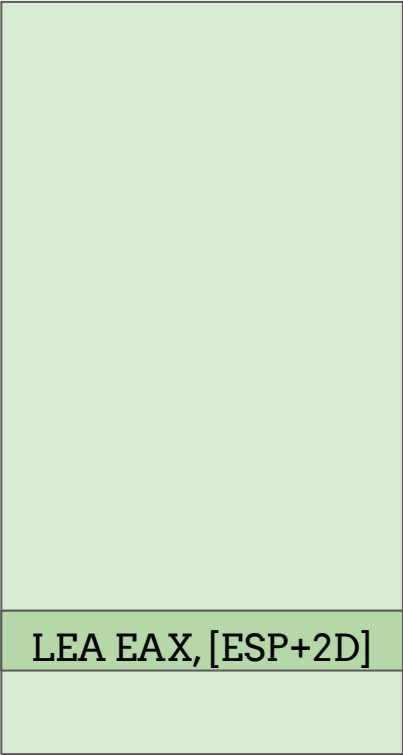


FAKE_READ_HANDLER



MEMORY

0x77C76F58



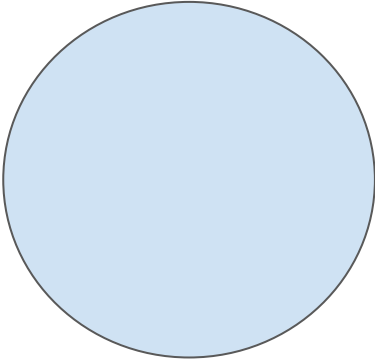


JITC Detection - DLL Hook

TRACE

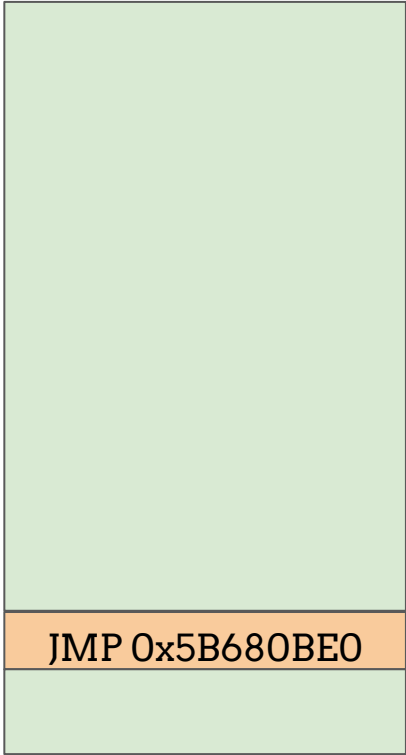


FAKE_READ_HANDLER



MEMORY

0x77C76F58



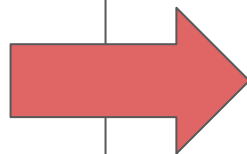


JITC Detection - DLL Hook

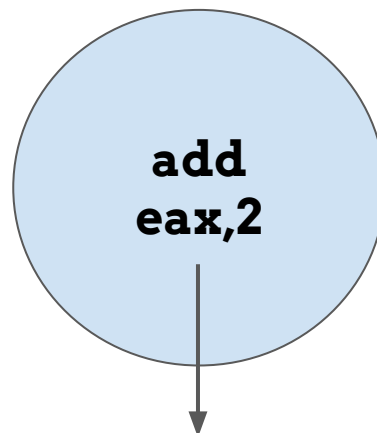
TRACE

```
add eax,2  
mov edx,[eax]  
cmp edx,0x8d  
jnz ebx
```

eax = 0x77C76F58



FAKE_READ_HANDLER



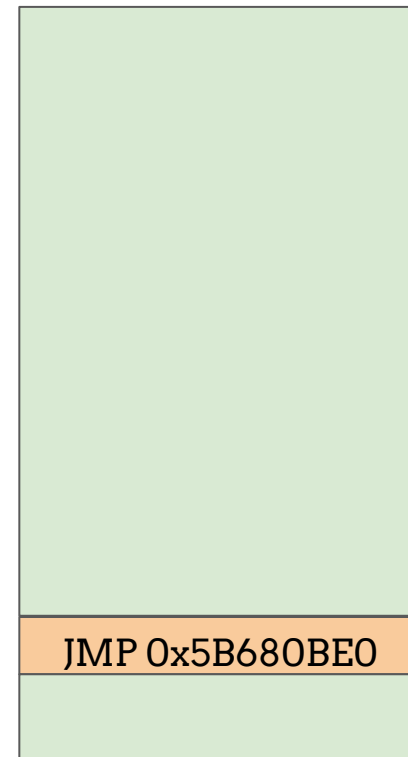
Is memory read
operation?

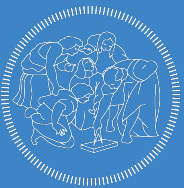


Nope!
Go next

MEMORY

0x77C76F58



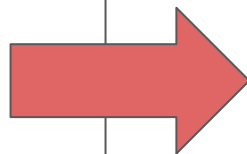


JITC Detection - DLL Hook

TRACE

```
add eax,2  
mov edx,[eax]  
cmp edx,0x8d  
jnz ebx
```

eax = 0x77C76F58



FAKE_READ_HANDLER

```
mov edx,  
[eax]
```

Is memory read
operation?



Yes

MEMORY

0x77C76F58

JMP 0x5B680BE0



JITC Detection - DLL Hook

TRACE

```
add eax,2  
mov edx,[eax]  
cmp edx,0x8d  
jnz ebx
```

eax = 0x77C76F58

FAKE_READ_HANDLER

```
mov edx,  
[eax]
```

Is the target address inside a
fake memory item?

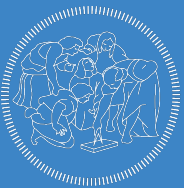


Yes
fake memory function
invoked

MEMORY

0x77C76F58

JMP 0x5B680BE0



JITC Detection - DLL Hook

TRACE

```
add eax,2  
mov edx,[eax]  
cmp edx,0x8d  
jnz ebx
```

eax = 0x77C76F58

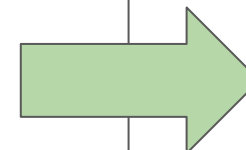
FAKE_READ_HANDLER

```
mov edx,  
[eax]
```

FakeMemoryFunc()

0x77C76F58

0x77C76F5F



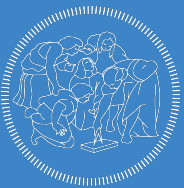
MEMORY

0x01C00A2B

LEA EAX,[ESP+2D]

0x77C76F58

JMP 0x5B680BE0



JITC Detection - DLL Hook

TRACE

```
add eax,2  
mov edx,[eax]  
cmp edx,0x8d  
jnz ebx
```

Instrumented process
read the fake value:
LEA EAX,[ESP+2D]
and doesn't detect
PIN

FAKE_READ_HANDLER

```
mov edx,  
[eax]
```

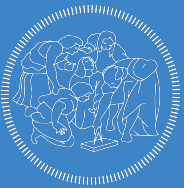
MEMORY

0x01C00A2B

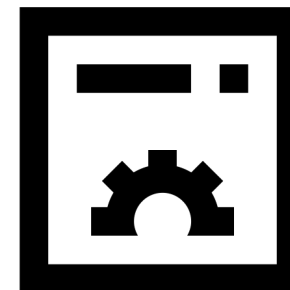
LEA EAX,[ESP+2D]

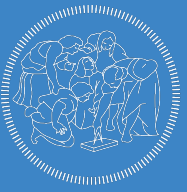
0x77C76F58

JMP 0x5B680BE0



- **Memory Page Permissions**
 - Checks if there are **WX pages**
- **DLL Hook Detection**
- **Memory Allocations**



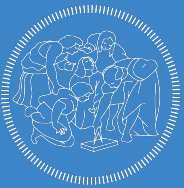


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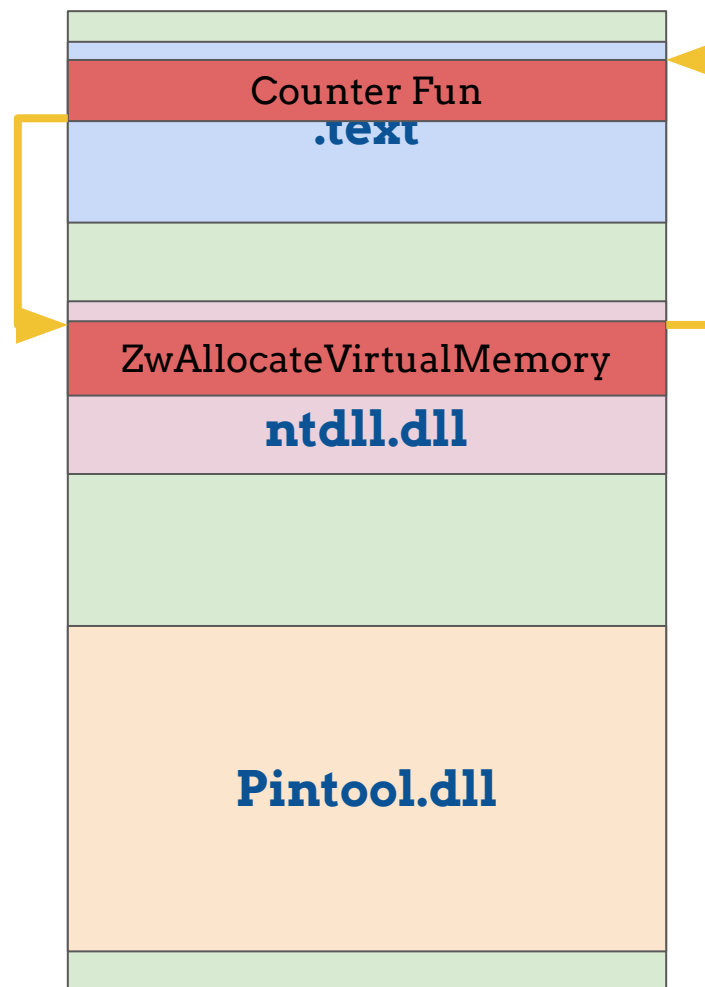
JIT Compiler - API Hook

JIT Compiler needs **Memory** to perform the compiling

We can monitor the allocation by Hooking at
ZwAllocateVirtualMemory

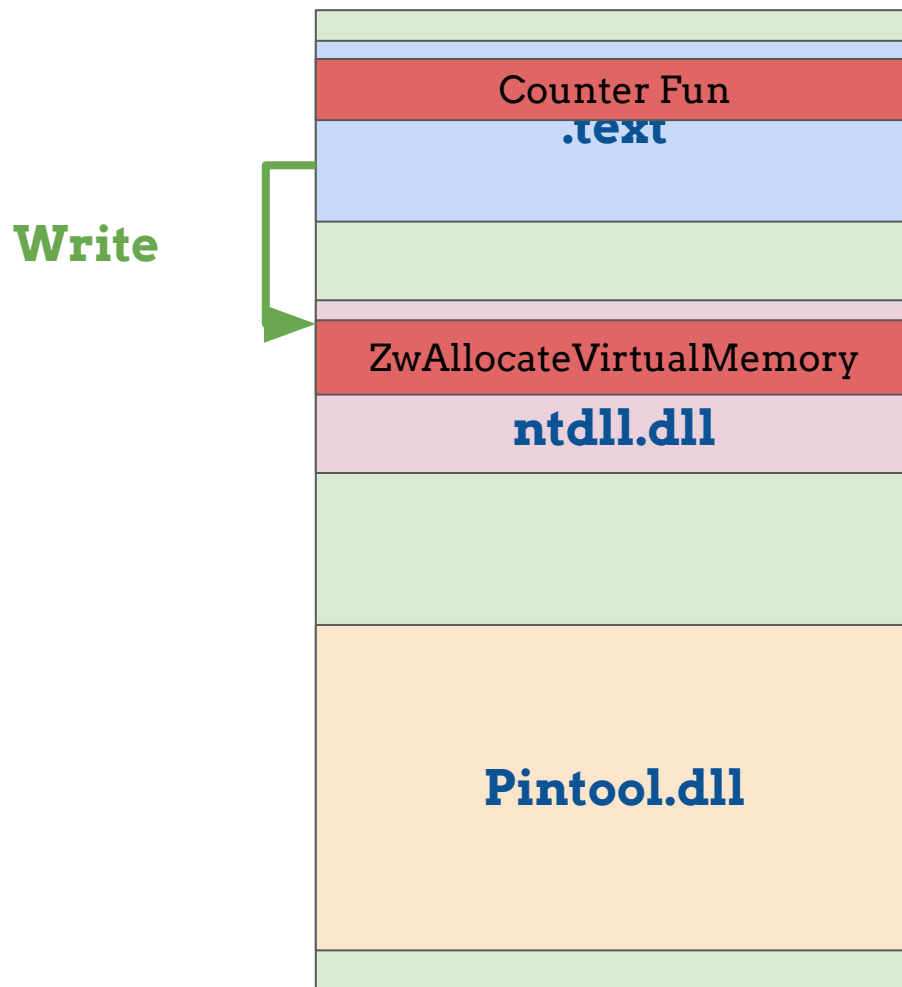


JIT Compiler - API Hook





JIT Compiler - API Hook





Arancino

Fake Memory Handler Modules

Fake Read Handler
Module

Fake Write Handler
Module

Fake Free Handler
Module

**Pattern Matching
Module**

**Self Modifying Code
Module**

Process Information Module

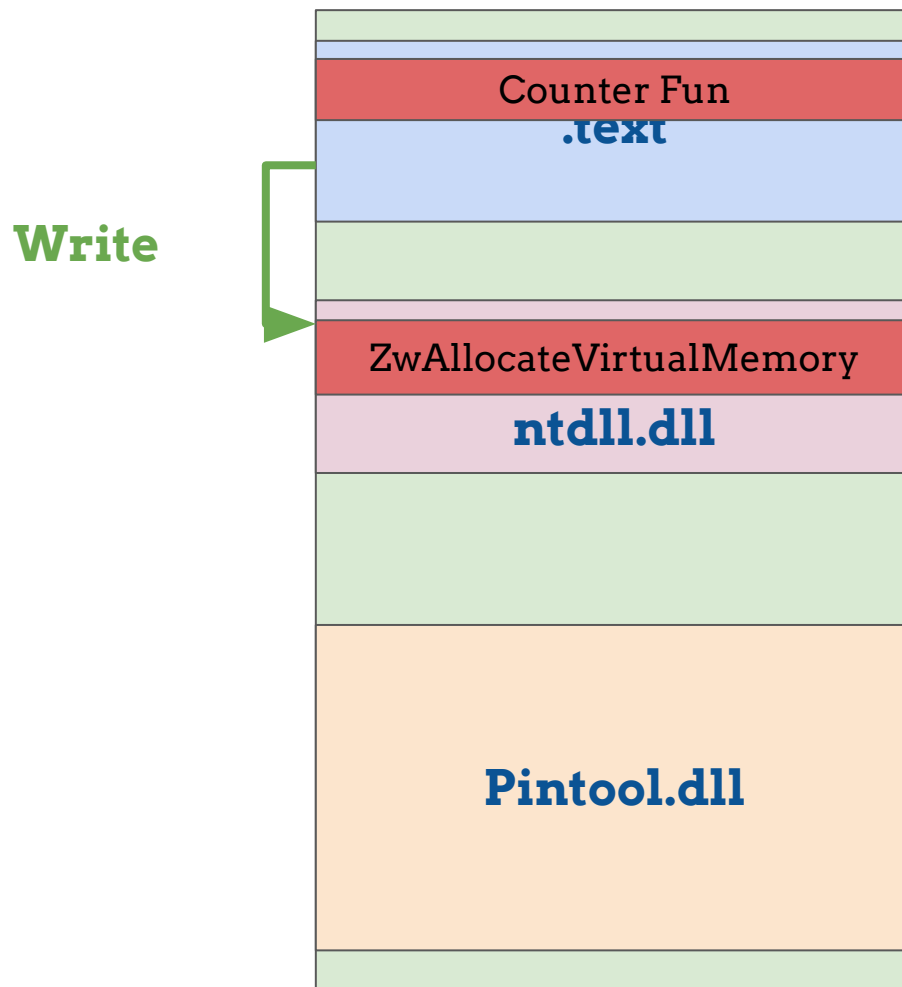
Hooking Module

Hooking Function Module

Hooking Syscall Module

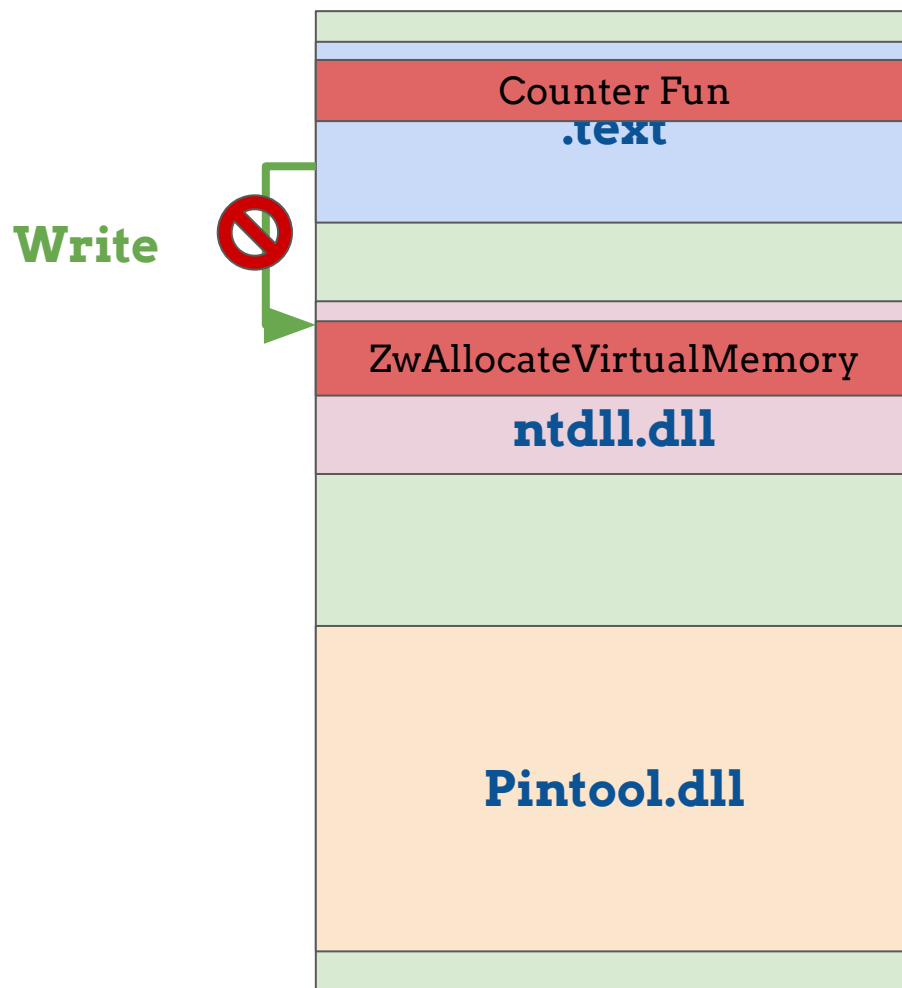


JIT Compiler - API Hook



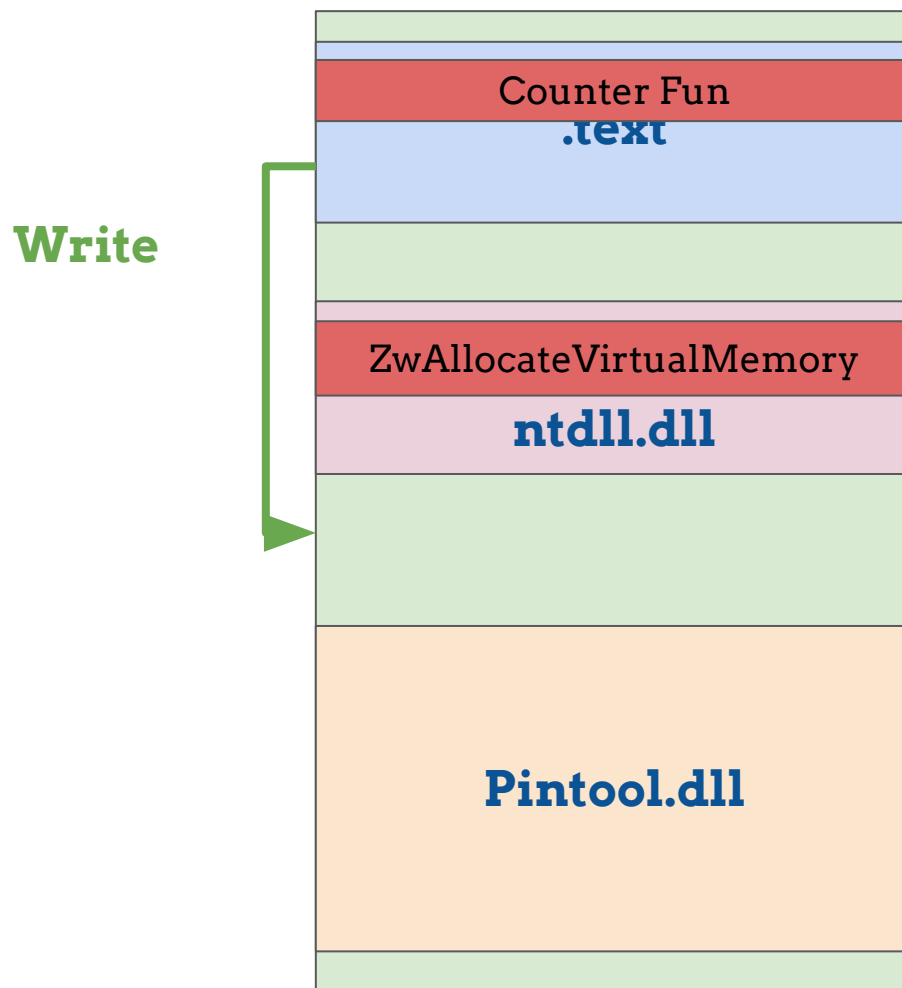


JIT Compiler - API Hook



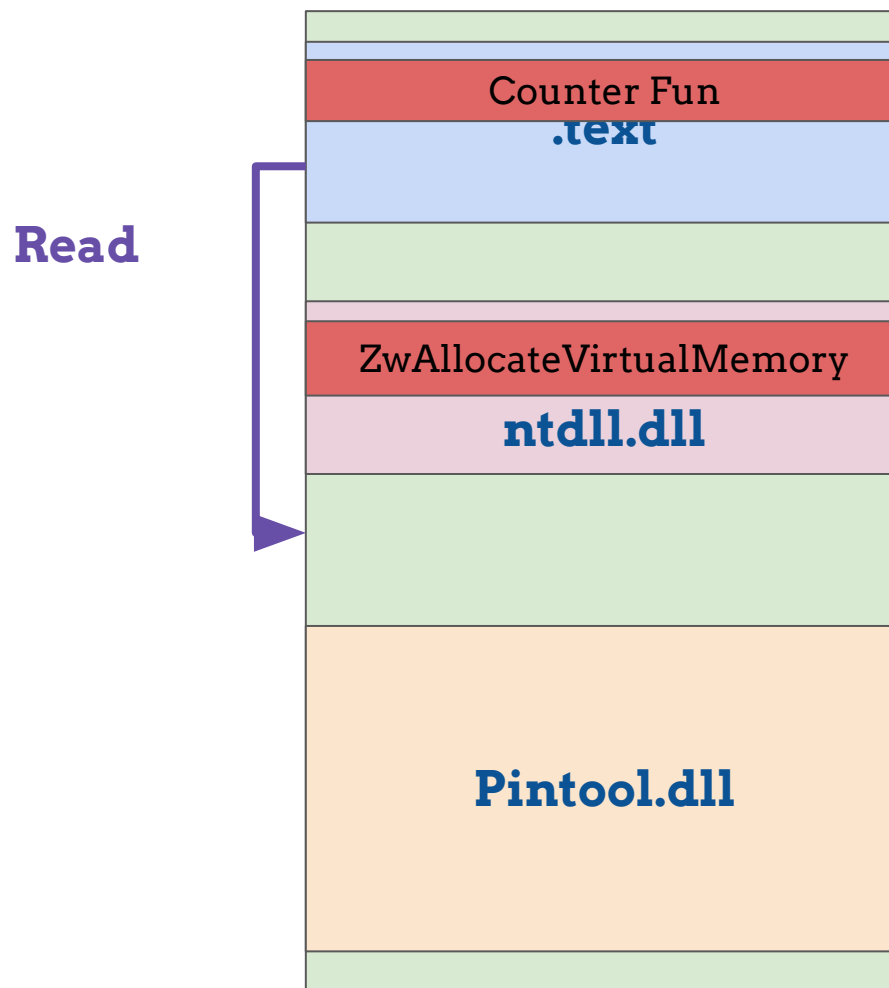


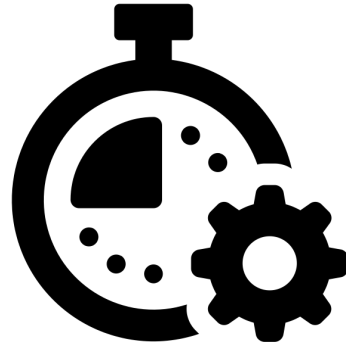
JIT Compiler - API Hook



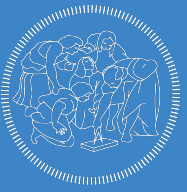


JIT Compiler - API Hook



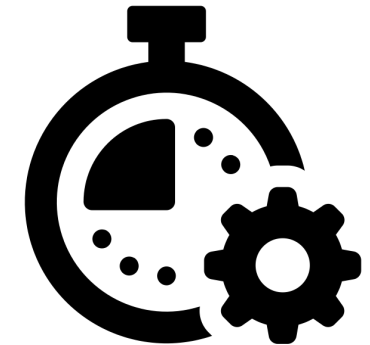


Overhead Detection



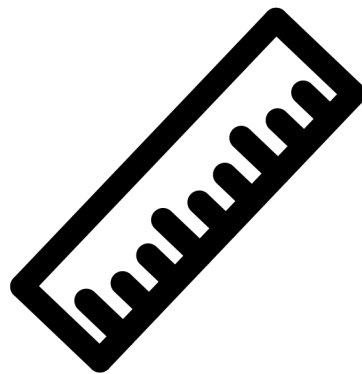
- **Windows Time**

- Use windows API
 - GetTickCount and timeGetTime
- Or Windows Structures
 - KUSER_SHARED_DATA.

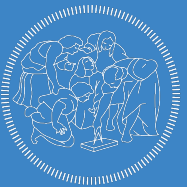


- **CPU Time**

- Count CPU cycles (`rdtsc`)

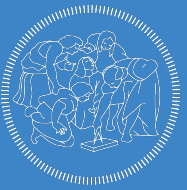


Evasive Malware Measurement



Dataset

- **7006** Binaries
- Virus Total Intelligence (3+ AV Detection)
- From October 2016 to February 2017



Environment Setup

- **Virtual Machine** (VirtualBox)
- **Windows 7** (64-bit)
- **Custom Apps** (Adobe Reader, Chrome, and media players)
- **User Data** (saved credentials, browser history, etc.)
- **Basic User Activity** (moving the mouse, launching applications)
- **5 min** run



Evasive Malware

At least one evasive behavior: **1,093 / 7006 (15.6%)**

Family Name [1]	Samples	Evasive	Techniques
virlock	619 (8.8%)	600 (96.9%)	2
confidence	505 (7.2%)	68 (13.5%)	4
virut	242 (3.5%)	13 (5.4%)	2
mira	230 (3.3%)	9 (3.9%)	1
upatre	187 (2.7%)	2 (1.1%)	1
lamer	171 (2.4%)	0 (0.0%)	0
sivis	168 (2.4%)	0 (0.0%)	0

[1] **AvClass** <https://github.com/malicialab/avclass>



Top Evasive Malware

At least one evasive behavior: **1,093 / 7006 (15.6%)**

Family Name [1]	Samples	Evasive	Techniques
sfone	19	19 (100.0%)	1
unruy	11	11 (100.0%)	1
virlock	619	600 (96.9%)	2
vilsel	13	8 (61.5%)	2
urelas	18	9 (47.4%)	2
confuser	52	8 (44.4%)	1
vobfus	29	19 (36.5%)	1

[1] **AvClass** <https://github.com/malicialab/avclass>

At least one evasive behavior: **1,093 / 7006 (15.6%)**

	Technique	#
Code Cache Artifacts	Self-modifying code	897
Environment Artifacts	Parent detection	259
JIT Compiler Detection	Write on protected memory region	40
Environment Artifacts	Check DEBUG flag	5
Environment Artifacts	Memory fingerprinting	3



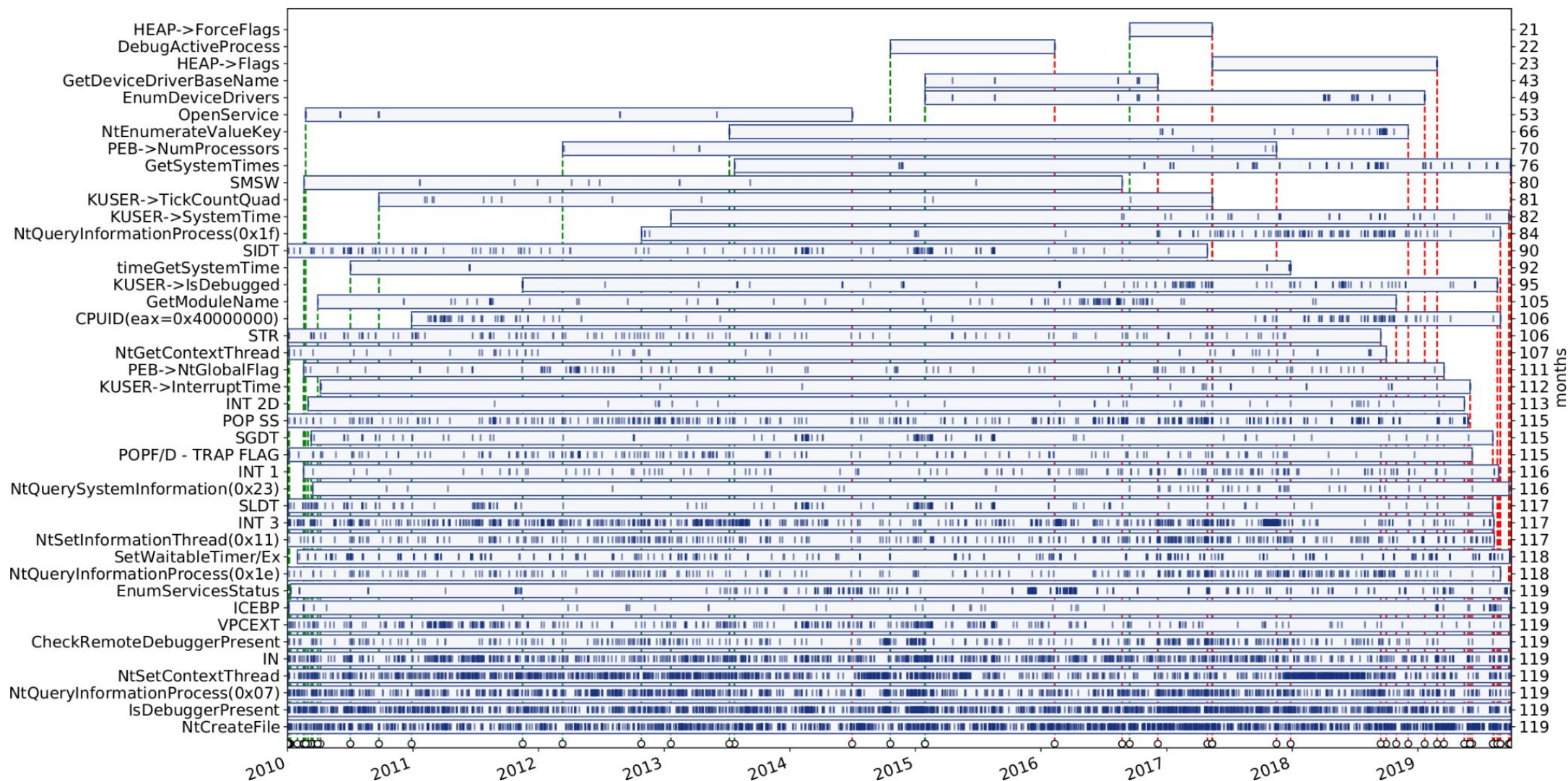
Overhead

	Pin time [ms]	Arancino [ms]	Arancino overhead [%]	Module activated
Parent Detection	850	870	2%	Hooking Module
EIP Detection - int2e	710	1,150	62%	Pattern Match Module
Memory Fingerprinting	2,000	7,090	254,5%	Fake Read Module
Memory Allocations	2,000	2,900	45%	Fake Write Module + Hooking Module



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Evasive Malware Timeline



Thanks!

<https://github.com/necst/arancino>

Mario Polino

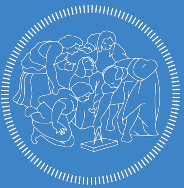
<mario.polino@polimi.it>

Questions?

<https://github.com/necst/arancino>

Mario Polino

<mario.polino@polimi.it>



- Icons, CC from Noun Project:
 - Vicons Design
 - Aya Sofya
 - Adnen Kadri
 - Stock Image Folio
 - Icon Fair
 - Creative Stall
 - Gregor Cresnar