


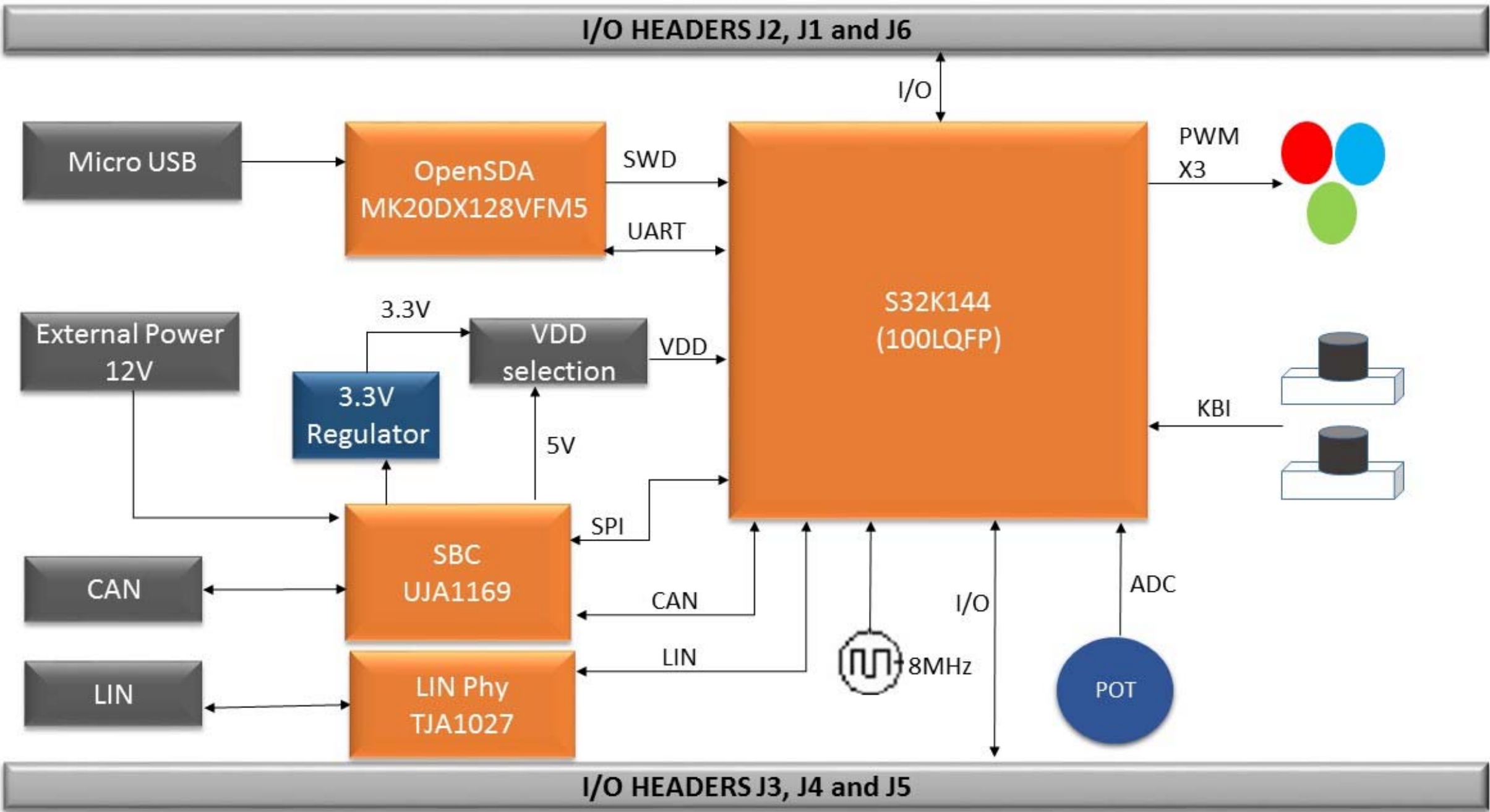
Table of Contents	
2	Notes and Block Diagram
3	S32K144 MCU
4	OpenSDA interface
5	Power Supply/SWD
6	I/O Headers

Revisions			
Rev	Description	Date	Approved
XA	Initial Release	APR-13-2016	O. Romero
A	Prototype Production	APR-14-2016	O. Romero
AX1	Development	AUG-16-2016	O. Romero
B	2nd Release	SEP-02-2016	O. Romero

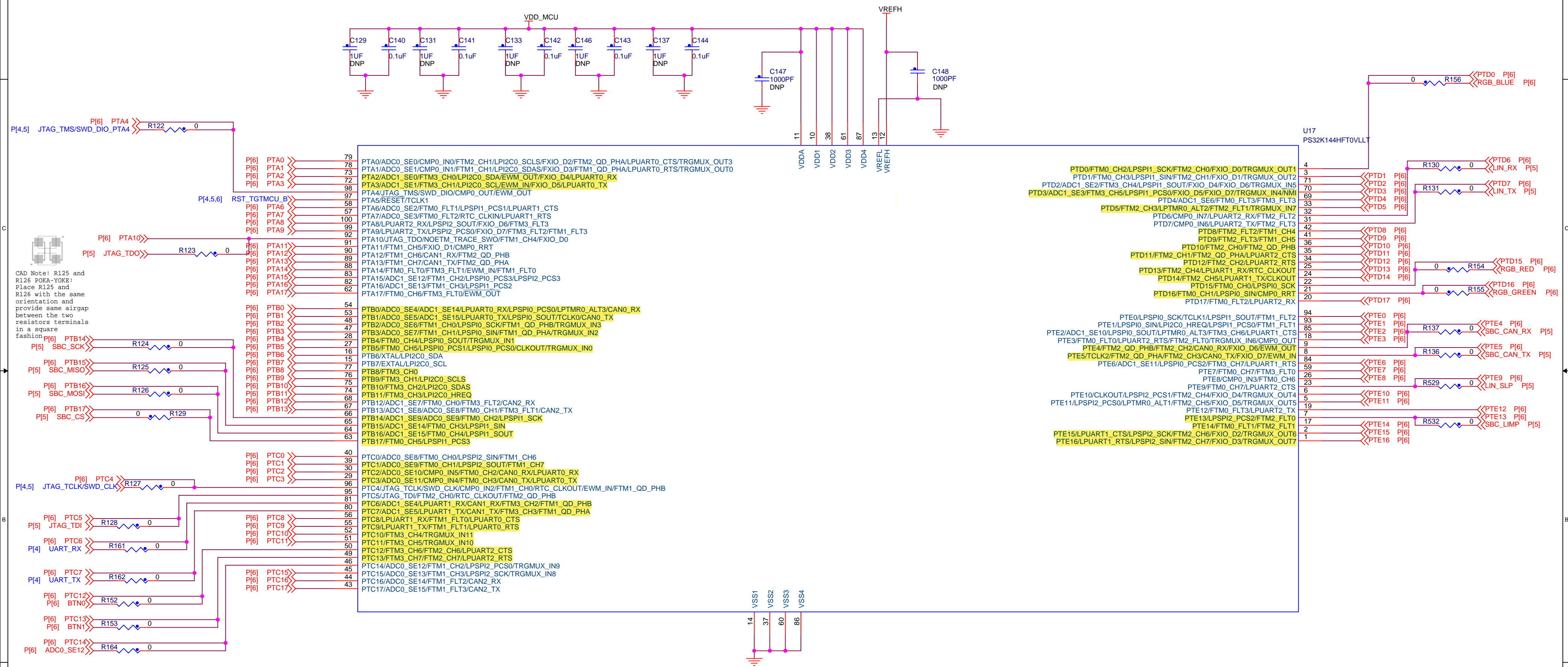
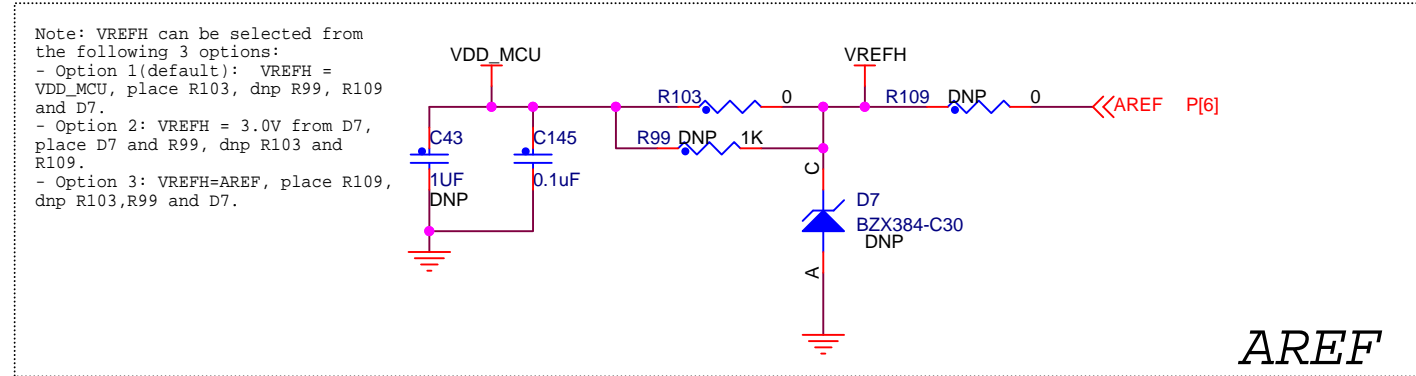
S32K144EVB-Q100

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ICAP Classification:		FCP:	FIUO: X PUBI:
Designer: Osvaldo Romero	Drawing Title: S32K144EVB-Q100		
Drawn by: Osvaldo Romero	Page Title: TITLE PAGE		
Approved: APPROVER	Size C	Document Number SCH-29248 PDF: SPF-29248	Rev B
Date: Friday, September 02, 2016		Sheet 1 of 6	

1. Unless Otherwise Specified:
All resistors are in ohms, 1% and 5 %
All capacitors are in uF, 10% , 20 % and 5%
All voltages are DC
All polarized capacitors are aluminum electrolytic
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification: FCP: FIUO: X PUBI:		
Drawing Title: S32K144EVQ100		
Page Title: Notes and Block Diagram		
Size C	Document Number SCH-29248 PDF: SPF-29248	Rev B
Date: Friday, September 02, 2016	Sheet 2 of 6	



OpenSDA Interface

