NPTEL » Compiler Design

Announcements

About the Course

Unit 3 - Week 1

Course outline How does an NPTEL online course work? week 0 Week 1 Lecture 01 : Introduction Lecture 02 : Introduction (Contd.) Lecture 03 : Introduction (Contd.) Lecture 04 : Introduction

(Contd.)

(Contd.)

(Contd.)

Lecture Materials

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Week 12

Detailed Solution

Text Transcripts

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O Quiz: Assignment 1

Lecture 05 : Introduction

Lecture 06 : Introduction

a) Code generation

Mentor Progress

Which phase of compiler does NOT use symbol table?

Assignment 1 The due date for submitting this assignment has passed. As per our records you have not submitted this assignment.

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Score: 0

No, the answer is incorrect.

a) Swap space

b) Main memory

c) CPU registers

No, the answer is incorrect.

11) Intermediate code helps in

a) Program Analysis

b) Code optimization

c) Retargeting code

12) Output file of Lex is ____ the input file is Myfile

d) Code check

No, the answer is incorrect.

a) Myfile.e

No, the answer is incorrect.

Accepted Answers:

b) Myfile.yy.c c) Myfile.lex d) Myfile.obj

Accepted Answers:

Accepted Answers:

d) None of the other options

Accepted Answers:

No, the answer is incorrect.

7) Error recovery helps to

No, the answer is incorrect.

a) Silicon Compilation

c) Circuit Compilation

d) None of the other options

Loops are the major targets for optimization since

b) Loop body is repeated to several times

c) Condition check takes exceedingly large time

For maximum speed of execution of target code , temporary variables be best allocated to

a) Loop may go to infinite execution

d) None of the other options

b) HDL Compilation

No, the answer is incorrect.

Accepted Answers:

Accepted Answers:

a) Report multiple errors

b) Rectify multiple errors

d) None of the other options

c) Both report and rectify multiple errors

Converting a hardware description into actual circuitry is known as

Accepted Answers:

No, the answer is incorrect.

A compiler can check

a) Logical error

b) Syntax error

c) Both logical error and syntax error

d) Not logical and syntax error

Accepted Answers:

No, the answer is incorrect.

Accepted Answers:

No, the answer is incorrect.

a) First

b) Second

c) Third

No, the answer is incorrect.

a) Parse tree

c) Binary tree

No, the answer is incorrect.

detected in the phase of

a) Lexical Analysis

b) Syntax Analysis

c) Semantic Analysis

d) None of the other options

Which of these is not true about Symbol Table

d) Created during pass 1

a) All the labels of the instructions are symbols

b) Table has entry for symbol name address value

c) Perform the processing of the assembler directives

Accepted Answers:

b) Keyword tree

Accepted Answers:

d) None of the mentioned

Output of the syntax analysis is called

d) All of the other options

A programming language does not allow integer division operation. This is generally

Accepted Answers:

b) Syntax Analysis c) Lexical Analysis d) None of the other options

Which phase of compiler is Syntax Analysis?

Due on 2020-02-12, 23:59 IST.

1 point

Ask a Question