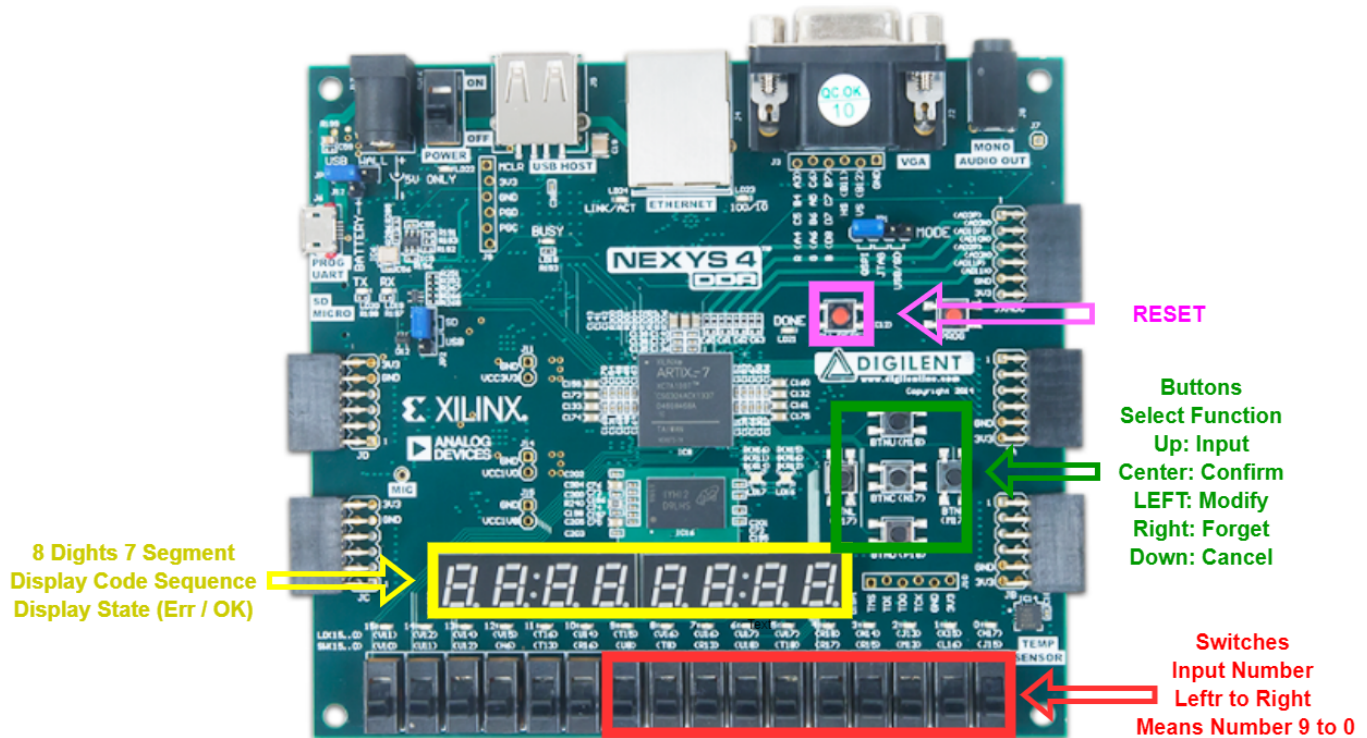


# University of Birmingham, 2020 Digit Design Assignment: An Combination Lock

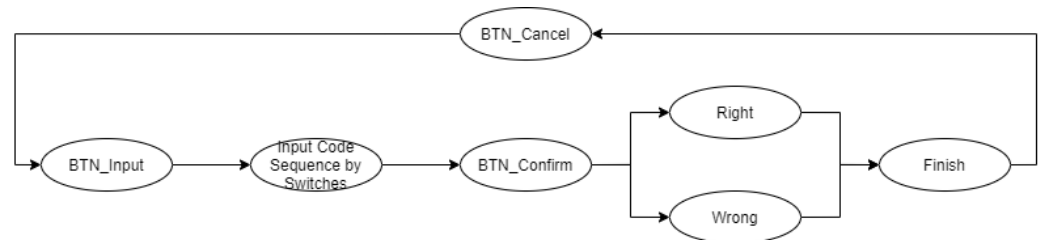
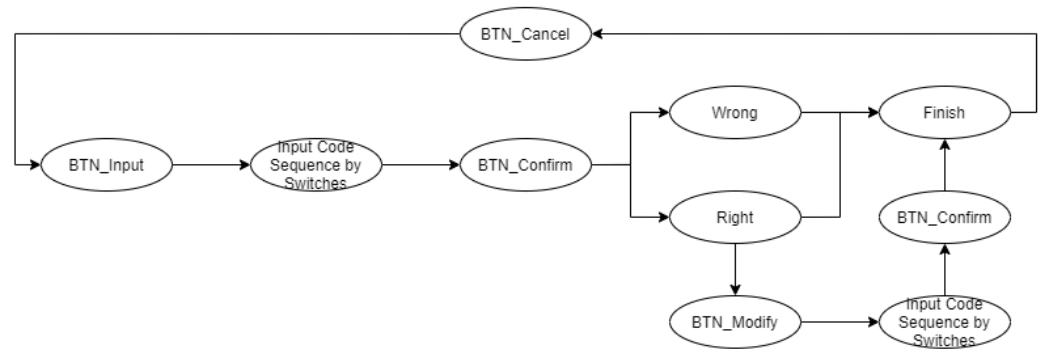
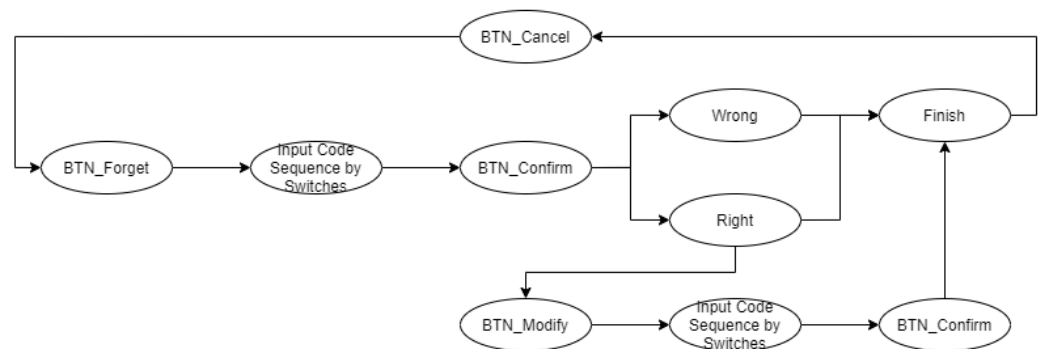
Using VHDL language and implement on Nexys4 DDR ,design by Yifan Du, Check it on [Github](#)

## Hardware Specification



- **RESET:** Reset all value, code sequence and password
- **Buttons:** Use button to entry different function (mode).
  - **UP:** Normal input mode
  - **Left:** Modify password mode, only working after input right code sequence
  - **Center:** Confirm button, when user finish input, push this button to confirm input code sequence.
  - **Right:** Forget password mode / Admin mode
  - **Down:** Cancel / exit button.
- **8 Digit 7 Segments:** Initial state will display "\_"
- **Switches:** Push up then push down same switch, it means input one number
  - e.g: push up switch [4] then push down, it means input decimal number 4

## Button Function Specification

**Normal Input Mode****Modity Password Mode****Forget Passowrd Mode**

Input whole code sequence then push confirm (center) button

*e.g* Input code sequence 0123, switch[0]-up-down -> switch[1]-up-down -> switch[2]-up-down  
-> switch[3]-up-down -> confirm