

A

A

B

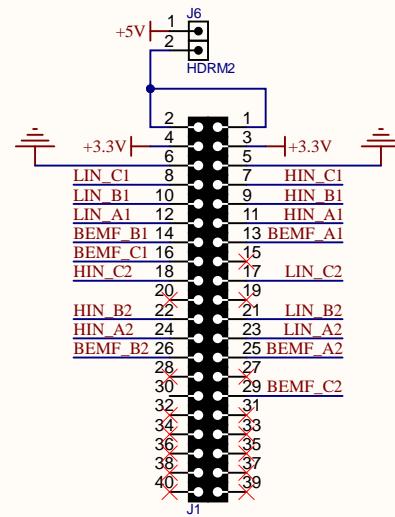
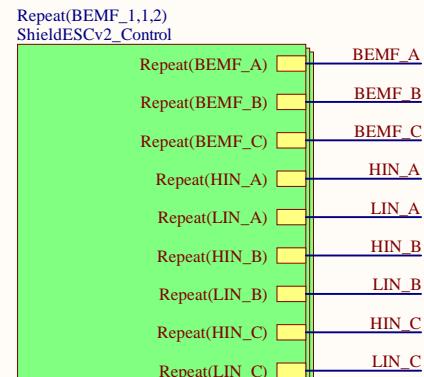
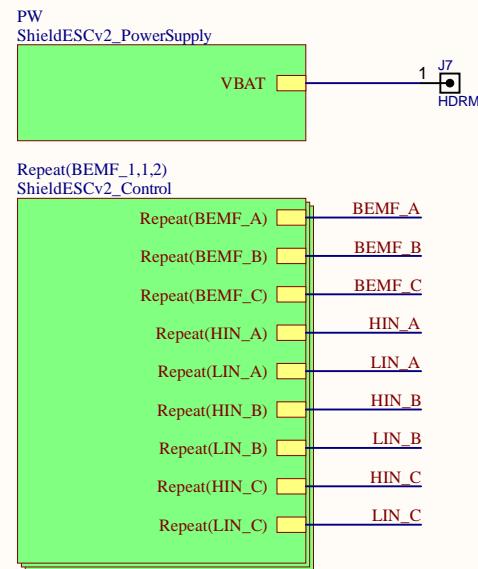
B

C

C

D

D



A

A

B

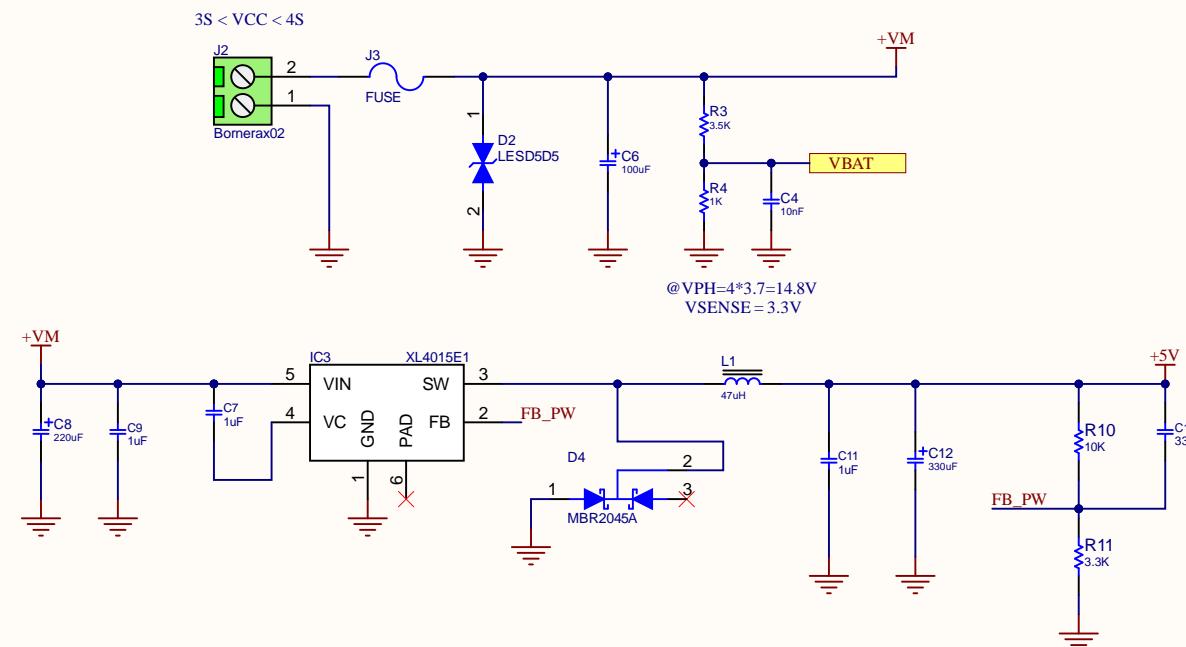
B

C

C

D

D



Title	ShieldESCV2_PowerSupply.Sc
Date	27/05/2025
Draw by	Yafahers Mendoza C.

A

A

B

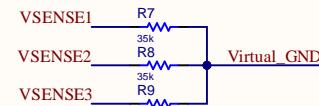
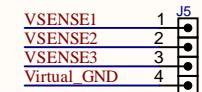
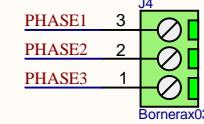
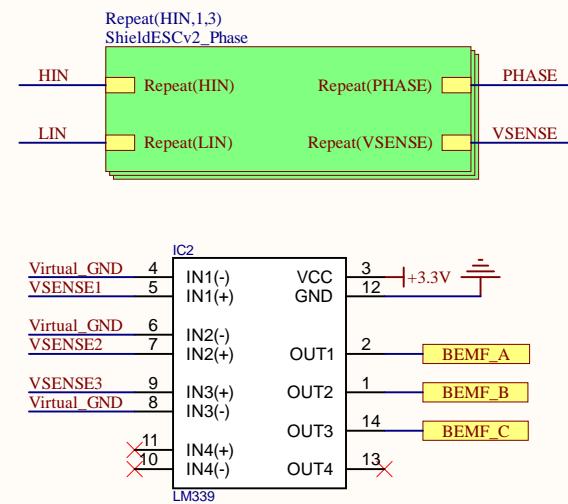
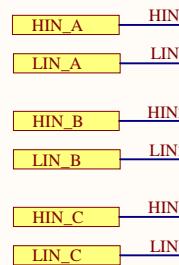
B

C

C

D

D



Title	ShieldESCV2_Control.SchDoc
Date	27/05/2025
Draw by	Yafahers Mendoza C.

A

A

B

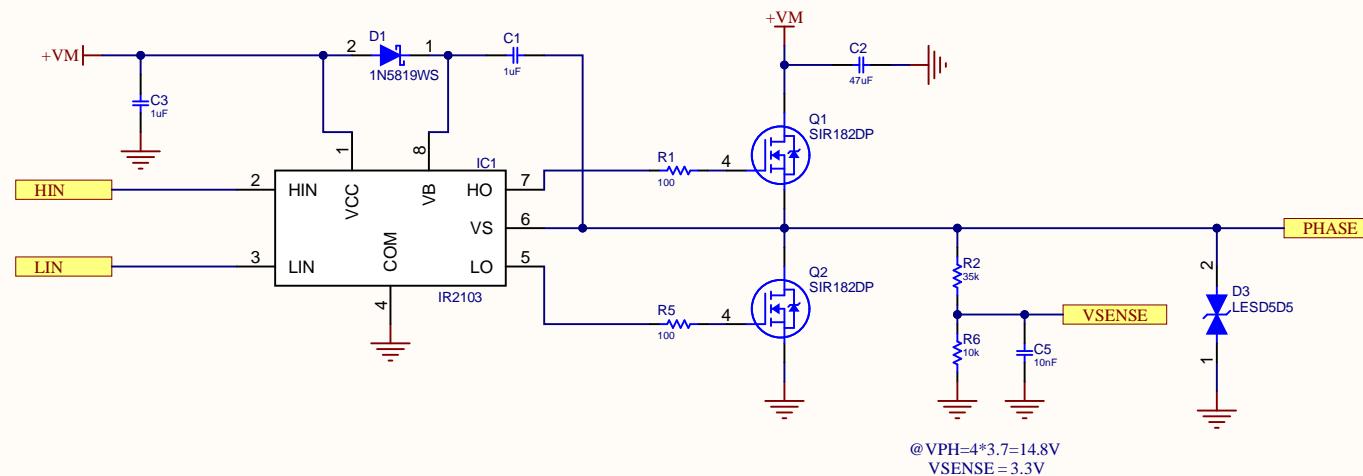
B

C

C

D

D



Title	ShieldESCv2_Phase.SchDoc
Date	27/05/2025
Draw by	Yafahers Mendoza C.

A4

Shield FPGA - Designed by Yafhers M.

