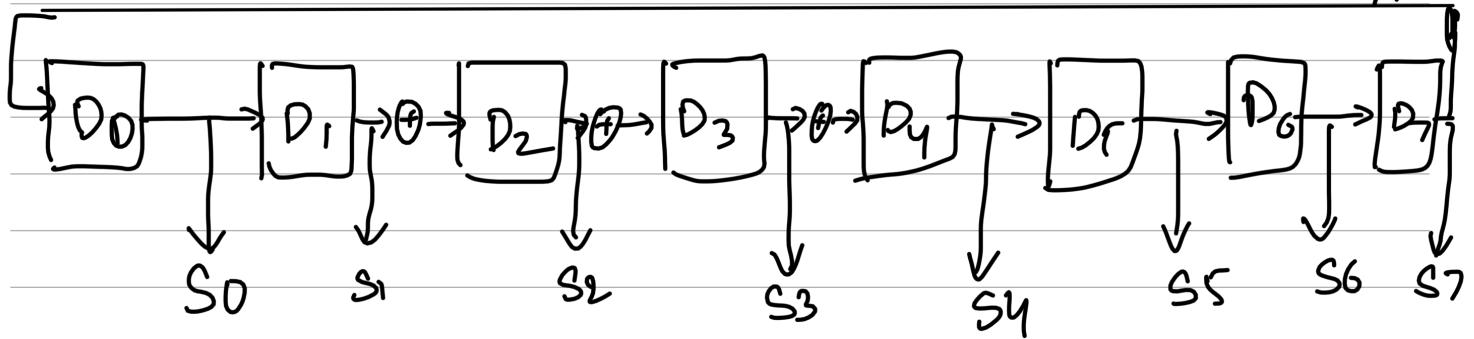


(Q2)

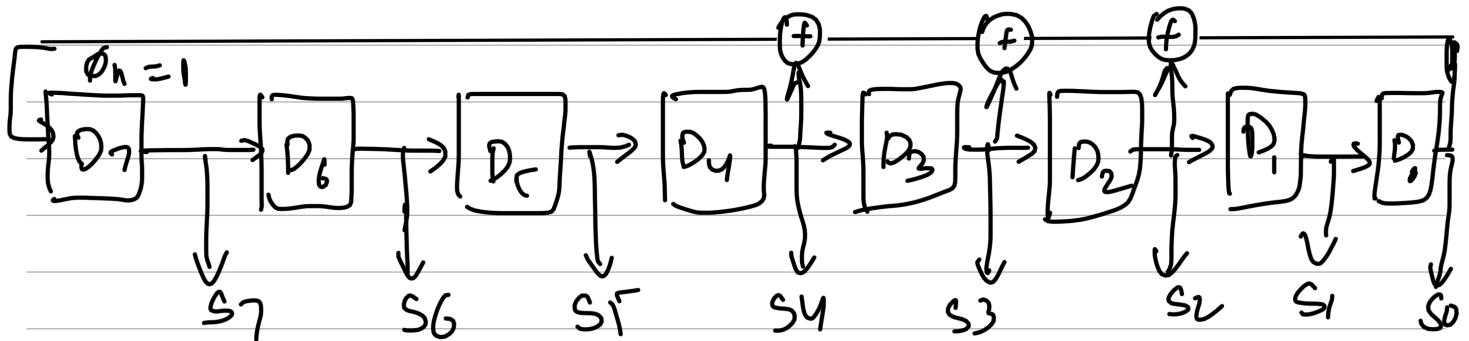
(i) 8-bit design for polynomial : $x^8 + x^4 + x^3 + x^2 + 1$

(c) Internal XOR LFSR.

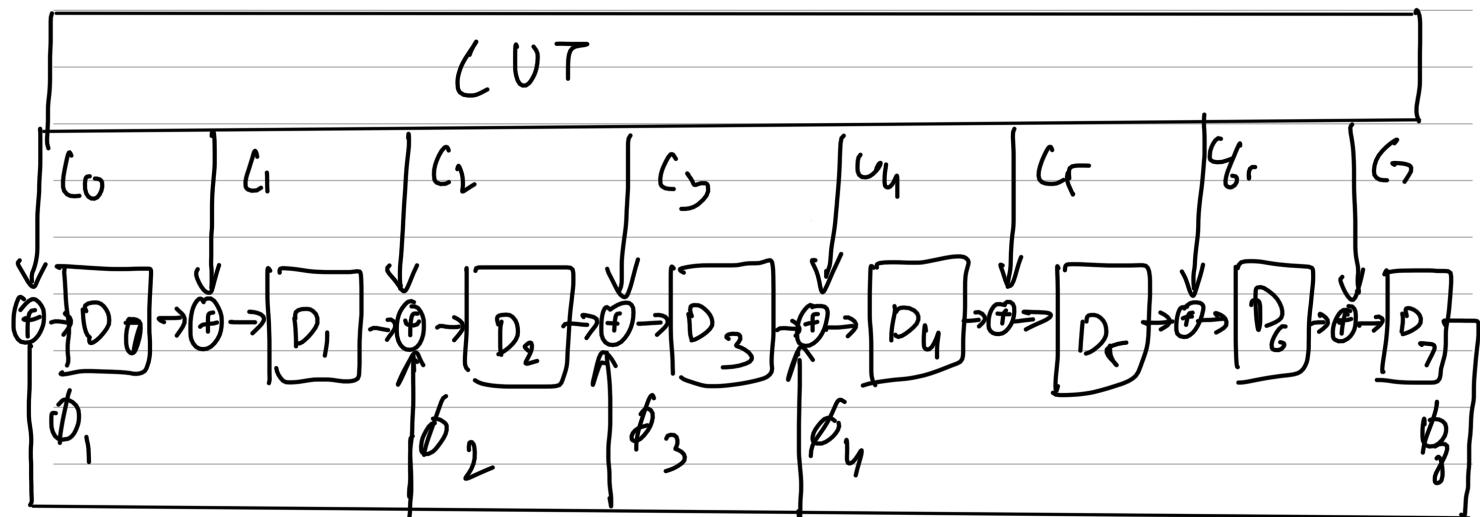
$$\phi_1 = 1$$



(ii) External XOR LFSR.



(iii) MISR



$C_0, C_1, C_2 \dots C_8$ are the inputs of XOR which are outputs of the circuit under test.

Table:

	Delay	Cost
Internal XOR LFSR	Crates are placed depending on the feedback or polynomial	Slightly higher than External XOR LFSR
External XOR LFSR.	XOR gates are its feedback polynomial	higher than Internal XOR LFSR.
MISR	Same as the Internal XOR LFSR.	less expensive

(i) External - XOR LFSR.

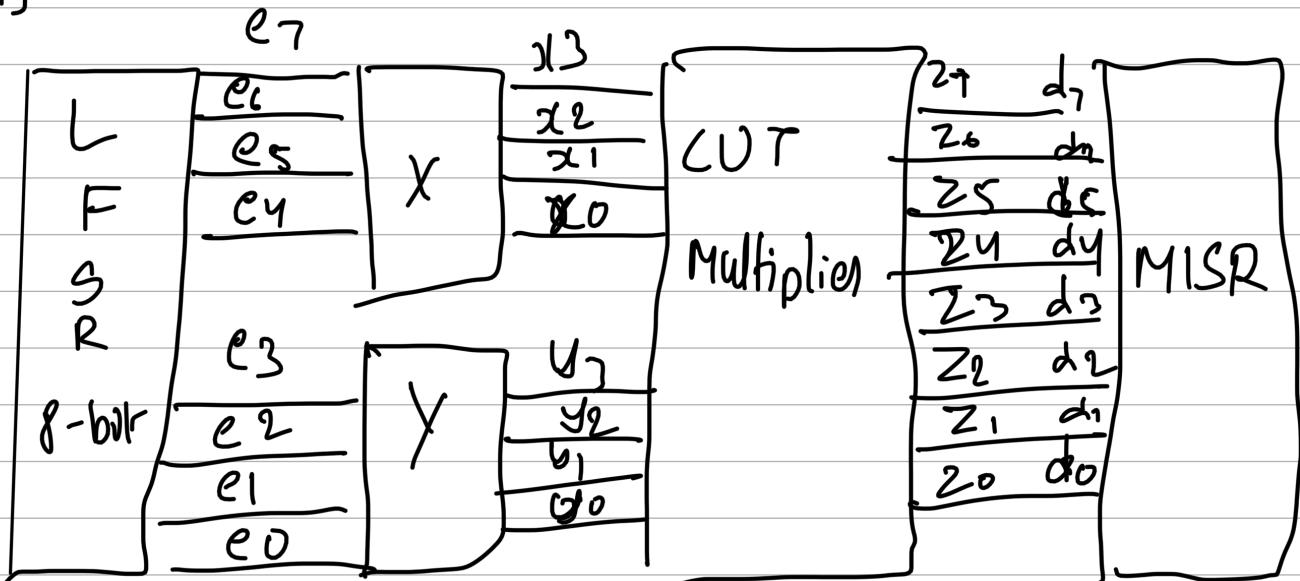
$$\begin{bmatrix} S_0(t+1) \\ S_1(t+1) \\ S_2(t+1) \\ S_3(t+1) \\ S_4(t+1) \\ S_5(t+1) \\ S_6(t+1) \\ S_7(t+1) \end{bmatrix} = \begin{bmatrix} 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} S_0(t) \\ S_1(t) \\ S_2(t) \\ S_3(t) \\ S_4(t) \\ S_5(t) \\ S_6(t) \\ S_7(t) \end{bmatrix}$$

Initial value = 1111111

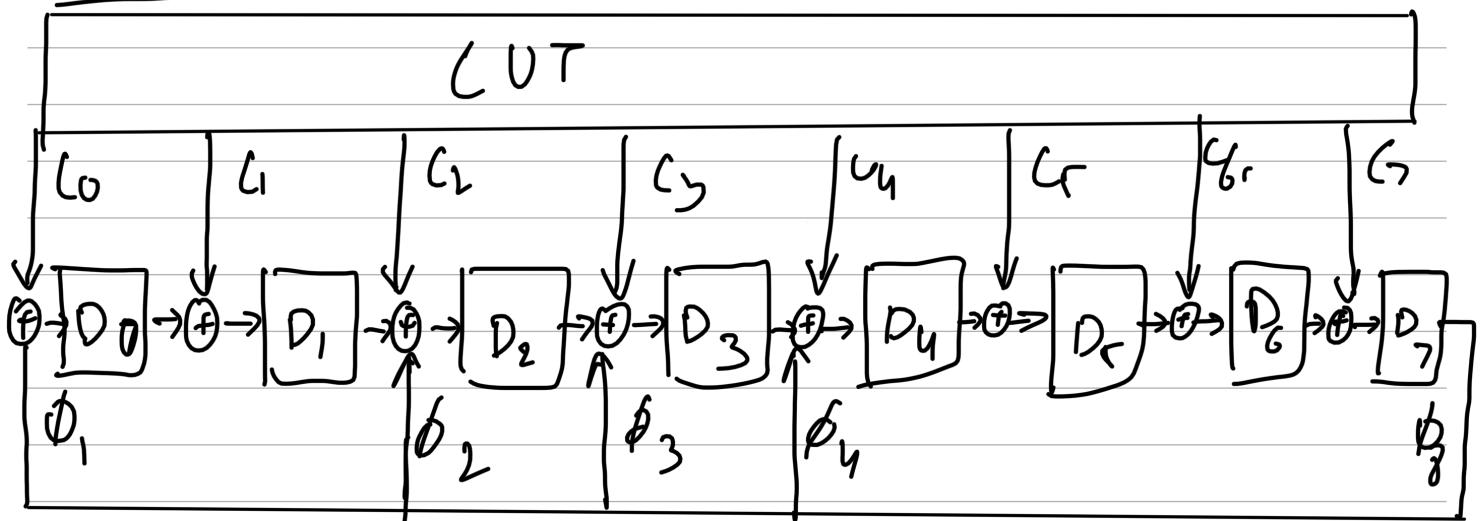
Cycle =

1	1	1	1	1	1	1	0	0	0	0	0	0	1	0	1	1	1	1
1	1	1	1	1	1	1	0	0	0	0	0	1	0	1	1	1	1	1
1	1	1	1	1	1	0	0	0	1	0	1	0	1	1	1	1	0	0
1	1	1	1	1	0	0	0	0	1	0	1	1	1	1	1	0	0	0
1	1	1	1	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0
1	1	1	0	0	0	0	1	0	1	1	1	1	0	0	0	0	1	1
1	1	0	0	0	0	1	0	1	1	1	1	0	0	0	0	1	1	1
1	0	0	0	0	1	0	1	1	1	0	0	0	0	1	1	1	1	0
1	0	0	0	1	0	1	1	0	0	0	0	1	1	0	1	1	0	0

(iii)



MISR :



Initial seed : 0000 0000

To find z₇ z₆ z₅ z₄ z₃ z₂ z₁ z₀

$$\begin{array}{r} \textcircled{1} \quad 0111 \\ \underline{1111} \\ 0111 \\ 0111 \\ 0111 \\ \hline 0101101 \end{array}$$

$$\begin{array}{r} \textcircled{2} \quad 0011 \\ \underline{1111} \\ 0011 \\ 0011 \\ 0011 \\ \hline 0010001 \end{array}$$

$$\begin{array}{r} \textcircled{3} \quad 0001 \\ \underline{1111} \\ 0001 \\ 0001 \\ 0001 \\ \hline 0001111 \end{array}$$

$$\begin{array}{r} \textcircled{4} \quad 0000 \\ \underline{1111} \\ 0000 \\ 0000 \\ 0000 \\ \hline 00000000 \end{array}$$

$$\begin{array}{r} \textcircled{5} \quad 1000 \\ \underline{0111} \\ 1000 \\ 1000 \\ 1000 \\ \hline 0111000 \end{array}$$

$$\begin{array}{r} \textcircled{6} \quad 0100 \\ \underline{0011} \\ 0100 \\ 0100 \\ 0000 \\ \hline 0001100 \end{array}$$

$$\begin{array}{r} \textcircled{7} \quad 1010 \\ \underline{0001} \\ 1010 \\ 0000 \\ 0000 \\ 0000 \\ \hline 0001010 \end{array}$$

$$\begin{array}{r} \textcircled{8} \quad 1101 \\ \underline{0000} \\ 0000 \\ 0000 \\ 0000 \\ \hline 0000 \end{array}$$

$$\begin{array}{r} \textcircled{9} \quad 1110 \\ \underline{1000} \\ 0000 \\ 0000 \\ 0000 \\ \hline 1110 \\ \hline 110000 \end{array}$$

$$\begin{array}{r} \textcircled{10} \quad 1111 \\ \underline{0100} \\ 0000 \\ 0000 \\ 1111 \\ 0000 \\ \hline 011100 \end{array}$$

$$\begin{array}{r} \textcircled{11} \quad 0111 \\ \underline{1010} \\ 0000 \\ 0000 \\ 0111 \\ 0111 \\ \hline 0110110 \end{array}$$

$$\begin{array}{r} \textcircled{12} \quad 0011 \\ \underline{1101} \\ 0011 \\ 0011 \\ 0011 \\ \hline 0010111 \end{array}$$

$$\textcircled{13} \quad \begin{array}{r} 0001 \\ 1110 \\ \hline 0000 \\ 0001 \\ 0001 \\ \hline 0001110 \end{array}$$

$$\textcircled{14} \quad \begin{array}{r} 1000 \\ 1111 \\ \hline 1000 \\ 1000 \\ 1000 \\ \hline 111000 \end{array}$$

$$\textcircled{15} \quad \begin{array}{r} 1100 \\ 0111 \\ \hline 1100 \\ 1100 \\ 1100 \\ \hline 0100100 \end{array}$$

$$\textcircled{16} \quad \begin{array}{r} 0110 \\ 0011 \\ \hline 0110 \\ 0110 \\ 0000 \\ \hline 0001010 \end{array}$$

$$\left[\begin{array}{c} z_0 \\ z_1 \\ z_2 \\ z_3 \\ z_4 \\ z_5 \\ z_6 \\ z_7 \end{array} \right] = \left[\begin{array}{cccccccccccccccc} \textcircled{1} & \textcircled{2} & \textcircled{3} & \textcircled{4} & \textcircled{5} & \textcircled{6} & \textcircled{7} & \textcircled{8} & \textcircled{9} & \textcircled{10} & \textcircled{11} & \textcircled{12} & \textcircled{13} & \textcircled{14} & \textcircled{15} & \textcircled{16} \end{array} \right]$$

20	1	1	1	0	0	0	0	0	0	0	1	0	0	0	0	0
z_1	0	0	1	0	0	0	1	0	0	0	1	1	1	0	0	1
z_2	1	0	1	0	0	1	0	0	0	1	1	1	1	0	1	0
z_3	1	0	1	0	1	1	1	0	0	0	0	1	0	1	0	1
z_4	0	1	0	0	1	0	0	0	1	1	1	0	1	0	1	0
z_5	1	0	0	0	1	0	0	0	1	1	1	0	0	1	1	0
z_6	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0
z_7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

d₀ 000010000000000 1110

d₁ 100111000100001000

d₂ 0101110001001010

d₃ 10110010011101010

d₄ 0010111000100100

d₅ 011001110001000010

d₆ 001000010000000000

d₇ 000000000000000000

D 010001101000111101001000

$$\text{Equivalent seq} \Rightarrow x^{20} + x^{17} + x^{15} + x^{14} + x^{13} + x^{12} + x^8 \\ x^6 + x^5 + x$$

$$\text{given seq} = x^8 + x^4 + x^3 + x^2 + 1$$

$$\begin{array}{r} x^{12} + x^9 + x^8 + x^2 + x \\ \hline x^{20} + x^{17} + x^{15} + x^{14} + x^{13} + x^{12} + x^8 \\ \hline x^6 + x^5 + x \\ x^{20} + x^{16} + x^{18} + x^{14} + x^{12} \\ \hline x^{17} + x^{16} + x^{13} + x^8 + x^6 + x^5 + x \\ x^4 + x^3 \\ \hline x^{16} + x^{12} + x^{11} + x^9 + x^8 + x^6 + x^5 + x \\ x^{16} + x^{12} + x^{11} + x^{10} + x^8 \\ \hline x^{10} + x^9 + x^6 + x^5 + x \\ x^{10} + x^6 + x^5 + x^4 + x^2 \\ \hline x^9 + x^4 + x^2 + x \\ x^9 + x^4 + x^3 + x \\ \hline x^7 + x^8 + x^3 + x^2 \end{array}$$

\therefore the final signature of M1SR (8-bit) is
 $x^7 + x^5 + x^3 + x^2$