$example HSPICE setup file

$transistor model

.include "/proj/cad/library/mosis/GF65_LPe/cmos10lpe_CDS_oa_dl064_11_20160415/models/YI-SM00030/Hspice/models/design.inc"

.include "INV.pex.netlist"

.option post runlvl=5

xi GND! OUT VDD! IN inv

vdd VDD! GND! 1.2v

vin IN GND! pwl(1ns 0v 0.75ns 1.2v 1.2ns 1.2v 1.275ns 1.2v 6ns 1.2v 6.075ns 0v 10ns 0v 10.075ns 1.2v 15ns 1.2v 15.075ns 0v 18ns 0v 18.075 1.2v 20ns 1.2v)

cout OUT GND! 70f

$transient analysis

.tr 100ps 20ns

$example of parameter sweep, replace numeric value W of pfet with WP in invlvs.sp

$.tr 100ps 12ns sweep WP 1u 9u 0.5u