Micro-Processor and Embedded Systems Lab-Session 6 Report for MCU

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Date: 30th September 2022

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AIM: week 5 and week 6 we must create an MCU.

Update about the project:

we have implemented a MCU with all the modules in this week. But did could not complete the test bench. Need additional time to complete the project.

Summary:

1. Using Nomachine access the VIVADO By sourcing **/proj/cad/startup/profile.xilinx_vivado_18.3.**
2. Command to open the tool is **vivado&**
3. Create a new project on the software for ALU and registers.
4. ALU and register file Verilog codes along with the test benches have been complied successfully.
5. Ran the behavioral simulation for the test bench codes.

MCU:

A microcontroller (MCU for microcontroller unit) is a small computer on a single VLSI integrated circuit (IC) chip

Conclusion:

We were able to successfully create the MCU. I have to implement a testbench for the created MCU.