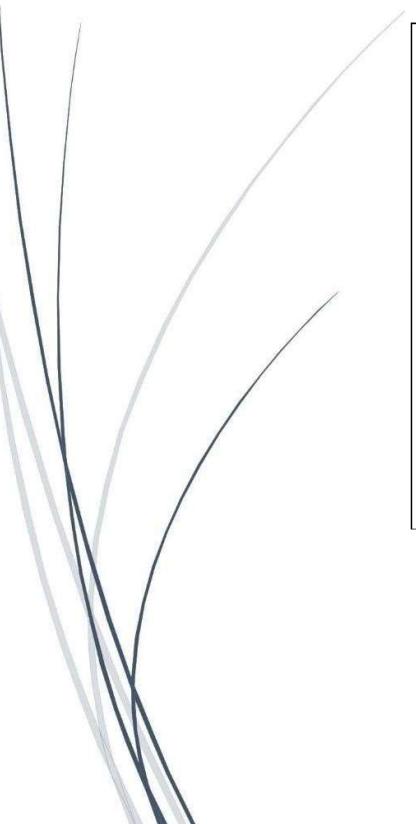




10/26/2022

IMPLEMENTATION OF CELL LIBRARY



Annadata, Yagna Srinivasa Harsha

yxa210024

Duvvuri, Sai Pravallika

psd220003

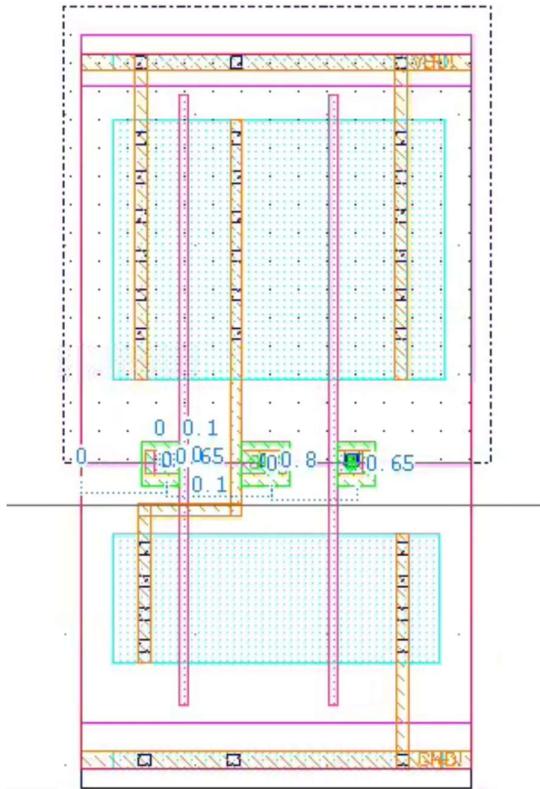
Thota, Sai Abhishek

sxt210056

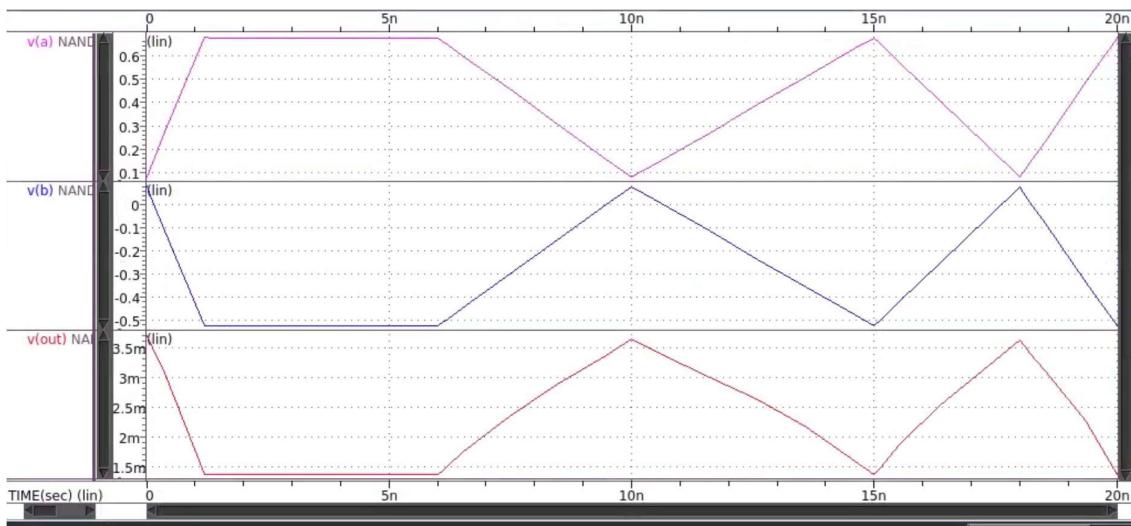
Cell Layout of Various Cells

NAND2

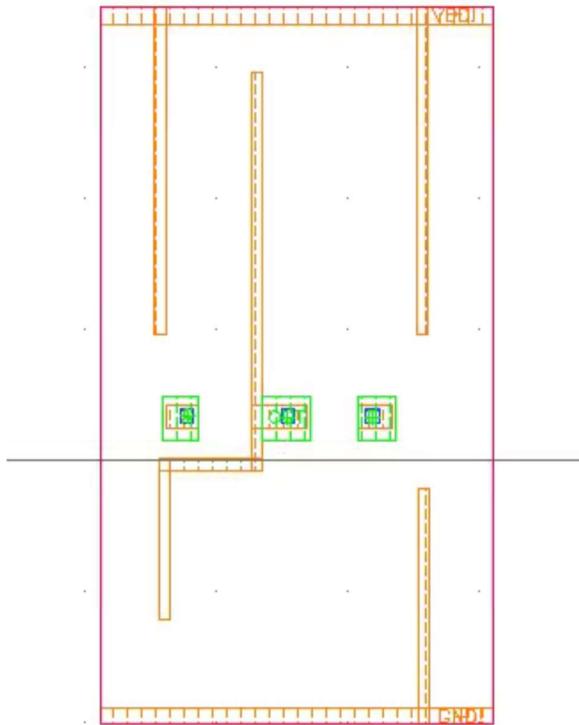
Layout



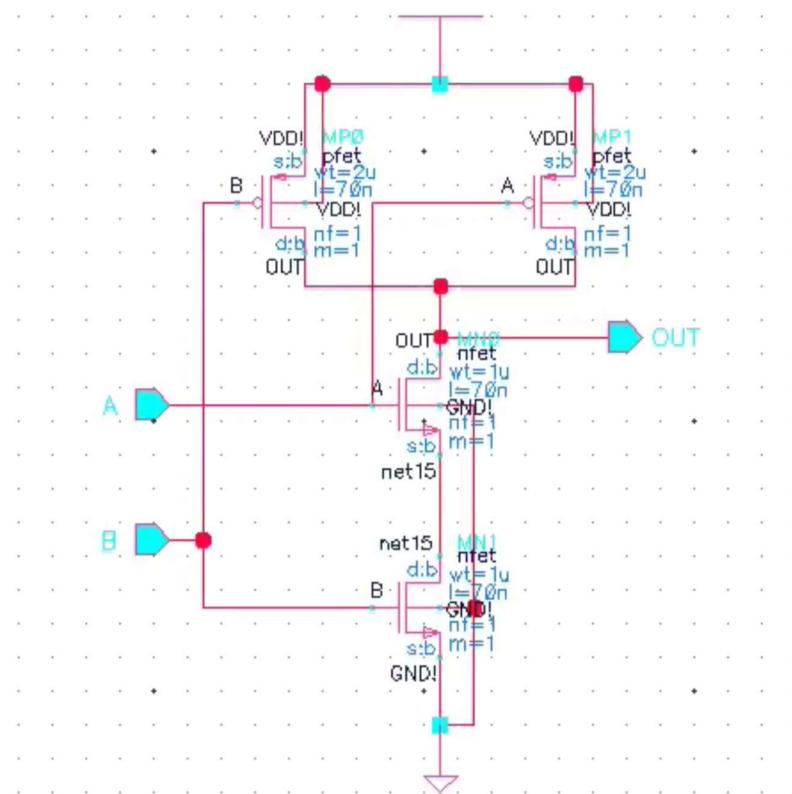
Simulation



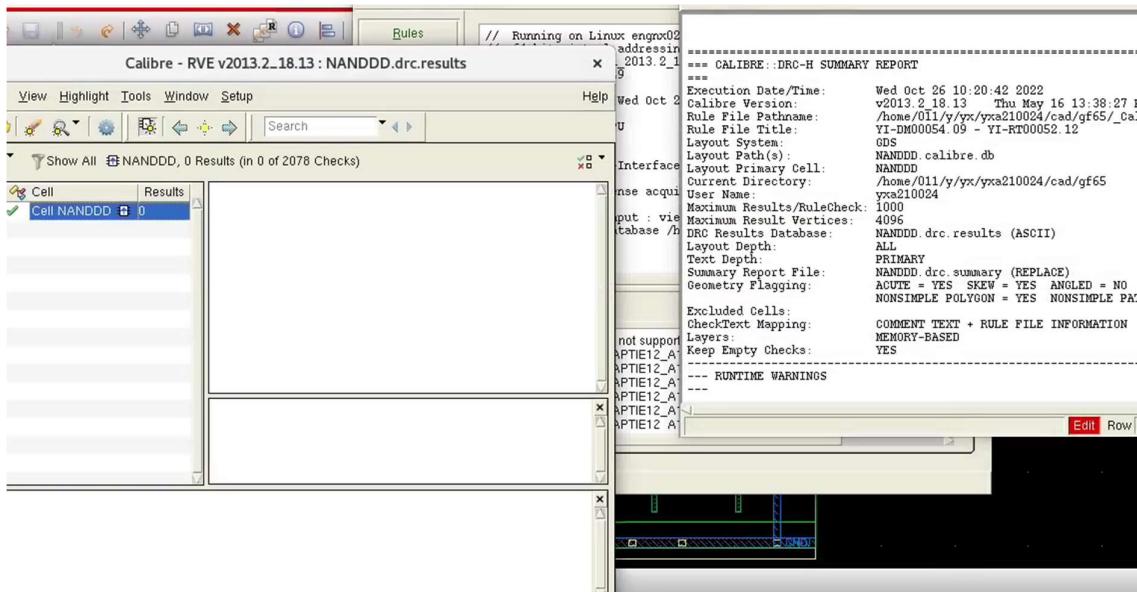
Abstract



Schematic

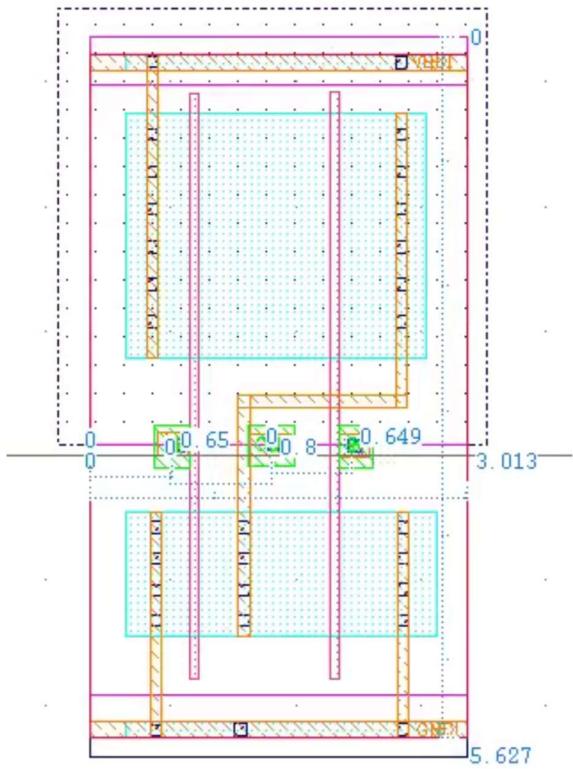


DRC Check

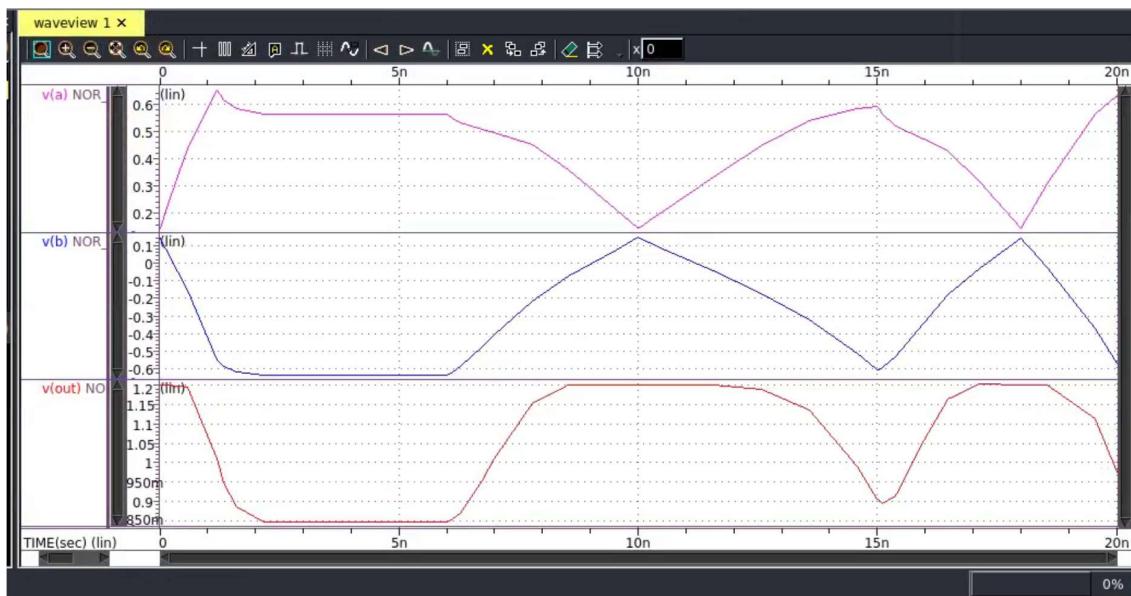


NOR2

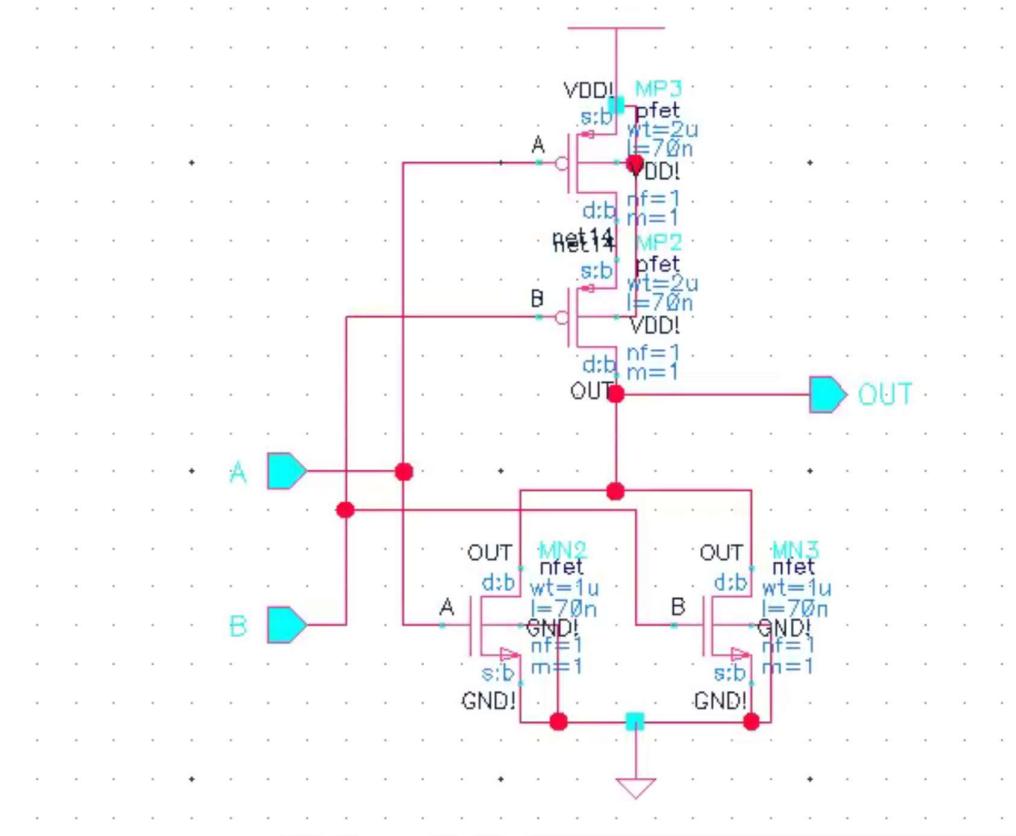
Layout



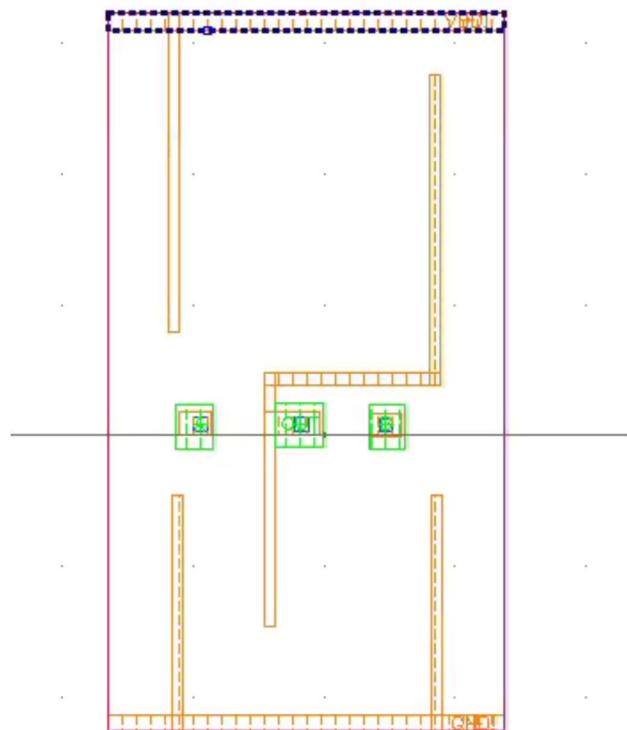
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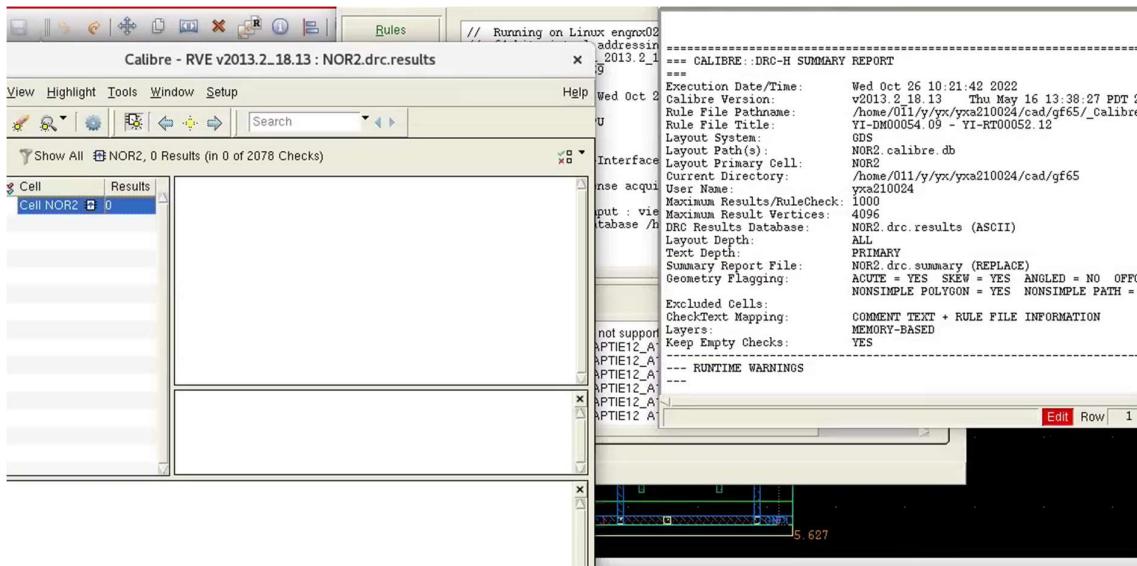
Schematic



Abstract

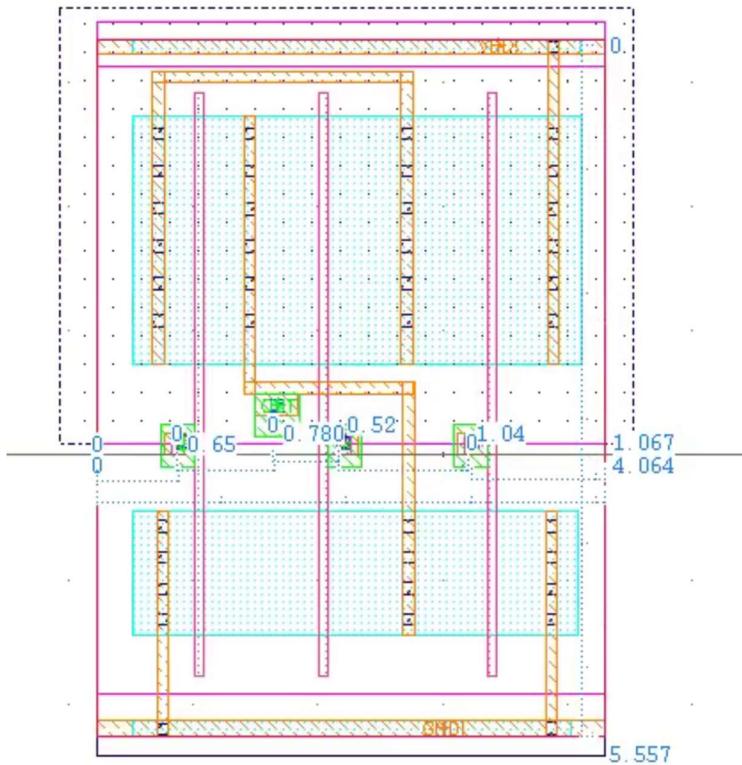


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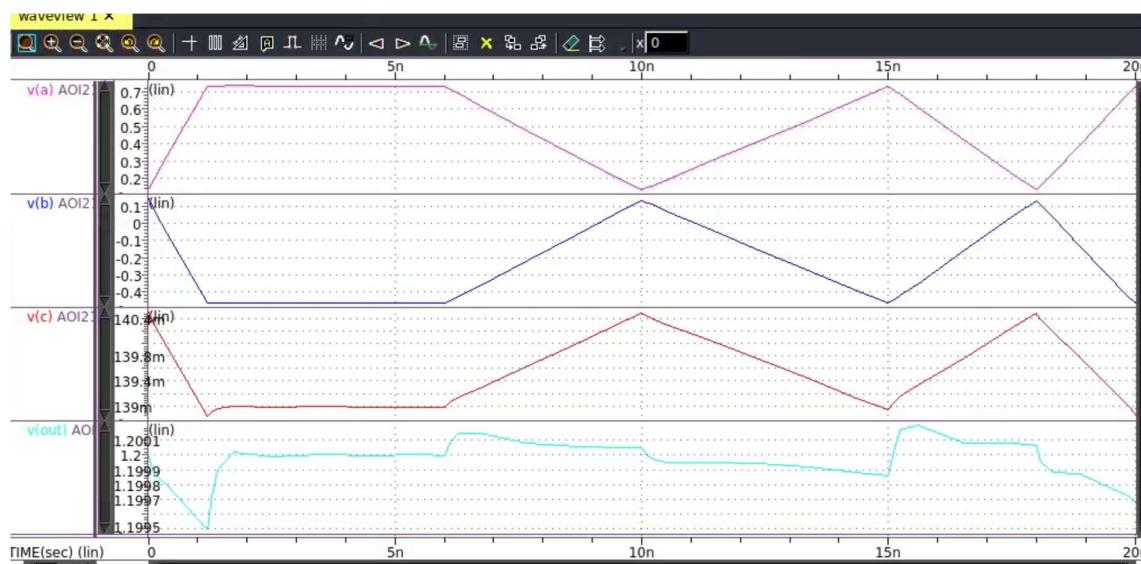


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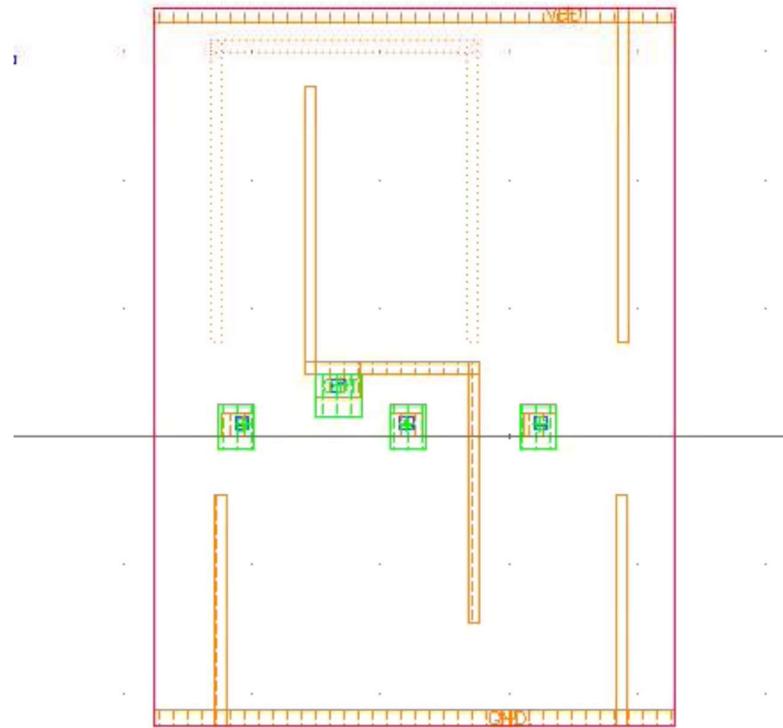
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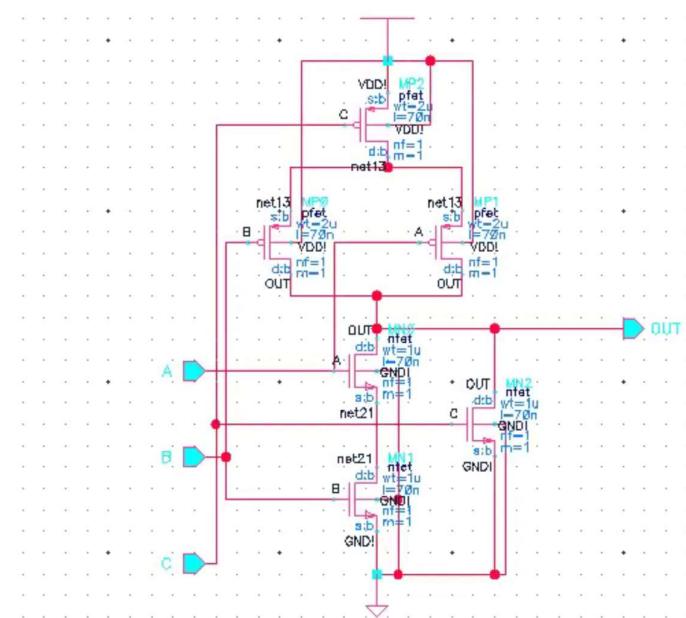
Simulation



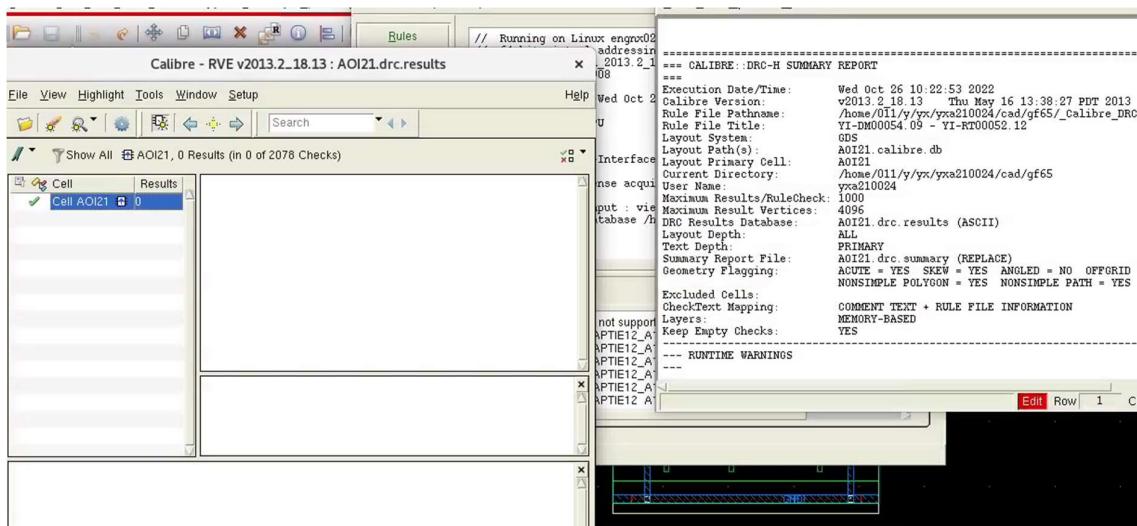
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Schematic

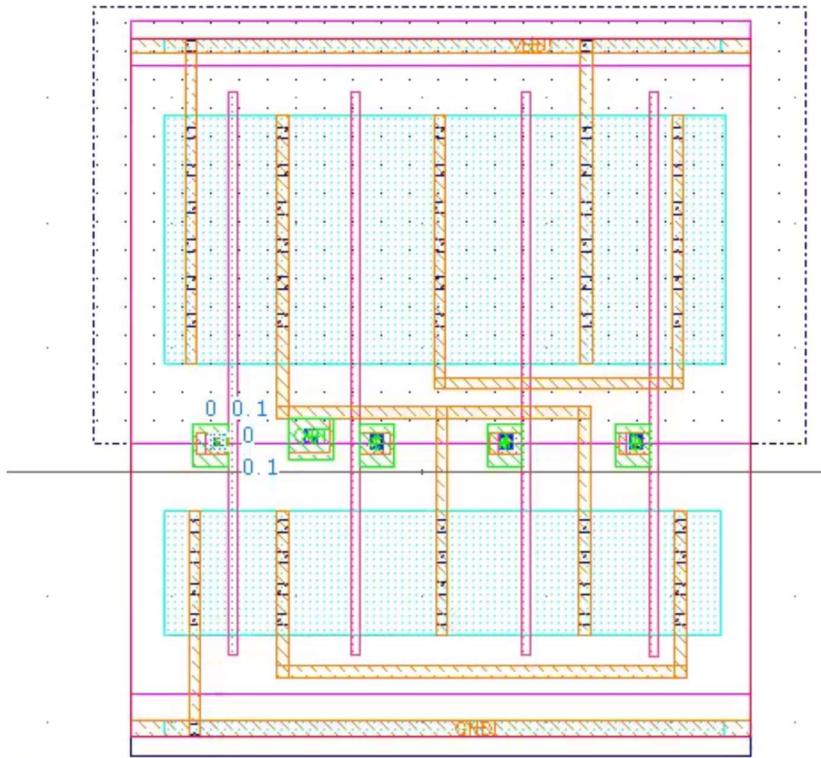


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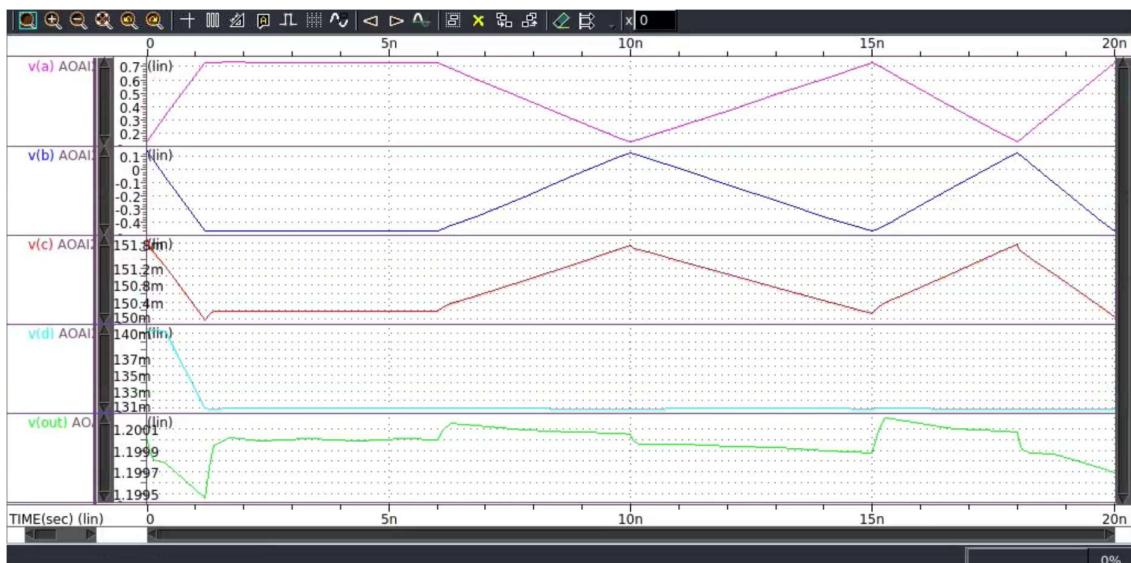


AOAI211

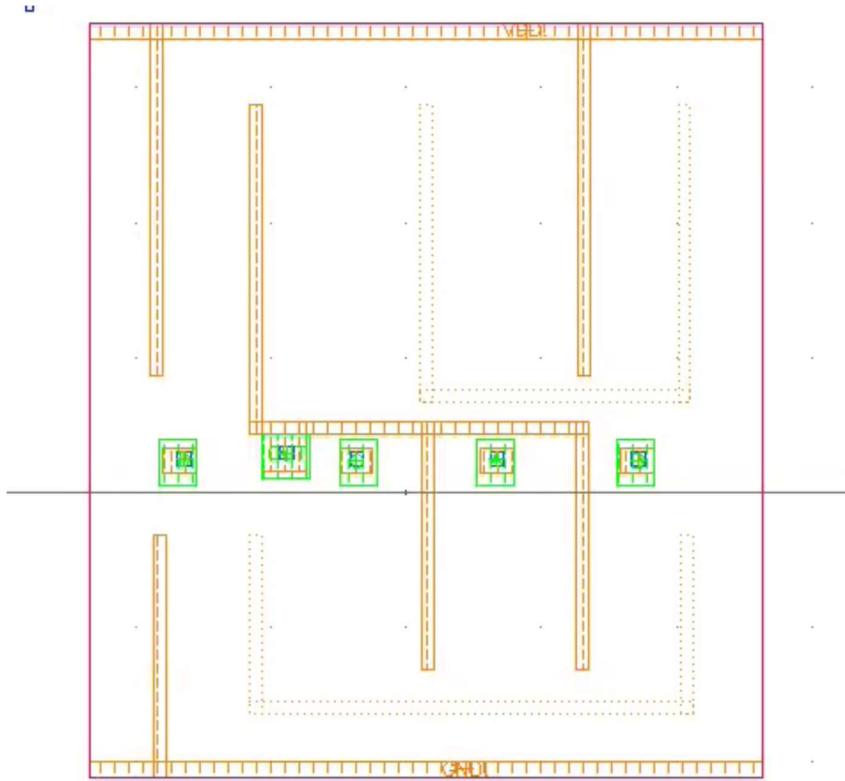
Layout



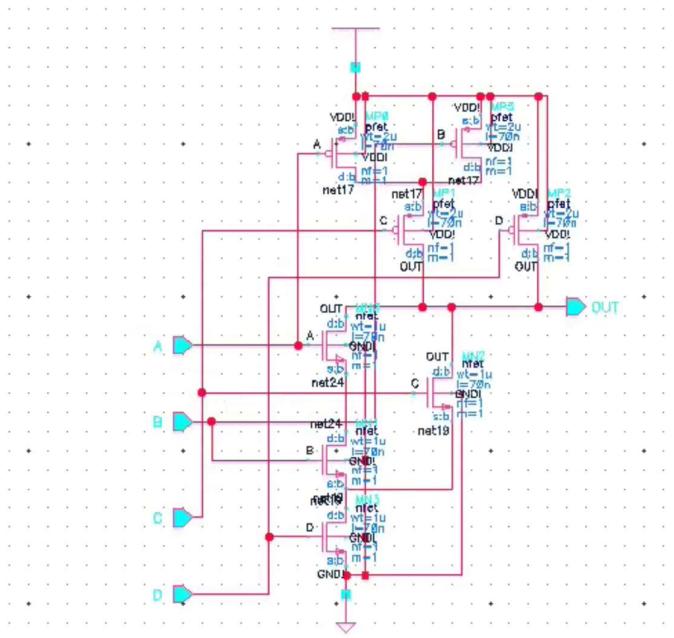
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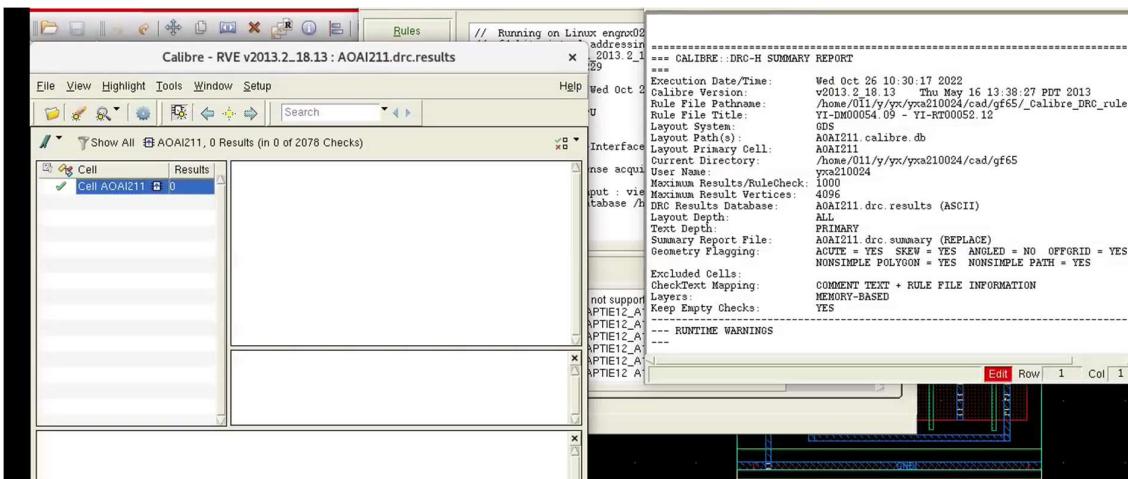
Abstract



Schematic

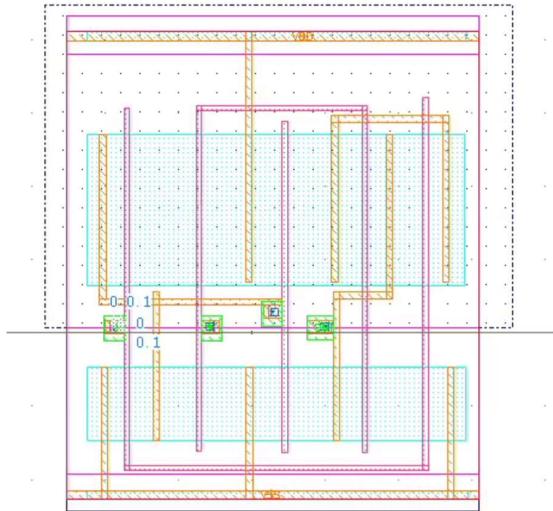


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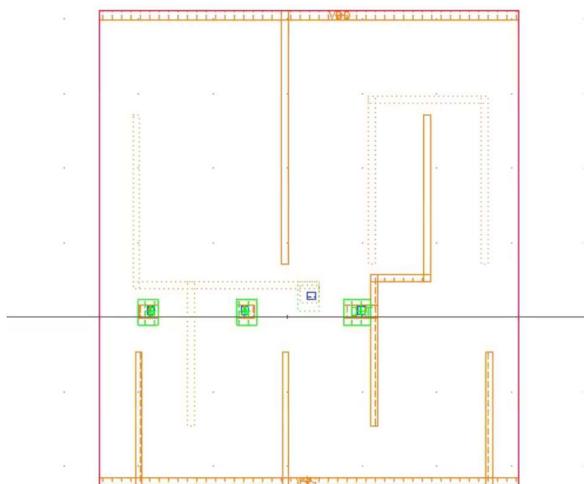


XOR

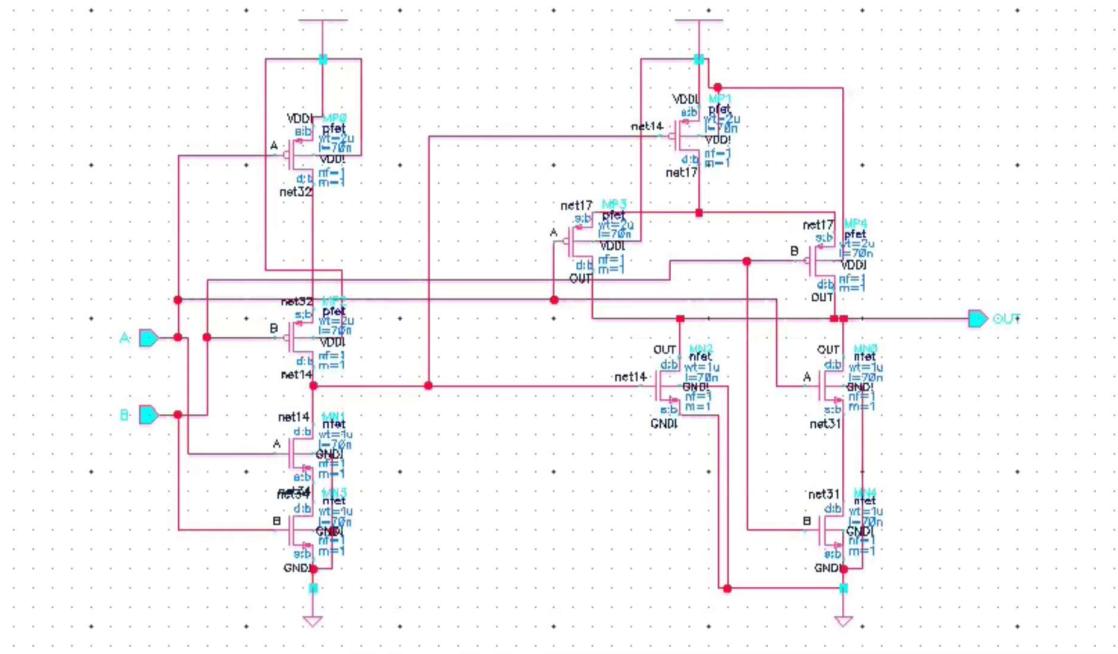
Layout



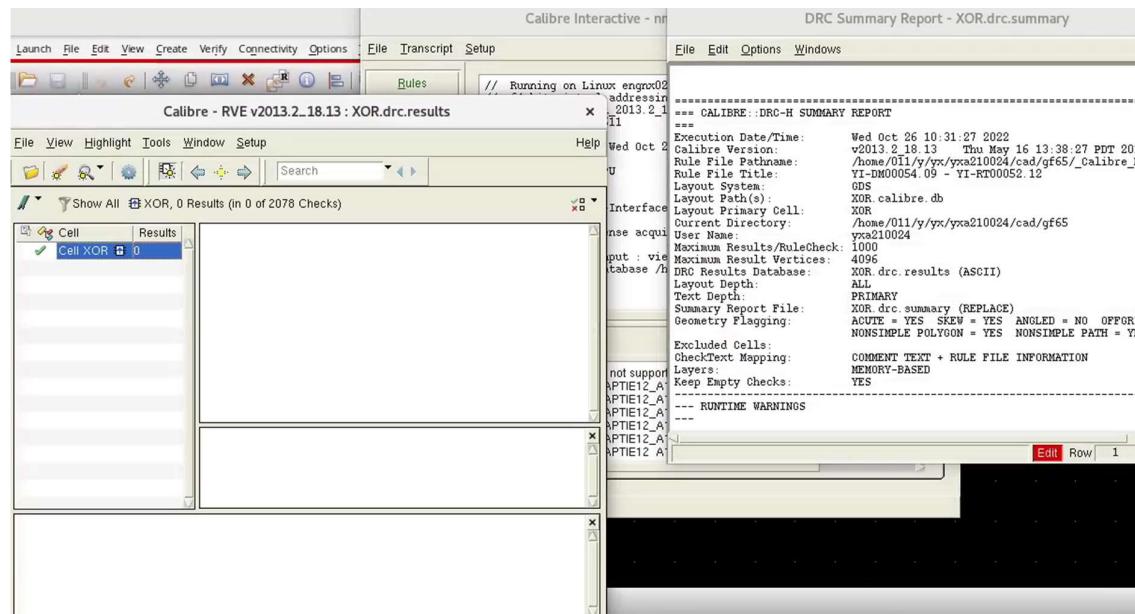
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Schematic

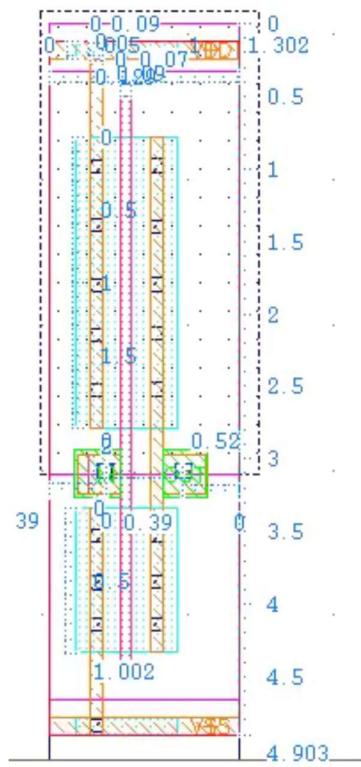


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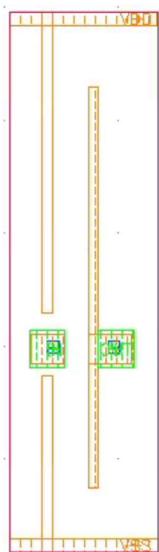


INV

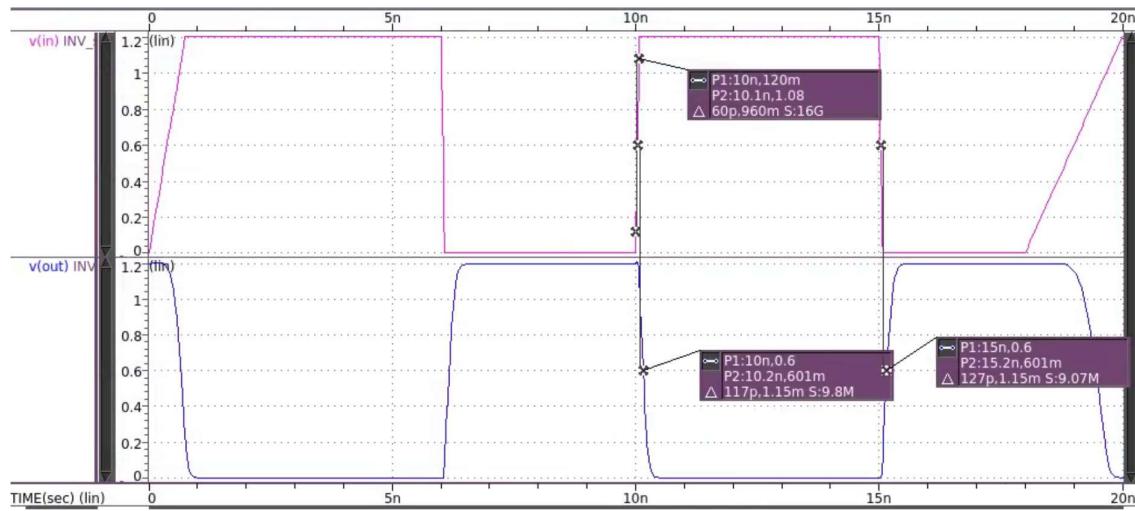
Layout



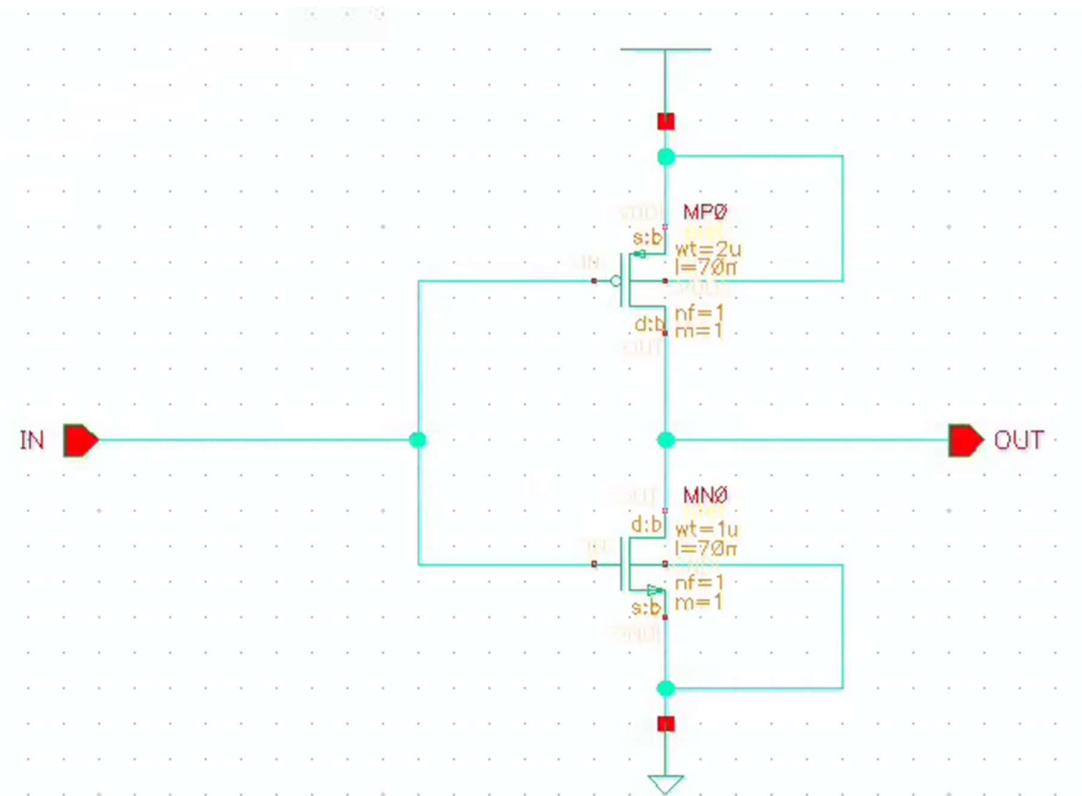
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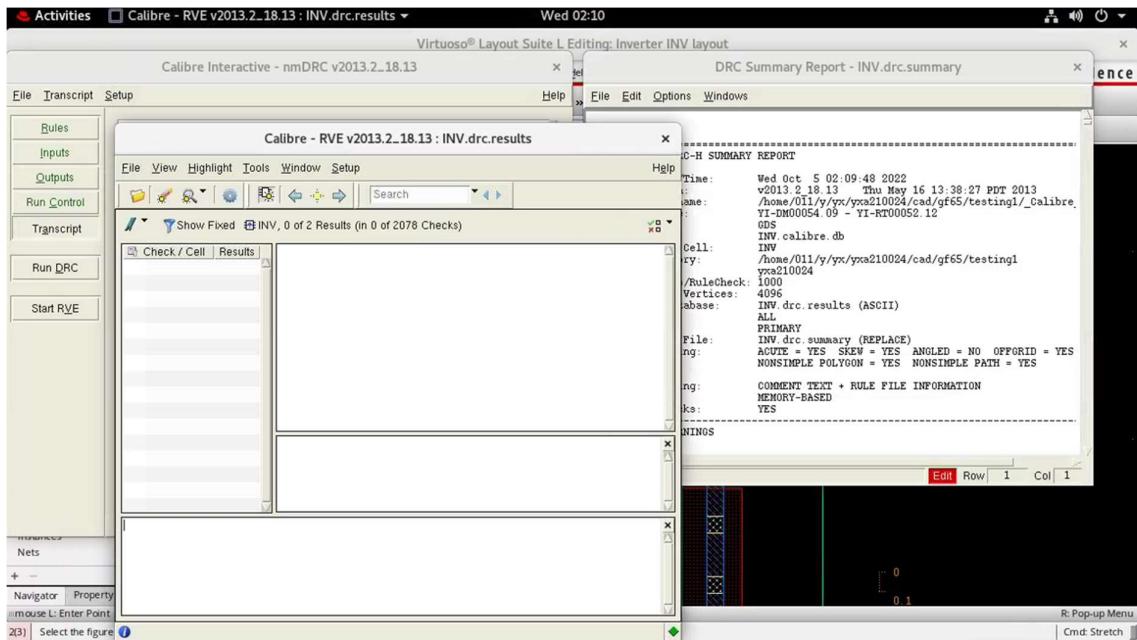
Simulation



Schematic

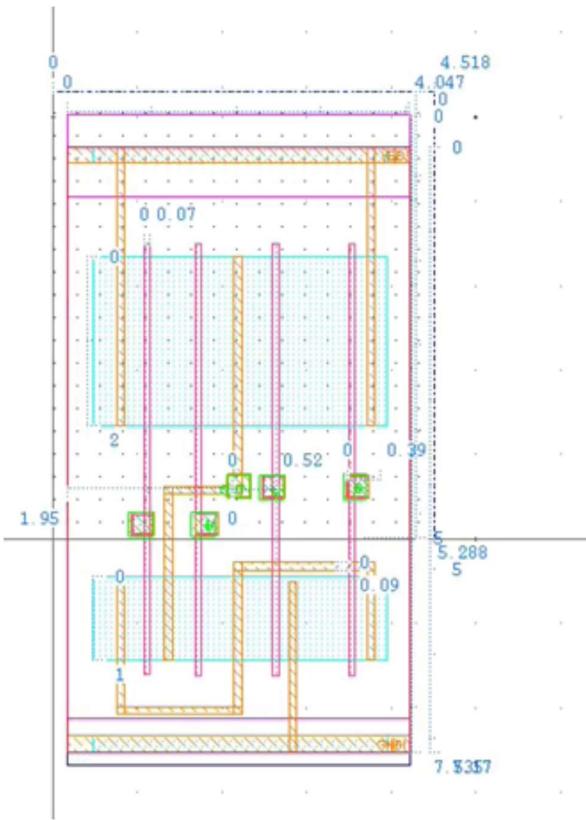


DRC Check



OAII22

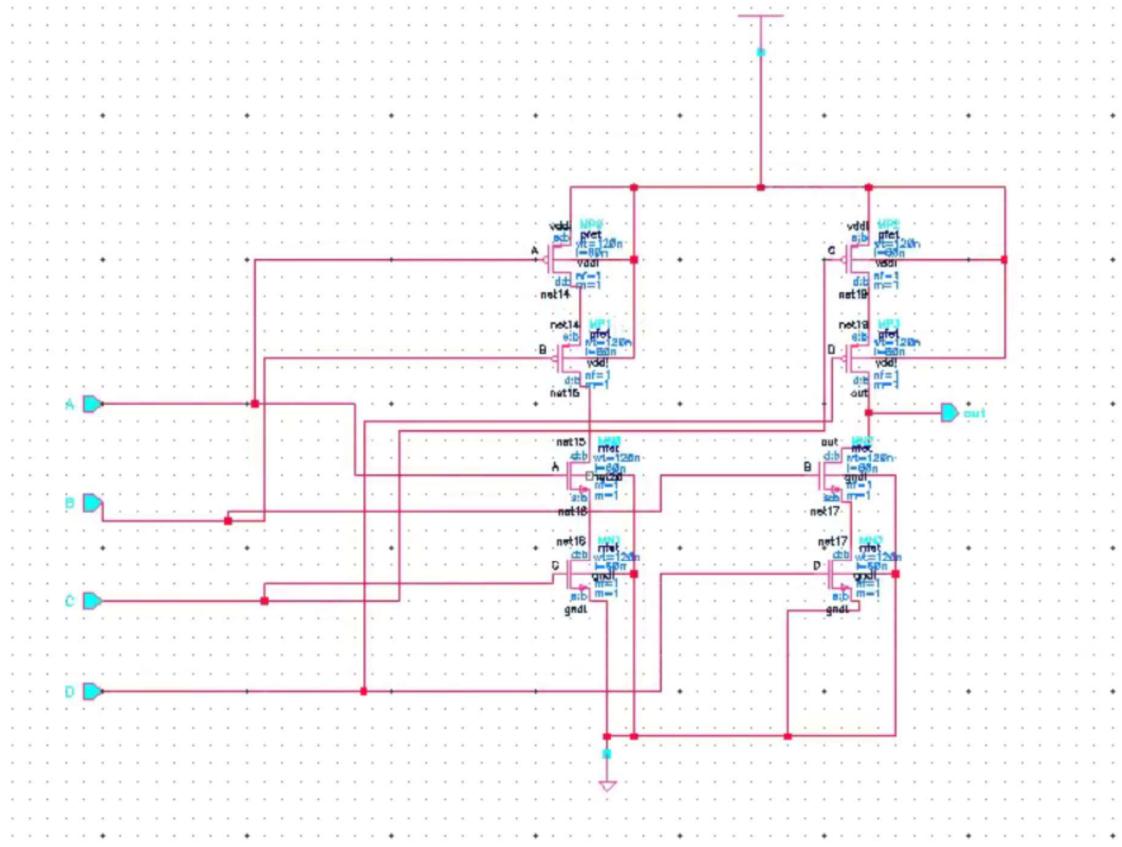
Layout



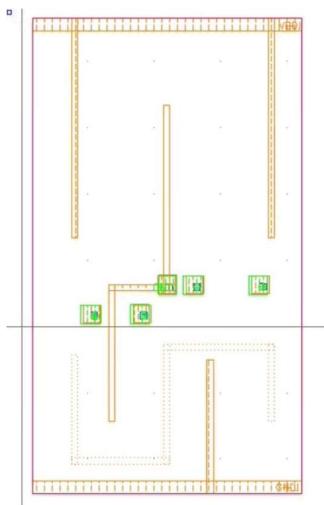
DRC Check

Show All	OAII22, 0 Results (in 0 of 2079 Checks)
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✓ Check GRB_BF03	0
✓ Check GRB_BF04	0
✓ Check GRB_BH0	0
✓ Check GRB_BH02	0
✓ Check GRB_BH02	0
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✓ Check GRB_BH04	0
✓ Check GRB_DEV	0
✓ Check GRB_DE02	0
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✓ Check GRB_JN02	0
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Schematic

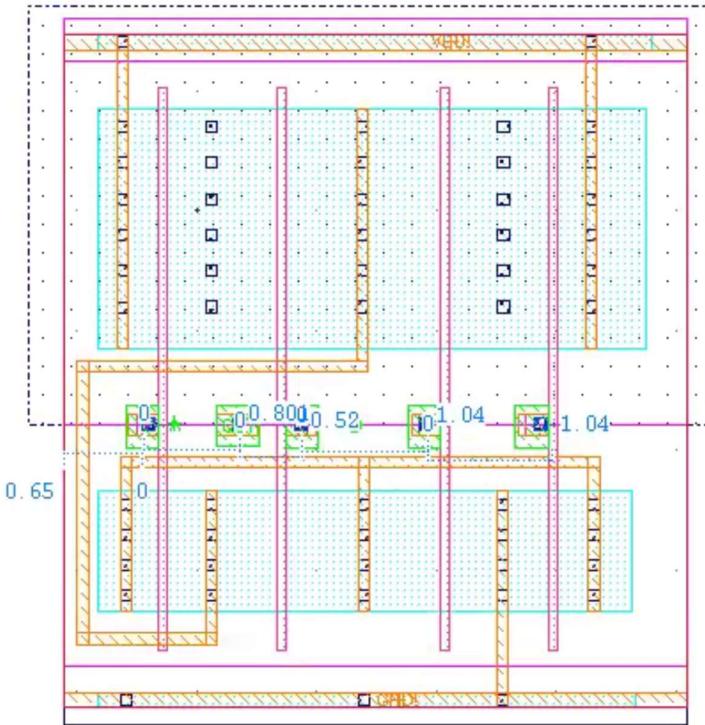


Abstract

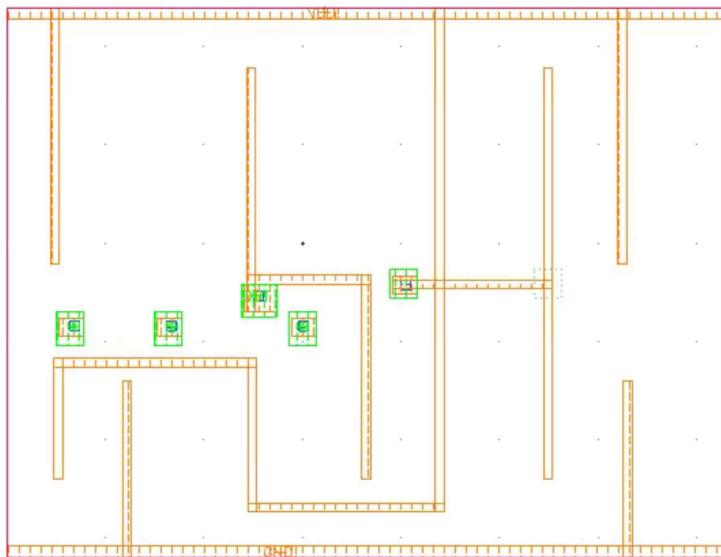


MUX 2:1

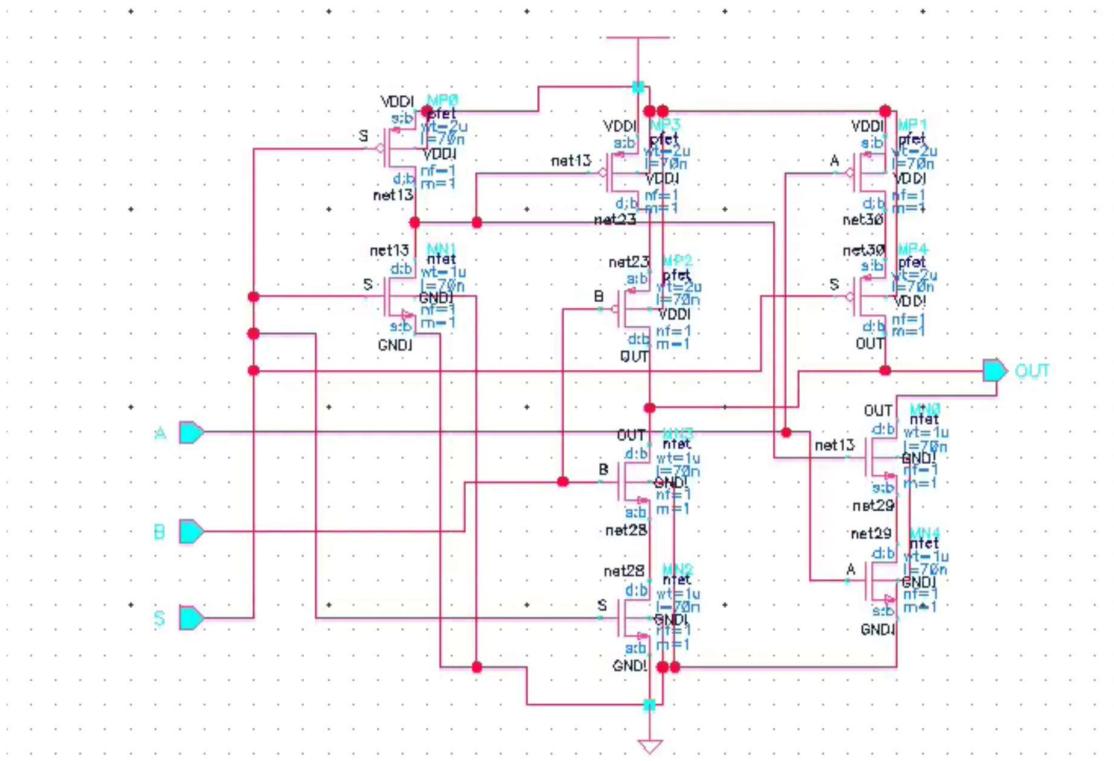
Layout



Abstract



Schematic



DRC Check

