



# Jetson Orin Nano Series, Jetson Nano, and Jetson TX2 NX Interface Comparison and Migration

Application Note

# Document History

DA-11084-001\_v1.0

Version	Date	Description of Change
0.1	August 26, 2022	Preliminary release – Advance information (Subject to change)
1.0	April 17, 2023	<ul style="list-style-type: none"><li>• Updated Figure 1: Highlighted PWM count to magenta text color.</li><li>• Figure 2 and Figure 3: Updated PWM count to 4x and highlighted text color to Magenta.</li></ul> Updated Table 1: <ul style="list-style-type: none"><li>&gt; Jetson Nano col   Video Decode row</li><li>&gt; Storage and SDIO/SD Card rows for Jetson Orin Nano 4GB and 8GB from Not applicable to Not Supported</li><li>&gt; Storage row for Jetson Orin Nano 4GB and 8GB columns</li><li>&gt; Video Decode row for Jetson Nano</li><li>&gt; CAN row Jetson Nano col from Not applicable to Not Supported</li><li>&gt; Added PWM</li><li>&gt; Updated row Power for Jetson Orin Nano 4GB columns</li><li>&gt; Updated row Input Voltage for Jetson Orin Nano 4GB and 8GB columns</li></ul> <ul style="list-style-type: none"><li>• Updated Table 2 and Table 3.</li><li>• Table 4: Updated VDD_IN.</li><li>• Added Section: UPHY Mapping and tables under Section: Interface Migration.</li><li>• Added Note to Section: PCIe Mapping Options.</li></ul>

# Table of Contents

Introduction .....	1
Jetson Orin Nano Series vs. Jetson Nano and Jetson TX2 NX .....	2
Module Interface Comparisons .....	4
Function and Interface Difference Details .....	6
Module Power .....	6
Input Voltage Rails .....	7
Mechanical Differences .....	7
Interface Migration .....	10
UPHY Mapping .....	10
USB 3.x Mapping Options .....	11
PCIe Mapping Options .....	11
Ethernet .....	12
SDIO and SD Card .....	12
HDMI and DisplayPort .....	12
CSI .....	13
Audio .....	14
I2C .....	15
SPI .....	15
UART .....	16
Debug .....	16
Connector Pin Differences Details .....	17

## List of Figures

Figure 1.	Jetson Nano Block Diagram .....	2
Figure 2.	Jetson TX2 NX Block Diagram .....	3
Figure 3.	Jetson Orin Nano Series Block Diagram .....	3
Figure 4.	Module 3D Envelope Top Views with Thermal Mounting Hole Dimensions .....	8
Figure 5.	Module 3D Envelope Bottom Views .....	9

## List of Tables

Table 1.	Jetson Orin Nano Series, Jetson Nano, and Jetson TX2 NX Feature Comparison...	4
Table 2.	Jetson Nano vs. Jetson Orin Nano 4GB Module Power Requirements .....	6
Table 3.	Jetson TX2 NX vs. Jetson Orin Nano Module Power Requirements .....	6
Table 4.	Input Voltage Rails .....	7
Table 5.	Mechanical Differences .....	7
Table 6.	UPHY0 Mapping Options (USB 3.2 and PCIe) .....	10
Table 7.	UPHY2 Mapping Options (PCIe) .....	10
Table 8.	USB 3.x Mapping Options .....	11
Table 9.	PCIe Mapping Options .....	11
Table 10.	HDMI and DP Mapping Options .....	12
Table 11.	Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano Audio Interfaces .....	14
Table 12.	Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano I2C Interfaces .....	15
Table 13.	Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano SPI Interfaces .....	15
Table 14.	Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano UART Interfaces .....	16
Table 15.	Connector Pin Function Differences .....	17

---

# Introduction

This application note compares the features and interfaces supported on the NVIDIA® Jetson™ Orin Nano series, Jetson Nano™, and Jetson TX2 NX modules. This application note also describes the migration path for designers familiar with either Jetson Nano or Jetson TX2 NX to design a carrier board for the Jetson Orin Nano series. This will support the features available on Jetson Orin Nano series modules.



Note: References to Jetson Orin Nano applies to Jetson Orin Nano 8GB and Jetson Orin Nano 4GB except where explicitly noted.

# Jetson Orin Nano Series vs. Jetson Nano and Jetson TX2 NX

Jetson Orin Nano series and both Jetson Nano and Jetson TX2 NX series are largely pin compatible. This section describes the differences between these modules.

Figure 1 and Figure 2 show the Jetson Orin Nano series, Jetson Nano, and Jetson TX2 NX series block diagrams. The interfaces or blocks that are supported only by one of the modules is highlighted in red. The interface types that are supported on both modules but where the number of lanes and instances, voltage level, or access is different are highlighted in magenta.

Figure 1. Jetson Nano Block Diagram

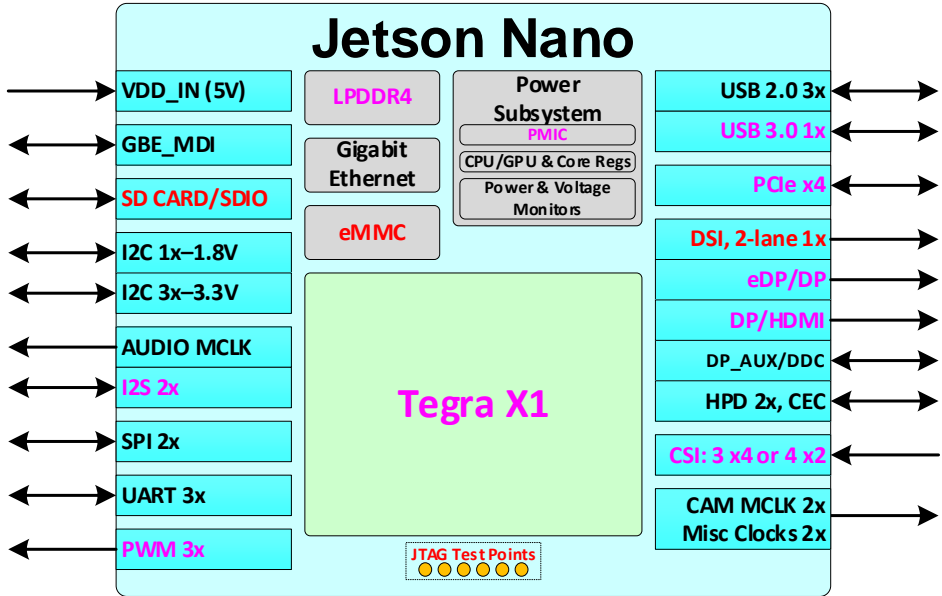


Figure 2. Jetson TX2 NX Block Diagram

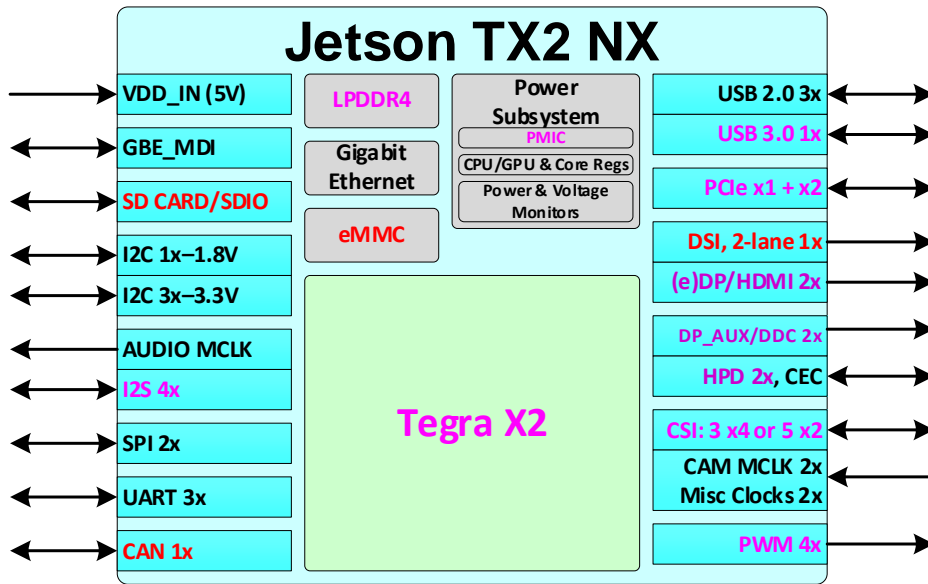
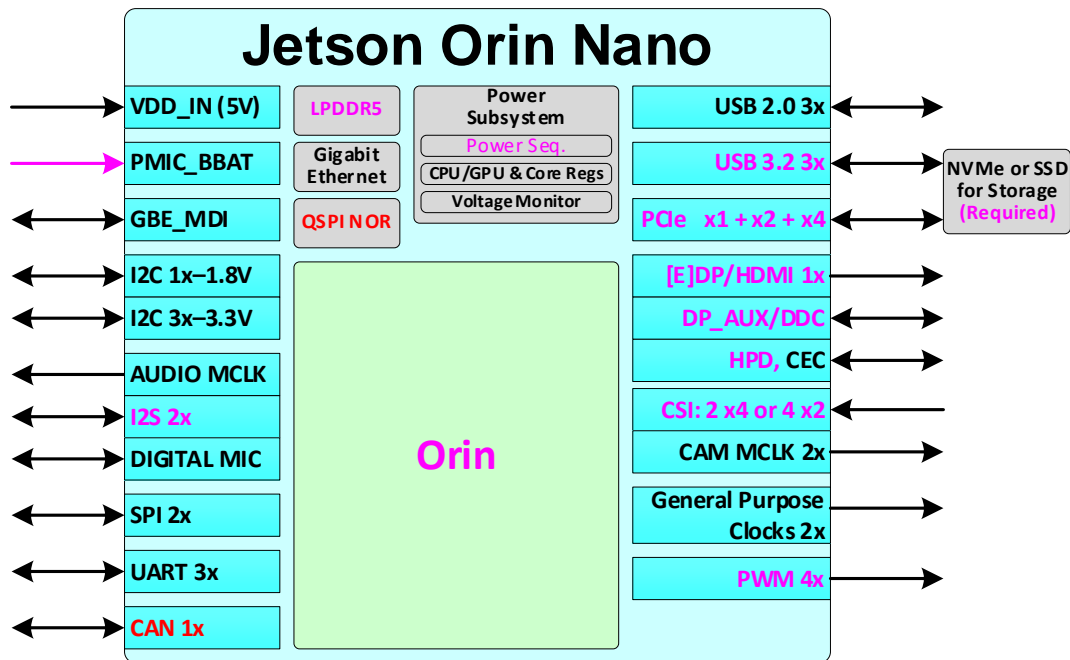


Figure 3. Jetson Orin Nano Series Block Diagram



# Module Interface Comparisons

Table 1 lists the key system specifications, devices, and interfaces that are supported on the Jetson Orin Nano series, Jetson TX2 NX, and Jetson Nano modules.

Table 1. Jetson Orin Nano Series, Jetson Nano, and Jetson TX2 NX Feature Comparison

Feature	Jetson Nano	Jetson TX2 NX	Jetson Orin Nano 4GB	Jetson Orin Nano 8GB
System Specifications and Device on the Module				
AI Performance	0.5 TFLOPS (Dense)	1.3 TFLOPS (Dense)	20 TOPS (Sparse) 10 TOPS (Dense)	40 TOPS (Sparse) 20 TOPS (Dense)
GPU	Maxwell, 128 CUDA Cores, 921 MHz	Pascal, 256 CUDA Cores, 1300 MHz	Ampere, 512 CUDA Cores, 16 Tensor Cores, 625 MHz	Ampere, 1024 CUDA Cores, 48 Tensor Cores, 625 MHz
CPU SPEC int 2k6 SPEC int rage	4X A57, 1.5 GHz 9 16	4X A57 + 2X D15, 2.0 GHz 14 27	6X A78, 1.5 GHz 25 106	
Memory	4GB, 25 GB/s	4GB, 51 GB/s	4GB, 34 GB/s	8GB, 68 GB/s
Storage	16 GB eMMC		External, NVMe using PCIe or SSD on USB 3.2	
Networking	10/100/1000 Mbit			
Video Decode	<b>H.265/H.264</b> 1x4K60 2x4K30 4x1080p60 8x1080p30	<b>H.265/H.264</b> 2x4K60 4x4K30 7x1080p60 14x1080p30	<b>H.265</b> 1x4K60 2x4K30 5x1080p60 11x1080p30 <b>H.264</b> 1x4K30 3x1080p60 7x1080p30	<b>AV1</b> 1x4K60 2x4K30 5x1080p60 10x1080p30 <b>VP9</b> 10x1080p30



Feature	Jetson Nano	Jetson TX2 NX	Jetson Orin Nano 4GB	Jetson Orin Nano 8GB
Video Encode	<b>H.265/H.264</b> 1x4K30 2x1080p60 4x1080p30	<b>H.265</b> 1x4K60 3x4K30 4x1080p60 8x1080p30 <b>H.264</b> 1x4K60 3x4K30 7x1080p60 14x1080p30	No NVENC. Video Encode supported by CPU	
Interfaces				
USB 2.0	3			
USB 3.x	1x USB 3.1		3x USB 3.2	
PCIe	1, x4 Gen2	1 x1 Gen2, 1 x2 Gen2	3 x1 Gen3, 1 x4 Gen3	
Display	2x 4K30	2x 4K60	1 multi-mode (4K30,2x1080p60), DP1.2 + MST, HDMI1.4, eDP 1.4	
Camera	12 lanes MIPI, CSI 2D PHY 1.1, (18 Gbps)	12 lanes MIPI, CSI 2D PHY 1.2, (30 Gbps)	8 lanes, MIPI CSI 2, DPHY 1.2, (20 Gbps), use GMSL for 8x cameras	
Audio (I2S)	2	4	2	
SDIO/SD Card	1x SD Card/SDIO		Not Supported	
Gigabit Ethernet	1x GbE			
I2C	4			
UART	3			
SPI	2			
CAN	Not Supported	1		
Fan	PWM and Tach			
PWM	3	4		
Miscellaneous				
RTC Back-up (PMIC_BBAT)	Input/Output, Supports rechargeable and non-rechargeable cells/super caps.		Input Only, Supports only non-rechargeable cells.	
Mechanical	70x45 mm, 260 pins			
Power	5 W   10 W	7 W   15 W	7 W   10 W	7 W   15 W
Input Voltage	5V			

---

# Function and Interface Difference Details

## Module Power

Table 2 and Table 3 list the module power requirement for the Jetson Nano, Jetson TX2 NX, and the Jetson Orin Nano series modules.

Table 2. Jetson Nano vs. Jetson Orin Nano 4GB Module Power Requirements

Description	Jetson Nano	Jetson Orin Nano 4GB
Thermal design power	5 W	7 W
	10 W	10 W

Table 3. Jetson TX2 NX vs. Jetson Orin Nano Module Power Requirements

Description	Jetson TX2 NX	Jetson Orin Nano 8GB
Thermal design power	7 W	7 W
	15 W	15 W

## Input Voltage Rails

Jetson Nano, Jetson TX2 NX, and Jetson Orin Nano series modules all have a single main input supply (VDD\_IN) plus a real-time clock (RTC) battery back-up supply input option (PMIC\_BBAT).

Table 4. Input Voltage Rails

Power Rail	Jetson Nano	Jetson TX2 NX	Jetson Orin Nano Series
VDD_IN	5V nominal		
RTC Back-up (PMIC_BBAT)	1.65V to 5.5V Input. 2.5 to 3.5V output for charging. Supports input for RTC back-up when system is off and output to charge a super capacitor or rechargeable cell. Output can be disabled to support non-rechargeable cells.		1.8V to 5.5V. Input only. Supports only non-rechargeable cells.

## Mechanical Differences

Jetson Nano, Jetson TX2 NX, and Jetson Orin Nano series modules have the same X/Y dimensions as can be seen in Table 5. There are four holes in the PCB for screws to pass through the thermal solution and the module to a metal bracket below the module. The thermal solution contacts the SoC (with thermal material placed between). The locations of the four holes are different for the Jetson Nano, Jetson TX2 NX, and Jetson Orin Nano series modules.

Table 5. Mechanical Differences

Feature	Jetson Nano	Jetson TX2 NX	Jetson Orin Nano Series
Size	69.5 mm × 45 mm		
Built-in thermal solution	None		
Thermal solution mounting (See the following figures for different mounting hole location dimensions).	Four holes in PCB for mounting thermal solution to module.	Same as Jetson Nano but mounting hole locations are different.	Same as Jetson Nano but mounting hole locations are different.

Figure 4. Module 3D Envelope Top Views with Thermal Mounting Hole Dimensions

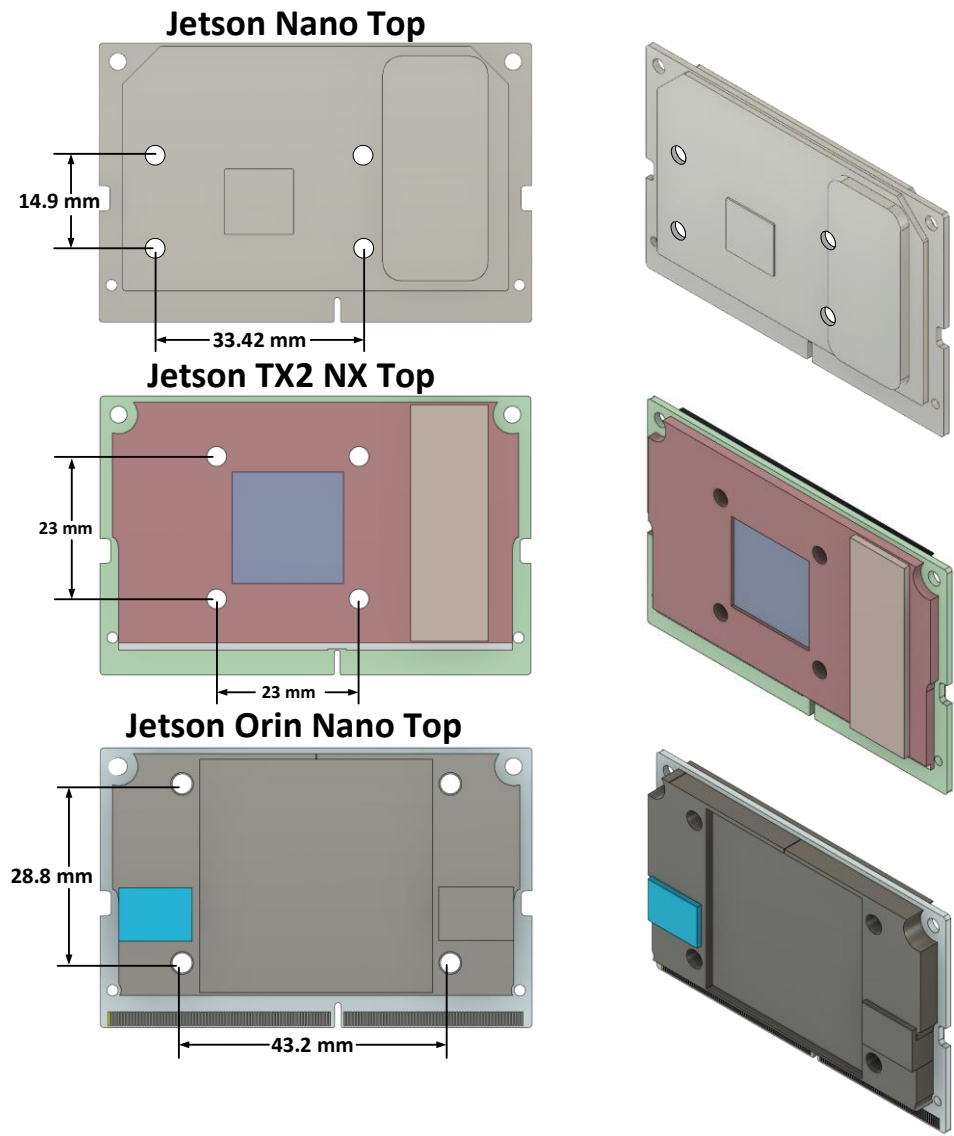
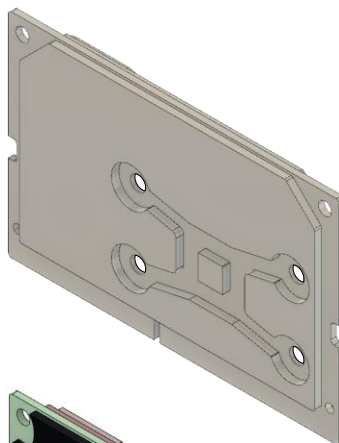
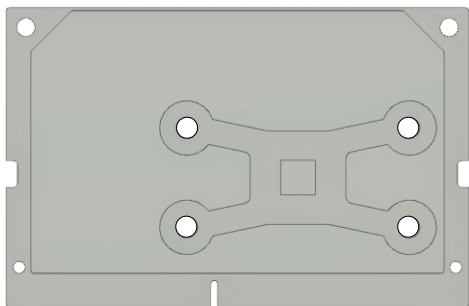
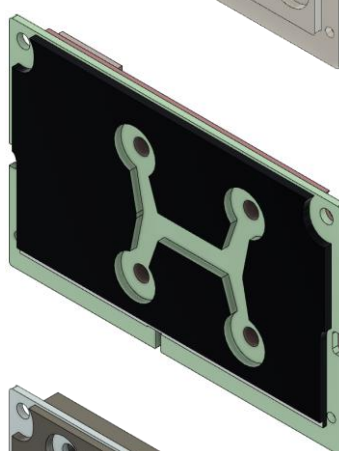
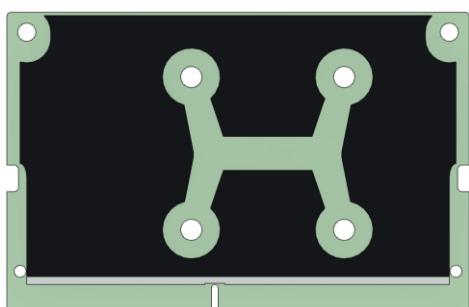
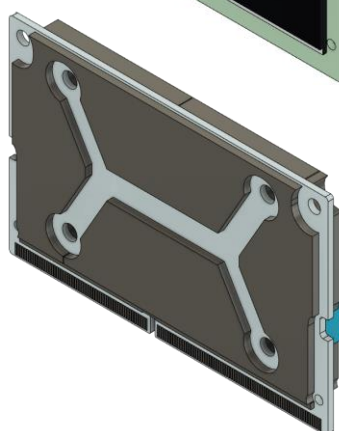
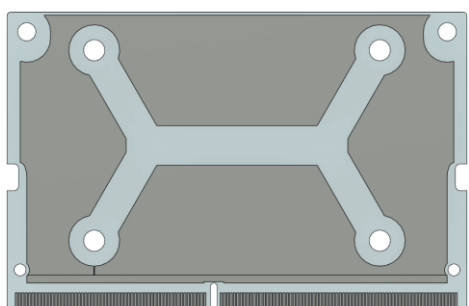


Figure 5. Module 3D Envelope Bottom Views

**Jetson Nano Bottom****Jetson TX2 NX Bottom****Jetson Orin Nano Bottom**

# Interface Migration

Table 6 and Table 7 show the functional pin-mapping options for Jetson Nano, Jetson TX2 NX, and Jetson Orin Nano series and how they align and differ.

## UPHY Mapping

Table 6. UPHY0 Mapping Options (USB 3.2 and PCIe)

Orin Module Pin Names	Orin Module Functions	UPHY0 Lanes	Nano Configs	TX2 NX Configs	Orin Nano Configs		
					Option #1	Option #2	Option #3
USBSS_RX/TX	USB 3.2 #1	Lane 0	USB 3.2 (P0)	USB 3.2 (P1)	USB 3.2 (P0)	USB 3.2 (P0)	USB 3.2 (P0)
DP0_TXD[1:0]_N/P	USB 3.2 #2	Lane 1	DP/HDMI	DP/HDMI	USB 3.2 (P1)	USB 3.2 (P1)	USB 3.2 (P1)
DP0_TXD[3:2]_N/P	USB 3.2 #3	Lane 2	DP/HDMI	DP/HDMI	USB 3.2 (P2)	USB 3.2 (P2)	Unused
PCIE1_RX0/TX0	PCIe #1 Lane 0	Lane 3	Used on module for Ethernet	PCIe x1 (C2), RP	PCIe x1 (C1), RP	PCIe x1 (C1), RP. Limited to Gen2	PCIe x1 (C1), RP
PCIE0_RX0/TX0	PCIe #1 Lane 0	Lane 4	PCIe x4 (C0), RP	PCIe x4 (C0), RP	PCIe x4 (C4), RP	PCIe x4 (C4), EP	PCIe x4 (C4), EP
PCIE0_RX1/TX1	USB 3.2 #1	Lane 5		Unused			
PCIE0_RX2/TX2	USB 3.2 #2	Lane 6					
PCIE0_RX3/TX3	USB 3.2 #3	Lane 7					

Table 7. UPHY2 Mapping Options (PCIe)

Orin Module Pin Names	Orin Module Functions	UPHY0 Lanes	Nano Configurations	TX2 NX Configurations	Orin Nano Configurations	
					Option #1	Option #2
CSI4_D[0:2]_RX0/TX0	PCIe #2 Lane 0	Lane 0	CSI	CSI	PCIe x2 (C7), RP	PCIe x1 (C7), RP
CSI4_D[1:3]_RX1/TX1	PCIe #2 Lane 1	Lane 1	CSI	CSI		PCIe x1 (C9), RP

## USB 3.x Mapping Options

Table 8 shows how the USB 3.2 ports on Jetson Orin Nano map with the USB 3.1 port or other functions on Jetson TX2 NX or the USB 3.0 port on Jetson Nano.

Table 8. USB 3.x Mapping Options

Connector Pin Names	Jetson Nano Functions	Jetson TX2 NX Functions	Jetson Orin Nano Series Functions
USBSS_TX/RX	USB 3.0 (5 Gbps) Port #0	USB 3.1 (5/10 Gbps) Port #0	USB 3.2 (5/10 Gbps) Port #0
DP0_TXD[1:0]_P/N	HDMI/DP	HDMI/DP	USB 3.2 (5/10 Gbps) Port #1
DP0_TXD[3:2]_P/N	HDMI/DP	HDMI/DP	USB 3.2 (5/10 Gbps) Port #2

## PCIe Mapping Options

Jetson Orin Nano series supports up to four PCIe interfaces:

- ▶ PCIe0: x4 lane interface (#C4), Gen3, Root Port or Endpoint
- ▶ PCIe1: x1 lane interface (#C1), Gen3, Root Port only
- ▶ PCIe2: x2 lane interface (#C7), Gen3, Root Port only or
- ▶ PCIe2 + PCIe3: x1 lane interface (#C7) plus x1 lane interface (#C9), both Gen3, Root Port only



Note: If PCIe0 (x4, #C4) on Orin Nano is used as Endpoint, either USB 3.2 Port #2 is not supported, or PCIe1 (x1, #C1) is limited to Gen2.

Jetson TX2 NX supports two PCIe interfaces:

- ▶ PCIe0: x2 lane interface (#C0), Gen2, Root Port only
- ▶ PCIe1: x1 lane interface (#C2), Gen2, Root Port only

Jetson Nano supports a single PCIe interface: PCIe0: x4 lane interface (#C0), Gen2, Root Port only.

Table 9 shows how the PCIe interface options on Jetson Orin Nano series map to the PCIe interface options on Jetson Nano and Jetson TX2 NX.

Table 9. PCIe Mapping Options

Jetson Module Connector Pin Names	Jetson Nano Functions	Jetson TX2 NX Functions	Jetson Orin Nano Series Function
PCIe0_RX0/TX0_P/N	PCIe	PCIe	PCIe
PCIe0_RX1/TX1_P/N	PCIe	PCIe	PCIe
PCIe0_RX2/TX2_P/N	PCIe	Reserved	PCIe
PCIe0_RX3/TX3_P/N	PCIe	Reserved	PCIe
PCIe0_CLK_P/N_P/N	PCIe	PCIe	PCIe

Jetson Module Connector Pin Names	Jetson Nano Functions	Jetson TX2 NX Functions	Jetson Orin Nano Series Function
PCIE0_RST*	PCIe	PCIe	PCIe
PCIE0_CLKREQ*	PCIe	PCIe	PCIe
PCIE1_RX0/TX0_P/N	Reserved	PCIe	PCIe
PCIE1_CLK_P/N	Reserved	PCIe	PCIe
PCIE1_RST*	Reserved	PCIe	PCIe
PCIE1_CLKREQ*	Reserved	PCIe	PCIe
CSI4_CLK_P/N	CSI	CSI	PCIe
CSI4_D[3:0]_P/N	CSI	CSI	PCIe
SDMMC_CLK	SDMMC	SDMMC	PCIe
SDMMC_CMD	SDMMC	SDMMC	PCIe
SDMMC_DAT[3:0]	SDMMC	SDMMC	PCIe
PCIE_WAKE*	PCIe	PCIe	PCIe

## Ethernet

Jetson Orin Nano series, Jetson Nano, and Jetson TX2 NX all support an MDI Gigabit Ethernet interface at the same module pins.

## SDIO and SD Card

Jetson Nano and Jetson TX2 NX bring a single SDMMC interface to the module pins (SDCARD pins supporting SD card or SDIO). Jetson Orin Nano does not support this interface. The module SDMMC pins are used for PCIe functionality.

## HDMI and DisplayPort

Jetson Orin Nano supports a single HDMI™, VESA® Embedded DisplayPort™ (eDP), and DisplayPort (DP) interface and supports multi-head support through MST (on the DP [DP1 pins] only). Jetson TX2 NX and Jetson Nano support two HDMI, eDP, and DP interfaces but does not support multi-head operation. On Jetson Orin Nano, two USB SS interfaces use a portion of the other display interface pins on Jetson Nano or Jetson TX2 NX.

Table 10. HDMI and DP Mapping Options

Connector Signal Group	Jetson Nano Functions	Jetson TX2 NX Functions	Jetson Orin Nano Series Functions
DP0_TXD0	DP/HDMI	DP/HDMI	USBSS1_RX



Connector Signal Group	Jetson Nano Functions	Jetson TX2 NX Functions	Jetson Orin Nano Series Functions
DP0_TXD1	DP/HDMI	DP/HDMI	USBSS1_TX
DP0_TXD2	DP/HDMI	DP/HDMI	USBSS2_RX
DP0_TXD3	DP/HDMI	DP/HDMI	USBSS2_TX
DP0_AUX	DP/HDMI	DP/HDMI	RSVD
DP0_HPD	DP/HDMI	DP/HDMI	RSVD
DP1_TXD0	DP/HDMI	DP/HDMI	DP/HDMI
DP1_TXD1	DP/HDMI	DP/HDMI	DP/HDMI
DP1_TXD2	DP/HDMI	DP/HDMI	DP/HDMI
DP1_TXD3	DP/HDMI	DP/HDMI	DP/HDMI
DP1_AUX	DP/HDMI	DP/HDMI	DP/HDMI
DP1_HPD	DP/HDMI	DP/HDMI	DP/HDMI

## CSI

Jetson Orin Nano, Jetson Nano, and Jetson TX2 NX all support MIPI CSI (DPHY only). The following configurations can be supported by each module to cameras or serializers:

Jetson Orin Nano supports eight lanes total.

- ▶ 2 x4
- ▶ 1 x4 + 2 x2
- ▶ 4 x2

Jetson TX2 NX supports 14 lanes.

- ▶ 3 x4 + 1 x2
- ▶ 2 x4 + 3 x2
- ▶ 1 x4 + 5 x2
- ▶ 6 x2

Jetson Nano supports 12 lanes.

- ▶ 3 x4
- ▶ 2 x4 + 2 x2
- ▶ 1 x4 + 3 x2
- ▶ 4 x2

## Audio

Jetson Orin Nano series and Jetson Nano support two I2S interfaces. Jetson TX2 NX supports four I2S interfaces. In addition, both support a primary audio clock, Digital Microphone (DMIC), and Digital Speaker (DSPK) interfaces.

Table 11. Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano Audio Interfaces

Pin #	Module Pin Name	Jetson Nano Function	Jetson TX2 NX Function	Jetson Orin Nano Function
Audio Functionality Supported by all three modules				
211	GPIO09	MCLK	MCLK	MCLK
199	I2S0_SCLK	I2S 0 SCLK	I2S 0 SCLK	I2S 0 SCLK
197	I2S0_FS	I2S 0 FS / DMIC / DSPK	I2S 0 FS / DMIC / DSPK	I2S 0 FS / DMIC / DSPK
193	I2S0_DOUT	I2S 0 DOUT	I2S 0 DOUT	I2S 0 DOUT
195	I2S0_DIN	I2S 0 DIN / DMIC / DSPK	I2S 0 DIN / DMIC / DSPK	I2S 0 DIN / DMIC / DSPK
226	I2S1_SCLK	I2S 1 SCLK	I2S 1 SCLK	I2S 1 SCLK
224	I2S1_FS	I2S 1 FS	I2S 1 FS	I2S 1 FS
220	I2S1_DOUT	I2S 1 DOUT	I2S 1 DOUT	I2S 1 DOUT
222	I2S1_DIN	I2S 1 DIN	I2S 1 DIN	I2S 1 DIN
Audio Functionality only Supported on Jetson TX2 NX				
128	GPIO05	GPIO	I2S 2 SCLK	GPIO
127	GPIO04	GPIO	I2S 2 FS	GPIO
124	GPIO02	GPIO	I2S 2 DOUT	GPIO
126	GPIO03	GPIO	I2S 2 DIN	GPIO
212	GPIO10	GPIO	I2S 3 SCLK	GPIO
130	GPIO06	GPIO	I2S 3 FS	GPIO
218	GPIO12	GPIO	I2S 3 DOUT	GPIO
112	SPI1_CS1*	SPI / GPIO	I2S 3 DIN	SPI / GPIO

## I2C

Jetson Orin Nano series, Jetson TX2 NX, and Jetson Nano all support four I2C interfaces as shown in the Table 12.

Table 12. Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano I2C Interfaces

Module Pin #	Jetson Module Signal Name
185	I2C0_SCK
187	I2C0_SDA
189	I2C1_SCK
191	I2C1_SDA
232	I2C2_SCK
234	I2C2_SDA
213	CAM_I2C_SCK
215	CAM_I2C_SDA

## SPI

Jetson Orin Nano series, Jetson TX2 NX, and Jetson Nano all support two SPI interfaces as shown in Table 13.

Table 13. Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano SPI Interfaces

Module Pin #	Jetson Module Signal Name
91	SPI0_SCK
89	SPI0_MOSI
93	SPI0_MISO
95	SPI0_CS0
97	SPI0_CS1
106	SPI1_SCK
104	SPI1_MOSI
108	SPI1_MISO
110	SPI1_CS0
112	SPI1_CS1

## UART

Jetson Orin Nano series, Jetson TX2 NX, and Jetson Nano support three UART interfaces.

Table 14. Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano UART Interfaces

Module Pin #	Jetson Module Signal Name
99	UART0_TXD
101	UART0_RXD
103	UART0_RTS*
105	UART0_CTS*
203	UART1_TXD
205	UART1_RXD
207	UART1_RTS*
209	UART1_CTS*
236	UART2_TXD
238	UART2_RXD

## Debug

Jetson Orin Nano, Jetson TX2 NX, and Jetson Nano support a debug UART.

---

# Connector Pin Differences Details

Table 15 lists the pins that have different functionality between Jetson Nano, Jetson TX2 NX, and Jetson Orin Nano.

Table 15. Connector Pin Function Differences

Module Pin #	Jetson Module Signal Name	Jetson Nano Function	Jetson TX2 NX Function	Jetson Orin Nano Series Function
76	DSI_CLK_N	DSI	DSI	RSVD
78	DSI_CLK_P	DSI	DSI	RSVD
70	DSI_D0_N	DSI	DSI	RSVD
72	DSI_D0_P	DSI	DSI	RSVD
82	DSI_D1_N	DSI	DSI	RSVD
84	DSI_D1_P	DSI	DSI	RSVD
90	DP0_AUX_N	DP / DDC	DP / DDC	RSVD
92	DP0_AUX_P	DP / DDC	DP / DDC	RSVD
88	DP0_HPD	DP / HDMI	DP / HDMI	RSVD
52	CSI4_CLK_N	CSI	CSI	PCIE
54	CSI4_CLK_P	CSI	CSI	PCIE
46	CSI4_D0_N	CSI	CSI	PCIE
48	CSI4_D0_P	CSI	CSI	PCIE
58	CSI4_D1_N	CSI	CSI	PCIE
60	CSI4_D1_P	CSI	CSI	PCIE
40	CSI4_D2_N	CSI	CSI	PCIE
42	CSI4_D2_P	CSI	CSI	PCIE
64	CSI4_D3_N	CSI	CSI	PCIE
66	CSI4_D3_P	CSI	CSI	PCIE
229	SDMMC_CLK	SDIO / SD	SDIO / SD	PCIE
227	SDMMC_CMD	SDIO / SD	SDIO / SD	PCIE
219	SDMMC_DAT0	SDIO / SD	SDIO / SD	PCIE
221	SDMMC_DAT1	SDIO / SD	SDIO / SD	PCIE

Module Pin #	Jetson Module Signal Name	Jetson Nano Function	Jetson TX2 NX Function	Jetson Orin Nano Series Function
223	SDMMC_DAT2	SDIO / SD	SDIO / SD	PCIE
225	SDMMC_DAT3	SDIO / SD	SDIO / SD	PCIE
39	DP0_TXD0_N	DP / HDMI	DP / HDMI	USB 3.2
41	DP0_TXD0_P	DP / HDMI	DP / HDMI	USB 3.2
45	DP0_TXD1_N	DP / HDMI	DP / HDMI	USB 3.2
47	DP0_TXD1_P	DP / HDMI	DP / HDMI	USB 3.2
51	DP0_TXD2_N	DP / HDMI	DP / HDMI	USB 3.2
53	DP0_TXD2_P	DP / HDMI	DP / HDMI	USB 3.2
57	DP0_TXD3_N	DP / HDMI	DP / HDMI	USB 3.2
59	DP0_TXD3_P	DP / HDMI	DP / HDMI	USB 3.2
217	GND	GND	GND	MODULE_ID

## Notice

This document is provided for information purposes only and shall not be regarded as a warranty of a certain functionality, condition, or quality of a product. NVIDIA Corporation ("NVIDIA") makes no representations or warranties, expressed or implied, as to the accuracy or completeness of the information contained in this document and assumes no responsibility for any errors contained herein. NVIDIA shall have no liability for the consequences or use of such information or for any infringement of patents or other rights of third parties that may result from its use. This document is not a commitment to develop, release, or deliver any Material (defined below), code, or functionality.

NVIDIA reserves the right to make corrections, modifications, enhancements, improvements, and any other changes to this document, at any time without notice.

Customer should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

NVIDIA products are sold subject to the NVIDIA standard terms and conditions of sale supplied at the time of order acknowledgement, unless otherwise agreed in an individual sales agreement signed by authorized representatives of NVIDIA and customer ("Terms of Sale"). NVIDIA hereby expressly objects to applying any customer general terms and conditions with regards to the purchase of the NVIDIA product referenced in this document. No contractual obligations are formed either directly or indirectly by this document.

Unless specifically agreed to in writing by NVIDIA, NVIDIA products are not designed, authorized, or warranted to be suitable for use in medical, military, aircraft, space, or life support equipment, nor in applications where failure or malfunction of the NVIDIA product can reasonably be expected to result in personal injury, death, or property or environmental damage. NVIDIA accepts no liability for inclusion and/or use of NVIDIA products in such equipment or applications and therefore such inclusion and/or use is at customer's own risk.

NVIDIA makes no representation or warranty that products based on this document will be suitable for any specified use. Testing of all parameters of each product is not necessarily performed by NVIDIA. It is customer's sole responsibility to evaluate and determine the applicability of any information contained in this document, ensure the product is suitable and fit for the application planned by customer, and perform the necessary testing for the application in order to avoid a default of the application or the product. Weaknesses in customer's product designs may affect the quality and reliability of the NVIDIA product and may result in additional or different conditions and/or requirements beyond those contained in this document. NVIDIA accepts no liability related to any default, damage, costs, or problem which may be based on or attributable to: (i) the use of the NVIDIA product in any manner that is contrary to this document or (ii) customer product designs.

No license, either expressed or implied, is granted under any NVIDIA patent right, copyright, or other NVIDIA intellectual property right under this document. Information published by NVIDIA regarding third-party products or services does not constitute a license from NVIDIA to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property rights of the third party, or a license from NVIDIA under the patents or other intellectual property rights of NVIDIA.

Reproduction of information in this document is permissible only if approved in advance by NVIDIA in writing, reproduced without alteration and in full compliance with all applicable export laws and regulations, and accompanied by all associated conditions, limitations, and notices.

THIS DOCUMENT AND ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS, AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY, OR OTHERWISE WITH RESPECT TO THE MATERIALS, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES OF NONINFRINGEMENT, MERCHANTABILITY, AND FITNESS FOR A PARTICULAR PURPOSE. TO THE EXTENT NOT PROHIBITED BY LAW, IN NO EVENT WILL NVIDIA BE LIABLE FOR ANY DAMAGES, INCLUDING WITHOUT LIMITATION ANY DIRECT, INDIRECT, SPECIAL, INCIDENTAL, PUNITIVE, OR CONSEQUENTIAL DAMAGES, HOWEVER CAUSED AND REGARDLESS OF THE THEORY OF LIABILITY, ARISING OUT OF ANY USE OF THIS DOCUMENT, EVEN IF NVIDIA HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. Notwithstanding any damages that customer might incur for any reason whatsoever, NVIDIA's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms of Sale for the product.

## Trademarks

NVIDIA, the NVIDIA logo, CUDA, Jetson, Jetson Nano, NVIDIA Maxwell, NVIDIA Orin, and NVIDIA Pascal are trademarks and/or registered trademarks of NVIDIA Corporation in the U.S. and other countries. Other company and product names may be trademarks of the respective companies with which they are associated.

## VESA DisplayPort

DisplayPort and DisplayPort Compliance Logo, DisplayPort Compliance Logo for Dual-mode Sources, and DisplayPort Compliance Logo for Active Cables are trademarks owned by the Video Electronics Standards Association in the United States and other countries.

## HDMI

HDMI, the HDMI logo, and High-Definition Multimedia Interface are trademarks or registered trademarks of HDMI Licensing LLC.

## Arm

Arm, AMBA, and Arm Powered are registered trademarks of Arm Limited. Cortex, MPCore, and Mali are trademarks of Arm Limited. All other brands or product names are the property of their respective holders. "Arm" is used to represent Arm Holdings plc; its operating company Arm Limited; and the regional subsidiaries Arm Inc.; Arm KK; Arm Korea Limited.; Arm Taiwan Limited; Arm France SAS; Arm Consulting (Shanghai) Co. Ltd.; Arm Germany GmbH; Arm Embedded Technologies Pvt. Ltd.; Arm Norway, AS, and Arm Sweden AB.

## Copyright

© 2022, 2023 NVIDIA Corporation. All rights reserved.

