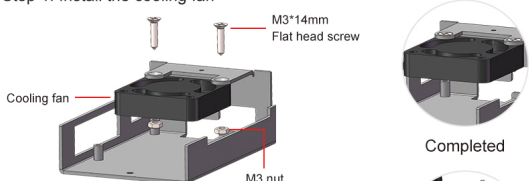
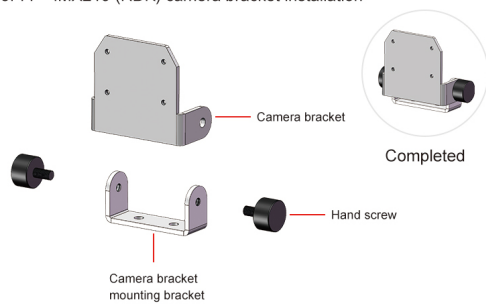
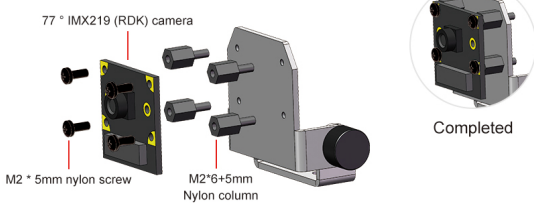
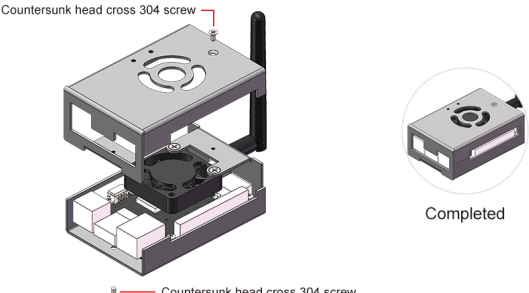


Install case antenna

Install case antenna

- 1.Assembly steps
2. Wiring
- 3.Completed

1.Assembly steps

Installation steps	Camera bracket installation
<p>Step 1: Install the cooling fan</p>  <p>Step 1: Install the cooling fan</p> <p>M3*14mm Flat head screw</p> <p>Cooling fan</p> <p>M3 nut</p> <p>Completed</p>	<p>Step 5: 77 ° IMX219 (RDK) camera bracket installation</p>  <p>Step 5: 77 ° IMX219 (RDK) camera bracket installation</p> <p>Camera bracket</p> <p>Hand screw</p> <p>Camera bracket mounting bracket</p> <p>Completed</p>
<p>Step 2: Antenna installation</p> <p>* Skip this step if antenna is not purchased</p>  <p>Sunrise Antenna</p> <p>WiFi antenna interface</p> <p>installation is complete</p> <p>Completed</p>	<p>Step 6: 77 ° IMX219 (RDK) camera installation</p>  <p>77 ° IMX219 (RDK) camera</p> <p>M2 * 5mm nylon screw</p> <p>M2*6+5mm Nylon column</p> <p>Completed</p>
<p>Step 3: RDK-X3 Installation</p>  <p>RDK-X3</p> <p>M2.5 * 4mm round head screw</p> <p>Completed</p>	<p>Step 7: Installation of camera assembly</p>  <p>M3*5mm Round head screw</p> <p>Completed</p>
<p>Step 4: Top shell installation</p> <p>Countersunk head cross 304 screw</p>  <p>Completed</p>	

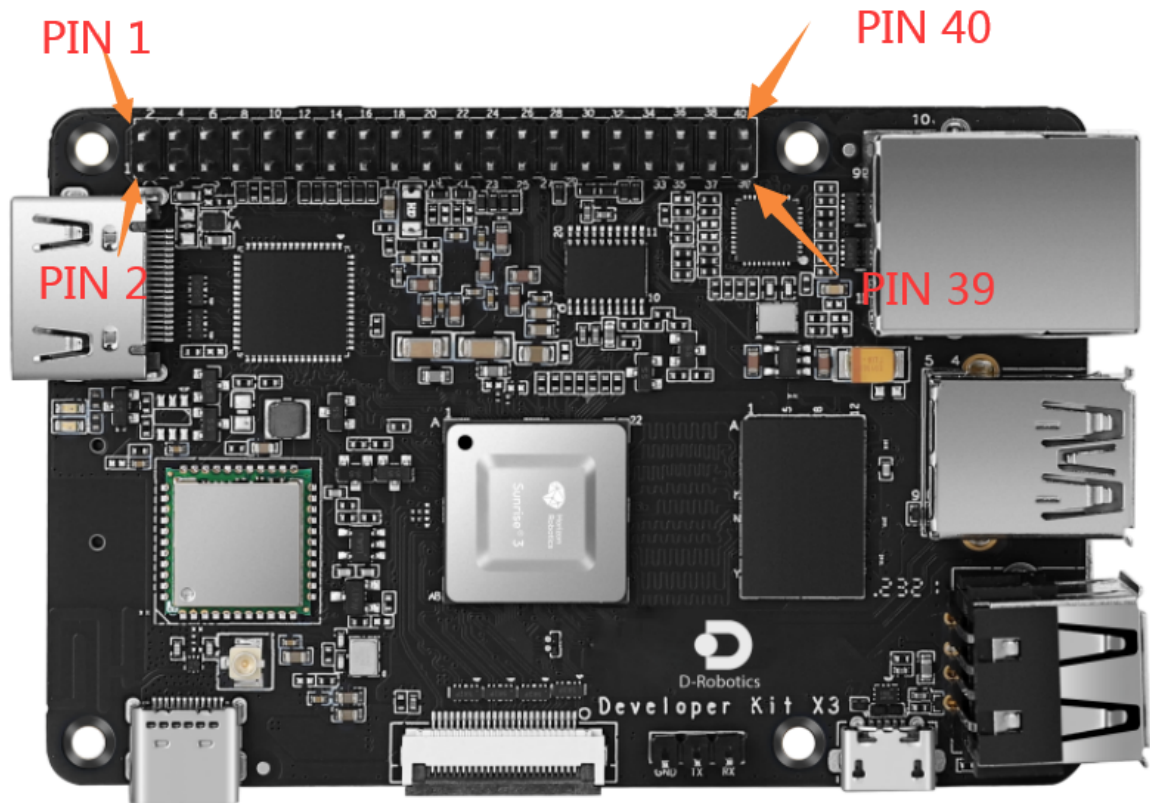
2. Wiring

The fan is connected to the outermost pin of the RDK X3 board.

The second pin connected to fan VCC (red line).

The third pin connected to fan GND (black line).

RDX X3 Board 40Pin Table														
Reuse function 2	Reuse function 1	Reuse function 0	Function Description	X3 Pin Number	BCM Encoding	CVM Function	Physical Pin Board Encoding	CVM Function	BCM Encoding	X3 Pin Number	Function Description	Reuse function 0	Reuse function 1	Reuse function 2
			3.3V power signal			VDD_3V3	1	2	VDD_5V		5V power signal			
		I2C0_SDA	I2C0 data signal	9	2	I2C0_SDA	3	4	VDD_5V		5V power signal			
		I2C0_CLK	I2C0 clock signal	8	3	I2C0_SDC	5	6	GND		GND signal			
		I2S0_MCLK	I2S0 MCLK clock signal	101	4	I2S0_MCLK	7	8	UART_TXD	14	111	UART3_TXD		
			GND signal			GND	9	10	UART_RXD	15	112	UART3_RXD		
PWM7	SPI2_MOSI	I2C2_CLK	GPIOD17 signal	12	17	GPIOD17	11	12	I2S0_BCLK	18	102	I2S0_BCLK		
PWM8	SPI2_MISO	I2C2_SDA	GPIOD27 signal	13	27	GPIOD27	13	14	GND		GND signal			
			GPIOD22 signal	30	22	GPIOD22	15	16	GPIOD23	23	27	GPIOD23		
			3.3V power signal			VDD_3V3	17	18	GPIOD24	24	22	GPIOD24		
	SPI1_MOSI		SPI1 MOSI signal	6	10	SPI1_MOSI	19	20	GND		GND signal			
	SPI1_MISO		SPI1 MISO signal	7	9	SPI1_MISO	21	22	GPIOD25	25	29	GPIOD25		
	SPI1_SCLK		SPI1 CLK signal	3	11	SPI1_SCLK	23	24	SPI1_CSN	8	5	SPI1_CSN		
			GND signal			GND	25	26	GPIOD7	7	28	GPIOD7		
	SPI2_CSN	I2C3_SDA	I2C3 clock signal	15	0	I2C3_SDA	27	28	I2C3_SCL	1	14	I2C3_SCL		
	LPWM3		GPIOD5 signal	119	5	GPIOD5	29	30	GND		GND signal			
	LPWM2		GPIOD6 signal	118	6	GPIOD6	31	32	PWM4	12	25	PWM4		
PWM0			PWM0 signal	4	13	PWM0	33	34	GND		GND signal			
		I2S0_LRCK	I2S0 LRCK signal	103	19	I2S0_LRCK	35	36	GPIOD16	16	20	GPIOD16		
	LPWM1		GPIOD26 signal	117	26	GPIOD26	37	38	I2S1_SDIO	20	108	I2S1_SDIO		
			GND signal			GND	39	40	I2S0_SDIO	21	104	I2S0_SDIO		



Antenna connection, as shown below.

3.Completed