

!Note:

The maximum continuous input and output voltage of the Raspberry Pi's GPIO pin is 3. 3V. Do not connect it directly with other electronic components, otherwise it will dam age the Raspberry Pi.

```
Step 1:Create a file to store the project
nano gpio.c
Step 2: Writing code
#include<stdio.h>
#include<wiringPi.h>
                             //Import wiringPi.h library
#define GPIO0 0
                     //Define GPIO0 as pin 0 of wPi, corresponding to GPIO0
int main()
{
       printf("This is an example of controlling the high and low levels of GPIO pin outp
ut\n");
       wiringPiSetup();
                                            //Initialize wiringPi
       pinMode(GPIO0,OUTPUT);
                                            //Set GPIO0 to output mode
       printf("Set GPIO0 : H\n");
       digitalWrite(GPIO0,HIGH);
                                      //GPIO0 output high level
       //printf("Set GPIO0 : L\n");
       //digitalWrite(GPIO0,LOW); //GPIO0 output low level
       while(1)
       {
       return 0;
}
After writing, press Ctrl + X to exit this file.
The system will prompt you whether you need to save, press Y to save and exit.
Step 3: Compile this .c file.
gcc gpio.c -o gpio -lwiringPi
Step 4: Run this code
./gpio
Step 5: Press Ctrl + C
Step 6: Check the pin status of the Raspberry Pi.
gpio readall
```



```
pi@raspberrypi:~/work/example/C $ gcc gpio.c -o gpio -lwiringPi
pi@raspberrypi:~/work/example/C $ ./gpio
This is an example of controlling the high and low levels of GPIO pin output
Set GPI00 : H
pi@raspberrypi:~/work/example/C $ gpio readall
                                  --+---Pi 4B--
                 Name | Mode | V | Physical | V | Mode |
                                                             Name
                   3.3v
                 SDA.1
                            IN
                 SCL.1
                            IN
                            IN
                                                     IN
     4
               GPIO. 7
                                                             TxD
                                                                              14
                                                     IN
                                                             RxD
                                                                        16
                                            10
                    OV
                                                     IN
                                                             GPIO. 1
                                                                              18
    17
           0 | GPIO. 0 |
                           OUT | 1 | 11
                                            12
                                            14
                                                             OV
               GPIO. 2
                            IN
                                                             GPI0. 4
    22
               GPIO. 3
                                      15
                                                      IN
                                                                        4
                                                                              23
                                                     IN
                                                             GPIO. 5
                   3.3v
                                      19
    10
          12
                   MOSI
                            IN
                                                             OV
                                                      IN
                                                             GPIO. 6
                                                                              25
     9
          13
                   MISO
                            IN
    11
          14
                   SCLK
                            IN
                                                      IN
                                                             CEO
                                                                        10
                     OV
                                      25
                                                      IN
                                                             CE1
                                                                        11
     0
                            IN
                                                             SCL.0
          30
                 SDA.0
                                            28
                                                     IN
                                                                        31
          21
               GPI0.21
                            IN
                                      29
                                            30
                                                             0v
     6
               GPI0.22
                            IN
                                      31
                                                      IN
                                                             GPI0.26
                                                                        26
                                                                              12
    13
               GPI0.23
                            IN
                                      33
                                            34
    19
          24
               GPI0.24
                                                             GPI0.27
               GPI0.25
                            IN
                                            38
                                                      IN
                                                             GPI0.28
                                                                              20
                                      39
                                                             GPI0.29
                     ΘV
                          Mode
                               | V | Physical
                                                                              BCM
```

We can know that the GPIO0 is OUT mode, and V (state) is 1 (high level).

```
Step 7: Modify code
#include<stdio.h>
#include<wiringPi.h>
                            //Import wiringPi.h library
                     //Define GPIO0 as pin 0 of wPi, corresponding to GPIO0
#define GPIO0 0
int main()
{
       printf("This is an example of controlling the high and low levels of GPIO pin outp
utn";
                                           //Initialize wiringPi
       wiringPiSetup();
       pinMode(GPIO0,OUTPUT);
                                           //Set GPIO0 to output mode
       //printf("Set GPIO0 : H\n");
       //digitalWrite(GPIO0,HIGH);
                                      //GPIO0 output high level
       printf("Set GPIO0 : L\n");
       digitalWrite(GPIO0,LOW);
                                      //GPIO0 output low level
       while(1)
       {
```



```
return 0;
}
After writing, press Ctrl + X to exit this file.
The system will prompt you whether you need to save, press Y to save and exi

Step 3: Compile this .c file.
gcc gpio.c -o gpio -lwiringPi

Step 4: Run this code
./gpio

Step 5: Press Ctrl + C
```

Step 6: Check the pin status of the Raspberry Pi. gpio readall

		:~/work/e									
	an exa	ample of co	ontroll	ing	the hi	gh an	d lov	v level	s of GPIO	pin ou	tput
	berrypi	-/work/ex	cample/C	S	./apio						
Set GPI	00 : L	ample of co			the hi	gn an	u Lov	v tevet	S OI GPIO	pin ou	cput
i@rasp		L:~/work/e			~ .						
+	wPi	Name	Mode	V	+Pi Phys	4B	+	Mode	Name	wPi	+
+	wPi	Name	Mode	V	+Pi Phys ++	4B ical +	+	Mode	Name +	wPi	
BCM	wPi	Name 3.3v	Mode	V	+Pi Phys ++ 1	4B ical +	+	Mode	Name + 5v	wPi	
+ BCM + 1 2	wPi 	Name 3.3v SDA.1	Mode IN	V	+Pi Phys + 1 3	4B ical + 2 4	+	Mode	Name + 5v 5v	wPi	
+ BCM + 2 3	wPi	Name 3.3v SDA.1 SCL.1	Mode IN IN	V	+Pi Phys ++ 1	4B ical + 2 4 6	+ V + 	Mode 	Name + 5v 5v 0v	wPi 	BCM +
BCM	wPi 	Name 3.3v SDA.1	Mode IN	V	+Pi Phys + 1 3	4B ical + 2 4	+	Mode	Name + 5v 5v	wPi	

We can know that the GPIO0 is OUT mode, and V (state) is 0 (low level).