

PWM breathing light

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1. Learning objectives

1. Learn the principle of PWM breathing light.
2. Output a PWM signal through TIMG6_C0 to control the LED indicator from dark to bright, and then from bright to dark, similar to human breathing.

PWM principle

PWM is the abbreviation of Pulse Width Modulation, which means pulse width modulation in Chinese. It is a very effective technology that uses the digital output of the microprocessor to control the analog circuit. Its advantages such as simple control, flexibility and good dynamic response make it the most widely used control method in power electronics technology. Its application fields include measurement, communication, power control and conversion, motor control, servo control, dimming, switching power supply, and even some audio amplifiers. Therefore, learning PWM has very important practical significance.

In fact, we can also understand it this way. PWM is a method of digitally encoding the level of analog signals. Through the use of high-resolution counters, the duty cycle of the square wave is modulated to encode the level of a specific analog signal. PWM signals are still digital because at any given moment, the full-scale DC power supply is either fully present (ON) or completely absent (OFF). The voltage or current source is applied to the analog load in a repetitive sequence of pulses that are either on (ON) or off (OFF). On is when the DC power supply is applied to the load, and off is when the power supply is disconnected. As long as the bandwidth is sufficient, any analog value can be encoded using PWM.

Basic PWM Parameters

PWM is pulse width modulation, which has two very important parameters: frequency and duty cycle.

- **Frequency:** The frequency of PWM is the inverse of the entire cycle, that is, the number of times the PWM signal is repeated per unit time. It indicates how many signal cycles are completed per second. The frequency determines the rate at which the PWM signal changes. The higher the frequency, the shorter the signal cycle and the more times it changes.
- **Duty Cycle:** The duty cycle refers to the proportion of the high level in a cycle. It is usually expressed as a percentage. It reflects the "width" of the PWM signal, that is, the ratio of the

duration of the high level part to the total cycle time. The duty cycle determines the average output voltage of the signal. The larger the duty cycle, the higher the average output voltage.

2. Hardware Construction

We introduced earlier that the MSPM0G series has a total of 7 timers, which can be divided into 2 types, general timer (TIMG) and advanced control timer (TIMA). The PWM function is implemented on the basis of the timer. From the user manual, we can know that MSPM0G3507 has 7 timers. Which timer has PWM function needs to refer to the pin description of the data manual. Each PWM channel corresponds to a pin of the microcontroller. This pin is not unique and fixed. One or two pins may correspond to the same channel. For example, TIMG_C0 corresponds to PA5 and PA12, which means that both PA5 and PA12 pins can be configured as channel 0 of the timer. When using it, we can choose any one of them for configuration.

10	PA5	HFXIN	TIMG8_C0 [2] / SPI0_PICO [3] / TIMA_FAL1 [4] / TIMG0_C0 [5] / TIMG6_C0 [6] / FCC_IN [7]	45	11	9	12
34	PA12		UART3_CTS [2] / SPI0_SCK [3] / TIMG0_C0 [4] / CAN_TX [5] / TIMA0_C3 [6] / FCC_IN [7]	5	27	16	-

This case uses the PB2 pin of LED1, configured as channel 0 of timer 6, to generate a PWM signal to control the on and off of the light.

15	PB2		UART3_TX [2] / UART2_CTS [3] / I2C1_SCL [4] / TIMA0_C3 [5] / UART1_CTS [6] / TIMG6_C0 [7] / TIMA1_C0 [8]	50	14	-	-
----	-----	--	---	----	----	---	---

There are two modes of PWM. One is edge-aligned PWM, which refers to the down-counting mode used by the timer. The output channel will output a high level when the timer starts counting. **When the count value is the same as the comparison value of the output channel (CC0 and CCP0, CC1 and CCP1 in the figure), the output channel will output a low level until the count value of the timer counts to 0, completing a cycle of pulse output.**

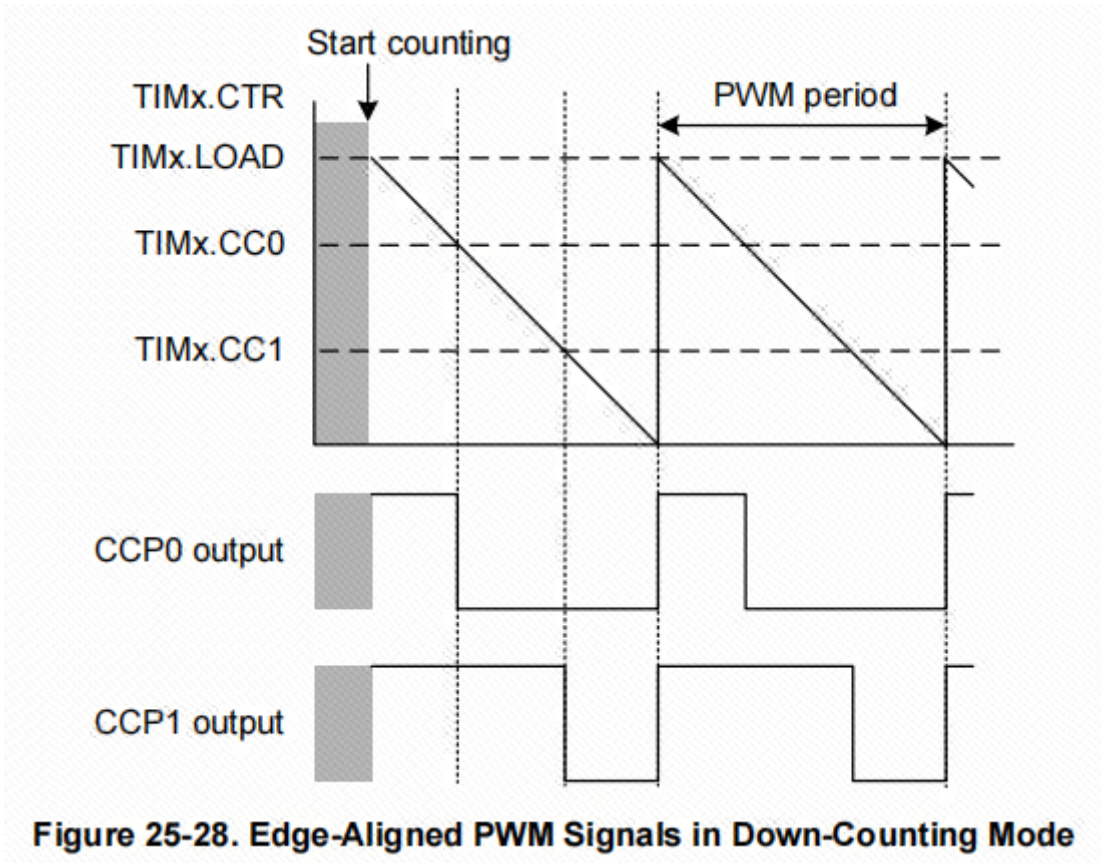
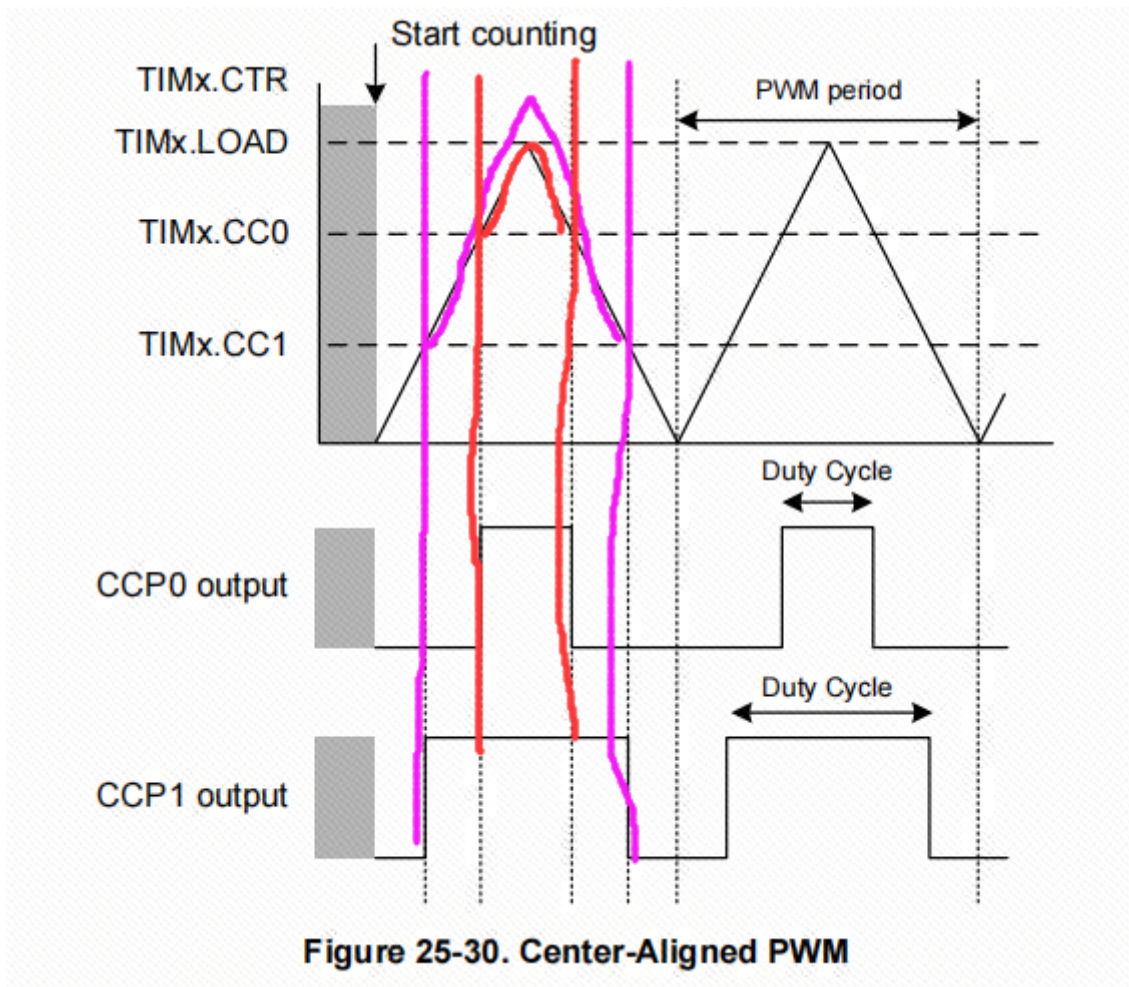


Figure 25-28. Edge-Aligned PWM Signals in Down-Counting Mode

The other mode is center-aligned mode, which refers to the up-down counting mode of the timer. The output channel will output a low level at the start of the timer, and flip the level once when the timer counts up and counts down twice to reach the comparison value of the output channel.

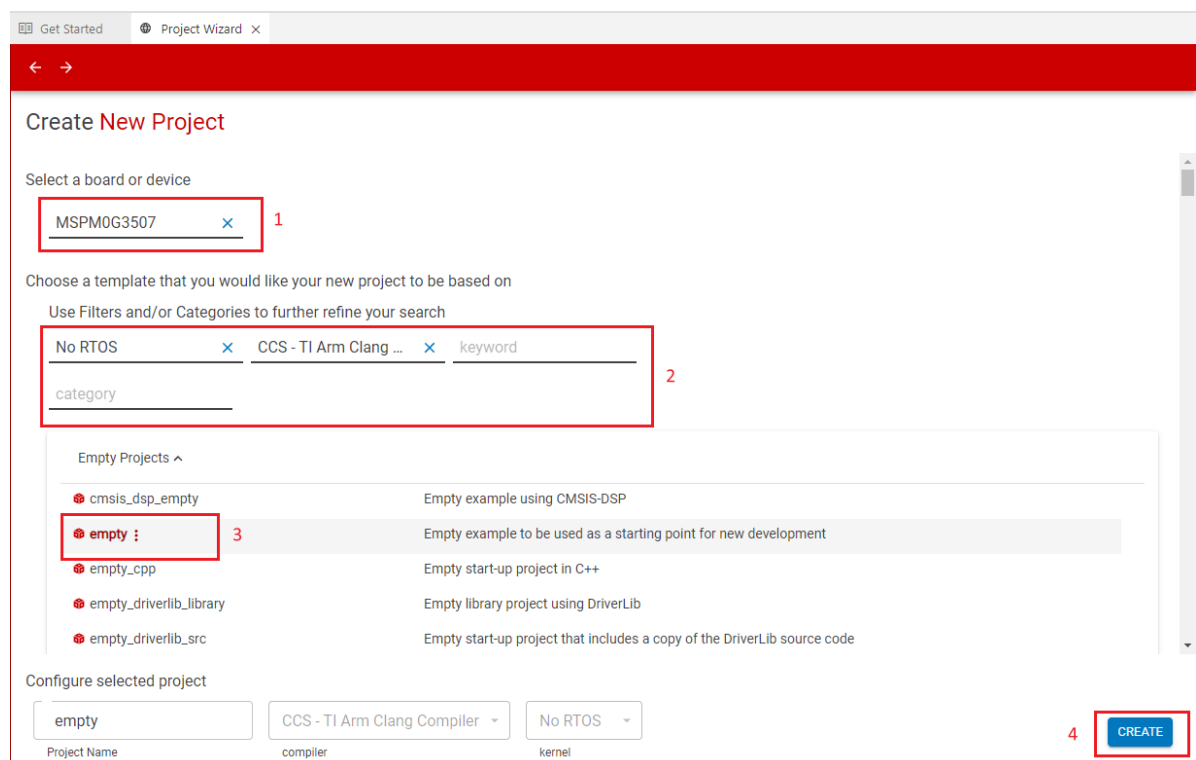
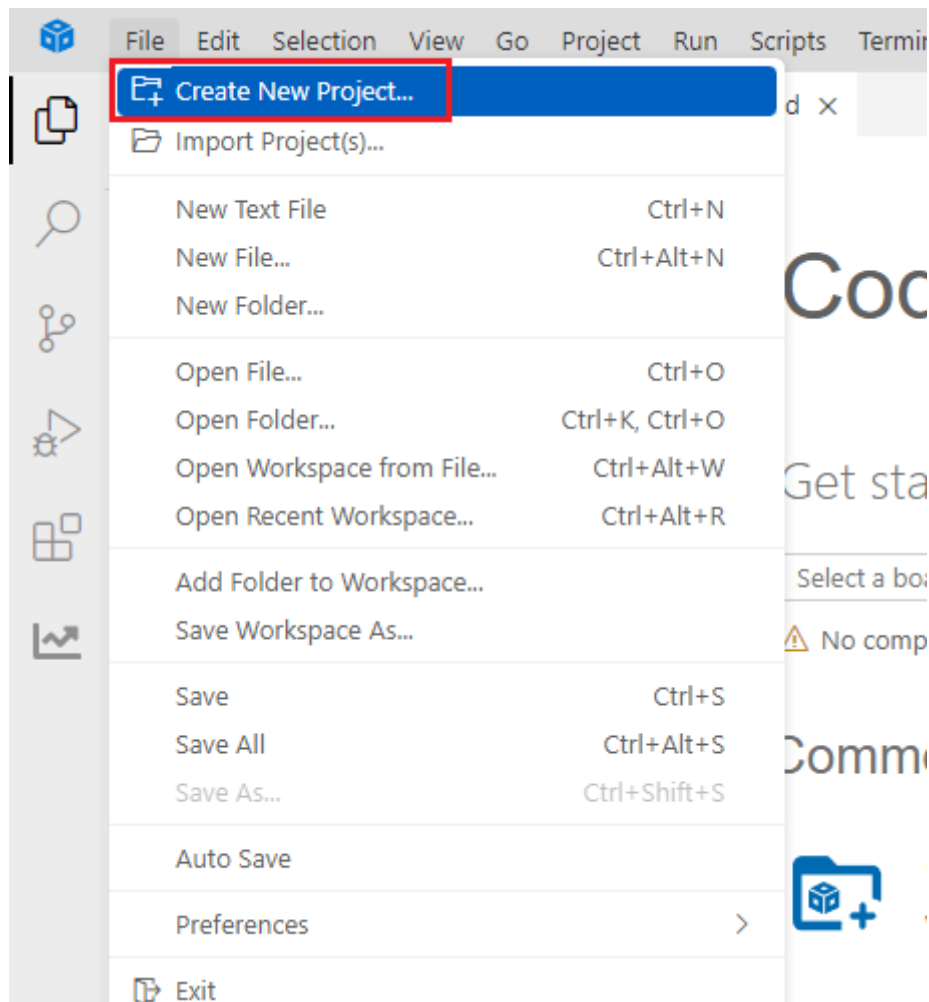


3. Experimental steps

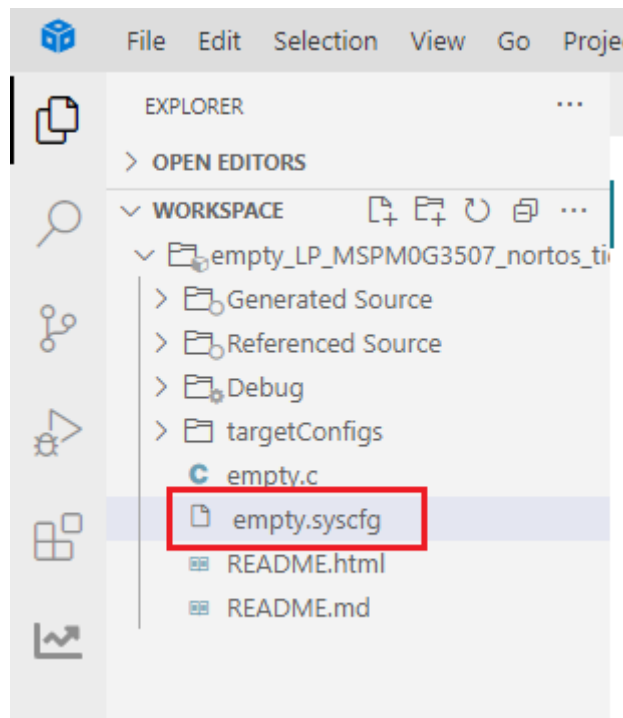
By driving the PWM signal to achieve the breathing light effect, by changing the duty cycle of the PWM signal, the current flowing through the LED can be adjusted, thereby achieving a gradual change in the brightness of the LED. Generally speaking, the human eye does not have a noticeable flickering sensation for refresh frequencies above 80Hz, because flickering cannot be detected at higher frequencies. The larger the duty cycle, the brighter the LED; the smaller the duty cycle, the dimmer the LED. Therefore, when the frequency is fixed, the brightness of the LED can be controlled by adjusting the duty cycle, thereby achieving a breathing light effect. In this case, the onboard LED light (PB2) will be used to achieve the breathing light effect, which gradually lights up and then gradually turns off.

1. Open the SYSCONFIG configuration tool

Create a new blank project empty in CCS.



Find and open the empty.syscfg file in the left workspace of CCS.



In SYSCONFIG, select MCU peripherals on the left, find the TIMER-PWM option and click to enter. Click ADD in TIMER-PWM to add the PWM peripheral under the timer.

07_pwm > empty.syscfg

Type Filter Text...

PROJECT CONFIGURATION (1)
Project Configurat... 1/1 ✓ +

MSPM0 DRIVER LIBRARY (7)
SYSTEM (9)
Board 1/1 ✓ +
Configuration NVM +
DMA +
GPIO 1 ✓ +
MATHACL +
RTC +
SYSCTL 1/1 ✓ +
SYSTICK 1/1 ✓ +
WWDT +
ANALOG (6)
ADC12 +
COMP +
DAC12 +
GPAMP +
OPA +
VREF +
COMMUNICATIONS (6)
I2C +
I2C - SMBUS +
MCAN +
SPI +
UART +
UART - LIN +
TIMERS (6)
TIMER +
TIMER - CAPTURE +
TIMER - COMPARE +
TIMER - PWM 1 +
TIMER - QEI +
Timer Fault +
SECURITY (2)
AES +
TRNG +

Software > TIMER - PWM

TIMER - PWM (0 of 7 Added) 2 **ADD** REMOVE ALL

Description

The **Pulse Width Modulation (PWM) Driver module** allows you generate edge-aligned output waveforms.

Under *Basic Configuration* users can:

- Configure clock source
- Configure timer settings
- Configure PWM:
 - Select PWM mode
 - Configure PWM Channels (compare values, duty cycle, etc.)

Under *Advanced Configuration* users can:

- Configure cross trigger

This module supports the different types of Timers included in MSPM0 including *TIMGx*, *TIMAx*. The following table includes a description of the features included in **MSPM0G350x**. Check the datasheet of your selected device for more detailed information.

TIM Name	Power Domain	Resolution	Prescaler	Repeat	CC Ch	Phase Load	Shadow Load	Shadow CC	Deadb...
TIMG0	PD0	16-bit	8-bit	-	2	-	-	-	-
TIMG6	PD1	16-bit	8-bit	-	2	-	-	-	-
TIMG7	PD1	16-bit	8-bit	-	2	-	Y	Y	-
TIMG8	PD0	16-bit	8-bit	-	2	-	-	-	-
TIMG12	PD1	32-bit	-	-	2	-	-	Y	-

Click the **Add** button to add a TIMER - PWM to your design

Name PWM_0

Selected Peripheral

Quick Profiles ^

PWM Profiles Custom v

Basic Configuration ^

2. PWM parameter configuration

Here, set channel 0 to 4kHz frequency, 0% duty cycle PWM, cycle count value to 1000, and compare value to 1000. In this way, count down from 1000, and the PWM state will flip once every time 1000 is encountered.

PWM_LED

Peripheral does not retain register contents in STOP or STANDBY modes. User should take care to save and restore register configuration in application. See Retention Configuration section for more details.

Name

PWM_LED

Selected Peripheral

TIM6

Quick Profiles

^

PWM Profiles

Custom

▼

Basic Configuration

^

Clock Configuration

^

Timer Clock Source

BUSCLK

▼

Timer Clock Divider

Divided by 8

▼

Calculated Timer Clock Source

4000000

.....

Timer Clock Prescale

1

.....

Calculated Timer Clock Values

^

Calculated Clock Frequency (Hz)

4000000

.....

Timer Clock Information

61.04 Hz to 2.00 MHz w/ resolution of 250.00 ns

.....

PWM Period Count

1000

.....

Calculated PWM Frequency (Hz)

4000

.....

Start Timer

☒

PWM Configuration

^

PWM Mode

Edge-aligned Down Counting

▼

PWM Channel(s)

PWM Channel 0

▼

Enable Channel Complimentary Output

None

▼

PWM Channel 0 Channel Specific Configurables for PWM Channel 0

^

Initial Value

Low

▼

Counter Compare Value

1000

.....

Desired Duty Cycle (%)

0

.....

Actual Duty Cycle (%)

0

.....

Invert Channel

☐

Channel Update Mode

Capture Compare value has immediate effect

▼

Parameter description:

Name: custom name. Here it is set to PWM_LED

PWM Profiles: configure PWM template, select the default one.

Timer Clock Source: the clock source of the timer, select **BUSCLK** (bus clock), the default frequency is 32MHz.

Timer Clock Divider: Set the timer clock divider to **8 division**.

Calculated Timer Clock Source: Displays the clock frequency after division.

Timer Clock Prescale: Timer pre-allocation. We configure TIMG6, a 16-bit timer, which supports pre-division. No pre-division is set here.

Calculated Clock Frequency (Hz): Displays the frequency after division and pre-division.

PWM Period Count: PWM period count value. Here we set it to 1000.

Calculated PWM Frequency(Hz): The software automatically calculates the PWM frequency, which is displayed as 4000.

Start Timer: Whether to start the timer. Check it here to start the timer PWM output on power-on.

PWM Mode: PWM mode. Configured as **Edge-aligned Down Counting** edge alignment mode and down counting mode.

PWM Channel(s): PWM channel. Configured to only turn on channel 0.

Enable Channel Complimentary Output: Whether to enable channel complementary output. Not enabled here.

Initial Value: The default output state of the PWM channel. Configured as the default low-level output.

Counter Compare Value: The comparison value of PWM. Configured as 1000, that is, the setting range of the PWM duty cycle is 0-999.

Desired Duty cycle (%): The duty cycle of PWM. The default duty cycle of 0 is used here.

Actual Duty cycle (%): Displays the actual duty cycle automatically calculated by the software.

Invert Channel: Whether to enable invert output. Not enabled here.

Channel Update Mode: Channel update mode. Configured as **Capture Compare value has immediate effect**, the comparison value is updated immediately.

PinMux Peripheral and Pin Configuration		^
Timer Peripheral	TIMG6	▼ 🔒
Capture/Compare Pin 0	PB2/50	▼ 🔒

The control pin of LED1 is PB2, and PB2 has a timer 6 channel 0, so select **TIMG6**, **PB2/50** here.

GPIO (1 Added) ⓘ		⊕ ADD	🗑️ REMOVE ALL
✓ KEY		📄 🗑️	
Name	KEY		
Port	PORTA ▼		
Port Segment	Any ▼		

We have occupied the PB2 pin of LED1 with PWM, and it can no longer be used for GPIO peripherals and needs to be removed. Currently, there is only one button configuration left for the GPIO peripheral in the example.

Use the shortcut key Ctrl+S to save the configuration in the .syscfg file.

3. Write the program

In the empty.c file, write the following code

```
#include "ti_msp_dl_config.h"

volatile unsigned int delay_times = 0;

//搭配滴答定时器的精准毫秒级延时 Precise millisecond delay with tick timer
void delay_ms(unsigned int ms)
{
    delay_times = ms;
    while( delay_times != 0 );
}

int main(void)
{
    int i = 0;

    SYSCFG_DL_init();

    while (1)
    {

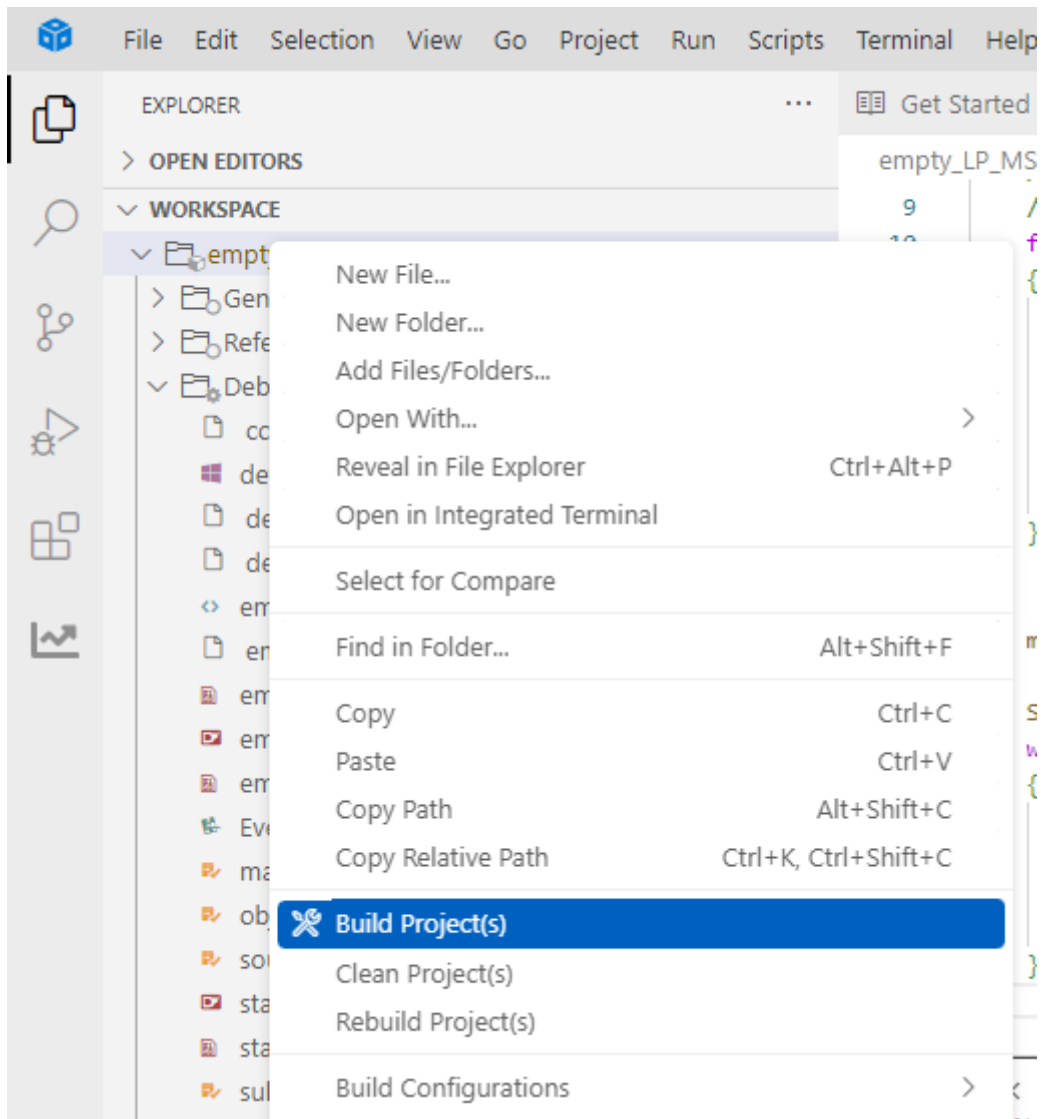
        // 呼吸灯渐亮过程 Breathing light gradually brightens
        for (i = 0; i <= 999; i++)
        {
            // 设置 LED 亮度 Setting LED Brightness

            DL_TimerG_setCaptureCompareValue(PWM_LED_INST,i,GPIO_PWM_LED_C0_IDX);
            delay_ms(1); // 延迟以控制亮度变化速度 Delay to control the speed of
            brightness change
        }
        // 呼吸灯渐暗过程 Breathing light dimming process
        for (i = 999; i > 0; i--)
        {
            // 设置 LED 亮度 Setting LED Brightness

            DL_TimerG_setCaptureCompareValue(PWM_LED_INST,i,GPIO_PWM_LED_C0_IDX);
            delay_ms(1); // 延迟以控制亮度变化速度 Delay to control the speed of
            brightness change
        }
    }

    //滴答定时器的中断服务函数 Tick timer interrupt service function
    void SysTick_Handler(void)
    {
        if( delay_times != 0 )
        {
            delay_times--;
        }
    }
}
```


4. Compile



If the compilation is successful, you can download the program to the development board.

4. Program Analysis

- empty.c

```

12  int main(void)
13  {
14      int i = 0;
15
16      SYSCFG_DL_init();
17
18      while (1)
19      {
20
21          // 呼吸灯渐亮过程 Breathing light gradually brightens
22          for (i = 0; i <= 999; i++)
23          {
24              // 设置 LED 亮度 Setting LED Brightness
25              DL_TimerG_setCaptureCompareValue(PWM_LED_INST,i,GPIO_PWM_LED_C0_IDX);
26              delay_ms(1); // 延迟以控制亮度变化速度 Delay to control the speed of brightness change
27          }
28          // 呼吸灯渐暗过程 Breathing light dimming process
29          for (i = 999; i > 0; i--)
30          {
31              // 设置 LED 亮度 Setting LED Brightness
32              DL_TimerG_setCaptureCompareValue(PWM_LED_INST,i,GPIO_PWM_LED_C0_IDX);
33              delay_ms(1); // 延迟以控制亮度变化速度 Delay to control the speed of brightness change
34          }
35      }
36  }

```

After initializing the hardware, enter the breathing light loop. Use the duty cycle of the PWM signal to control the brightness of the LED light. The breathing light **gradually brightens** and **gradually dims** uses two for loops to gradually adjust the PWM duty cycle from 0 to 999 and then from 999 to 0. `delay_ms(1)` sets the brightness change interval to 1 millisecond. The breathing light change speed can be controlled by adjusting the parameters of the delay function.

5. Experimental Phenomenon

After the program is downloaded, you can see that the LED light slowly turns on and off, showing a breathing light effect.

