Instruction Cache

Instruction Buffer

Warp Scheduler

Register File

core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU

L1 Cache

Shared Memory