## Instruction Cache





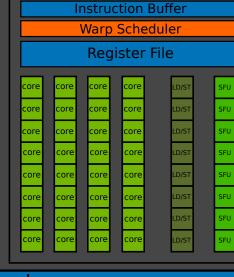
## L1 Cache

SFU

Instruction Buffer					
Warp Scheduler					
Register File					
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU
core	core	core	core	LD/ST	SFU

core

core



## L1 Cache

## **Shared Memory**