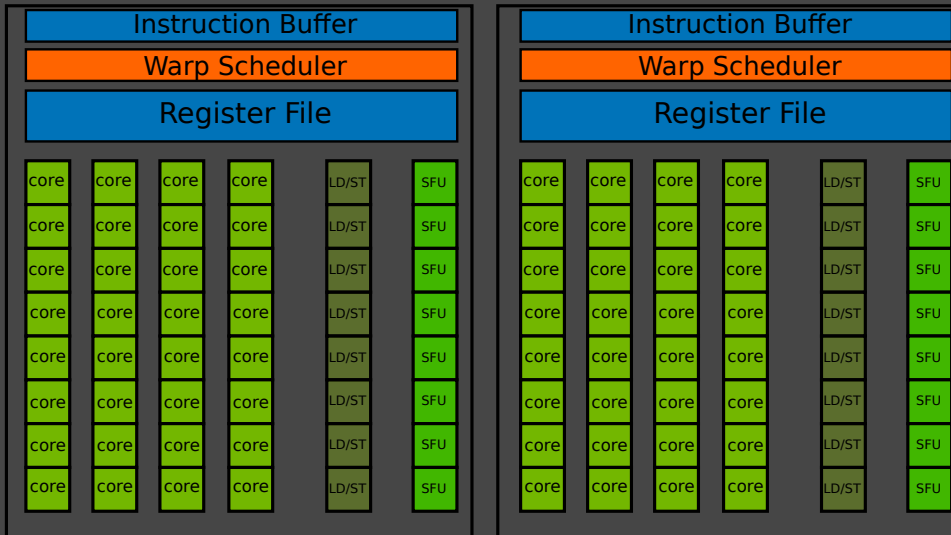
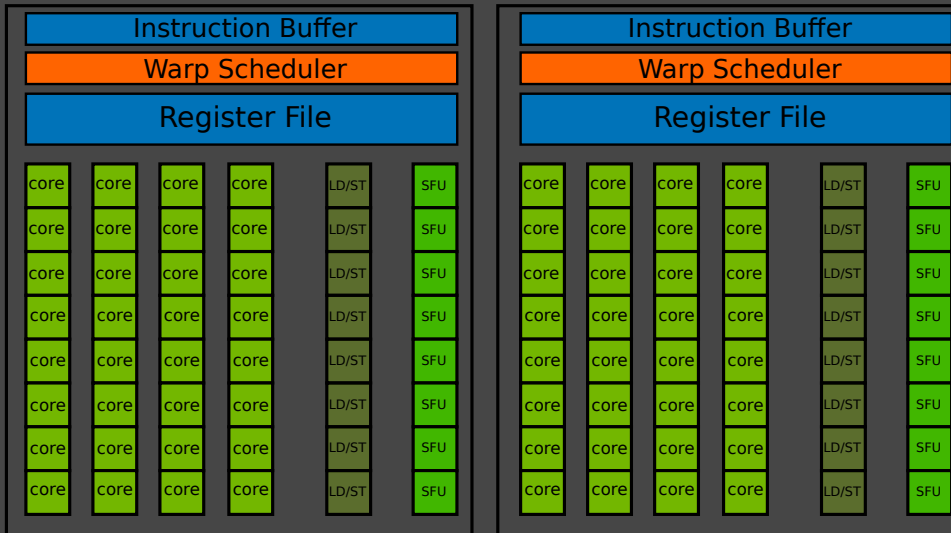


Instruction Cache



L1 Cache



L1 Cache

Shared Memory