

Learn YSYX Efficiently

WANG Rui, YSYX-23060042

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Part I: Learn YSYX Efficiently for Beginners

Internet Searching Matters

- Try to find the answer by yourself
- Find more learning resources that suit you

Documentation Matters

Maintained Information is Always Better;

Not just find a solution, but also think more about which is the best solution:

- The solution is suitable for your project
- The tool's author and community preference
- Easy for maintenance? Easy for debugging? Flexible for future changes?

ERROR Message Matters

Read the error message carefully before you search it or ask for help.

Learning Record Matters

Review and summarize what you have learned.

Plan the next step and set a goal day by day.

Before the Pre-Study Defense

- Check every required task
- Don't worry when you study hard

Part II: Learn YSYX Efficiently in C Stage

Speed Not Always Matters

Find more problems need to be solved, then you can learn more.

Learn things clearly and deeply, then you can learn more.

Design Your Own Path

Find your curiosity and interest during the whole YSYX learning process.

- Open Source Community?
- Architecture or Hardware Design?
- Software Development & Programming?
- Build tools & Automation?
- Documentation & Writing?
- Teaching & Sharing?
- Optimization & Performance Tuning?
- ...

Design Your Own Path

We don't push your speed or learning progress so **feel free to stay anywhere and explore more.**

Keep learning and keep exploring is fine.

Communication is Always Welcome

🔥把我们的微信群变得更火热🔥

Useful Resources

Books:

- Digital Design and Computer Architecture: RISC-V Edition
- The RISC-V Reader: An Open Architecture Atlas (RISC-V 开放架构设计之道)
- Digital Design with Chisel (Chisel Book)

Documents:

- Verilator User Manual
- **RISC-V ISA Specification**
- Arch Wiki
- `man` pages

Useful Resources

- GitHub Repos & Open Source Ecosystem
- 其他双周分享会的内容

谢谢大家！