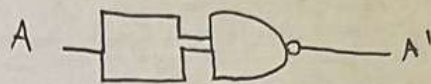
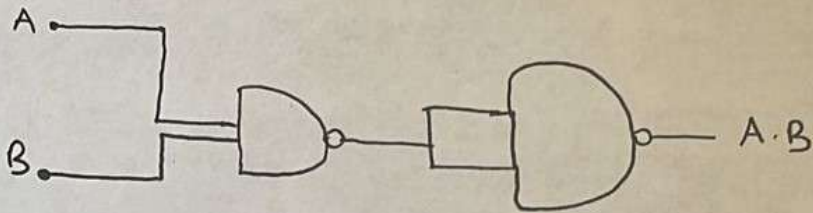


2) Inverter



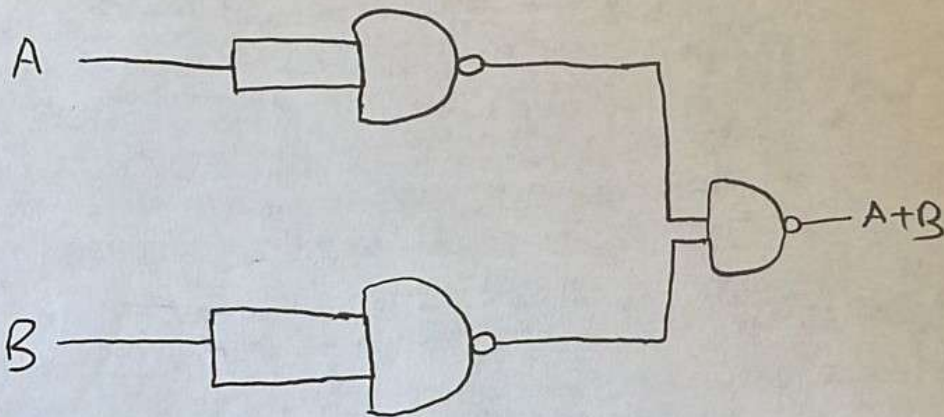
A	X
0	1
1	0

VE KAPISI



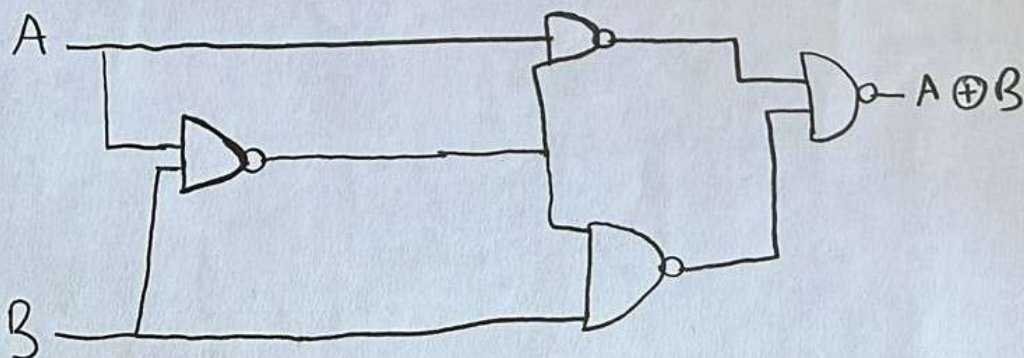
A	B	X
0	0	0
0	1	0
1	0	0
1	1	1

VEYA KAPISI



A	B	X
0	0	0
0	1	1
1	0	1
1	1	1

XOR KAPISI



A	B	X
0	0	0
0	1	1
1	0	1
1	1	0

