Another application of quantum error correction is Not just providing a protection as the data is transmitted through a noisy channel but protect the information as it goes through some gates

-) it turns out correct results can be abtained even if we we are using or faulty gates provided the error probability per gate is below a certain constant threshold.

The basic idea: compute directly on the encoded data (quantum states) in such a way that deceding in never required.

Assuming a simple quantum circuit:

10> H

The error could be seen energative:

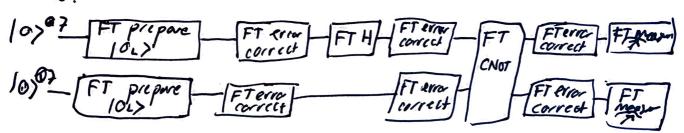
- state initialization

- we transmission of 406its (wires)

- measurement

To overcome these errors, first we replace each qubit with an encoded block of qubits. This is done by an error correcting code, such as 7-qubit steams code, and replace the gates with the gates that work with the encoded data. However this that work not enough since the encoded gates are likely not enough since the encoded gates are likely to produce errors that may not be correctable. Therefore encoded gates should be designed carefully so that they can only propagate errors to small so that they can number of gubits in the encoded data so that they can have corrected. Such gates are called failt tolerant fractions.

A second problem could be that error correction Can also introduce errors but they can be (circult) designal (similar to foult-tolerant gates) so that they do not introduce too many errors in the encoded Jafa



See thebook (Nielsen and Chuang) pp. 478-471 for the design of fault tolerant gates and the threshold theorem.

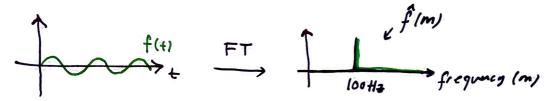
See the back pp. 497-499 for further references on fault therant quantum computing.

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1) Continous Fourier Transform:



2) Discrete Forrier Tronsform:

$$\frac{\text{Cost}}{\text{Fast (FFT)}} \to O(n^2/\frac{1}{2})$$

$$| \frac{1 - qubit}{|W|} = | \frac{1}{|V|} + \frac{1$$

desinc: Rs = (10 o e2711/25), note that R1=Z=(10) R2= (0i), and for large S, e 2171/23~1 and R5~I

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Folkik2k3> = 1 (10> + 22710.k3 11) 8-1- (10>+22710.k2k3) example: n=3 Ø 1/2 (10>+ e27 io. k, k2k3 11>) This suggests how the circuit should be.

For the first 9-bit, we can just apply a Hadonard gate to 1k3), which results is $\frac{1}{\sqrt{2}} (10) + (-1)^{k_3} 11) \text{ and observe that } (-1)^{k_3} = \mathbb{Z}^{2\pi i 0 \cdot k_5}$ For the second qubit,, a pply Hadard gate to 1k2>, obtaining 1 (10) + e2# i0.k211>) then conditioned on 1kg) apply a rotation, Rz. This multiplies 113 with a phase e 2 TT i O.Ok3, producing 1 (10) + R 11))
for the last qubit we apply Hadamand to 1k1) and "pply R2 canditioned on k2 and R3 (anditioned on k3 Which produced $\frac{1}{V_2}$ (10) + $e^{2\pi i \cdot 0.K_1 k_2 k_3 \mid 1 \mid 2 \mid 1}$, we now produced Felk, $k_2 k_3 \mid 2 \mid 1$ (but in wrong order) so we swap qubits 1 and 3.

* at most n gates are applied to each qubit * total o(n2) gates

very close to identify and doe not do much anyway (Rs with s>>logn)

As observed by Coppersmith, "An approximate Farier trays, useful in quantum factoring" (IBM research report) we can safely omit them teeping only O(logn) gates per qubit and O(nlogn) gates overall.

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