Highlights

Multi-objective optimization microchannel heat sink with embedded module with ribs and pin-fins

Xuebin LI, Chunquan LI, Hongyan HUANG, Yuanhao ZHENG

- Microchannel cooling in a Low-Temperature Co-fired Ceramic substrate;
- A comparison with the performance of similar designs;
- Numerical study of three parameters of this microchannel heat sink.

Multi-objective optimization microchannel heat sink with embedded module with ribs and pin-fins

Xuebin LI^{a,**}, Chunquan LI^{a,**}, Hongyan HUANG^{a,*}, Yuanhao ZHENG^a

^a Guilin University Of Electronic Technology, No.1 Jinji Road, Qixing District, Guilin, 541004, the Guangxi Zhuang Autonomous Region, China

Abstract

A new microchannel heat sink with embedded modules with ribs and pin-fins is proposed as an effective solution to realize microchannel heat dissipation within low-temperature co-fired ceramic substrates.

Keywords: Microchannel heat sink, Embedded modules, LTCC, Rib, Pin-fin

1. Introduction

散热重要性

人工智能和第五代移动通信技术等系统技术的发展 [1],推动着半导体行业在移动便携设备、高性能计算机、自动驾驶、物联网和大数据等应用领域的发展 [1],同时也推动着电子芯片向着小型化和高集成化方向发展快速发展 [2]。在过去的几十年里处理器上的晶体管数量依照摩尔定律 [3] 的预测呈现出指数级的增长趋势,如这张 50 年间的微处理器的发展趋势图。摩尔定律与登纳德定律 [4] 引领着半导体行业飞速发展,半导体芯片上电子元件的数量与日俱增,大大提高了芯片的综合性能,但随着半导体芯片性能的提高,尺寸的限制,导致芯片工作时温度的急剧上升,这将对其正常的运行工作产生严重的影响。半导体芯片散热技术的发展已成为制约半导体芯片进一步发展的一大重要原因。

在先进制程方面,随着先进制程技术的飞速发展,推动着芯片的制程越来越接近物理极限,量子隧穿效应开始介入,使得晶体管的漏电现象出现,打破了登纳德定律使得晶体管在更小制程时静态功耗不减反增,产生了更多的热量。芯片的散热问题成为了急需解决的问题。与此同时,摩尔定律从 7 nm 工艺节点以后发展速度放缓,因此先进封装成为后摩尔时代的主力军 [5]。诸多学者提出了许多打破摩尔定律限制的技术,如 2-D IC 集成技术、2.1-D IC 集

成技术 [6-8]、2.3-D IC 集成技术 [9, 10]、2.5-D IC 集成技术 [11-13] 和 3-D IC 集成技术 [14-16] 等。这些高密度封装技术的发展使得微电子器件能够在有限的空间内叠加更多层的芯片以及集成更多的功能模块。功能模块的高密度集成将会导致芯片在工作时发热量更大,使得整体系统的温度上升更大,严重影响芯片的正常工作。

电子设备失效的主要原因有:高温、潮湿、振动和灰尘 [17]。可以发现 55% 的电子设备的失效是由于温度导致。并在 55% 的热失效中有很大一部分是由于温度分布的不均匀所导致。温度分布不均主要有两个原因:一是工质沿着流道温度升高,热边界层变厚,传热系数降低,导致沿流道产生温度梯度,温度分布不均;二是由于发热组件热通量分布不均而导致的温度分布不均。

电子器件散热方式主要分为两种 [18], 一是被动冷却 如:自然对流散热 [19]、热电冷却 [20]、热管冷却。二是主动 冷却如:强迫风冷、液体对流散热 [21]、沸腾散热 [22, 23]、 射流冲击散热 [24, 25]、微通道散热 [26-30]、喷雾散热 [31] 和相变储能冷却[20,32,33]等。在这些散热方式中风冷的 散热能力较低,其散热极限仅为 15 W/cm²,并且受到工作 环境的限制, 因此无法满足工作于特殊环境下的高热流密 度集成电路的需求。传统液体对流散热则是由于其体积过 大,因此应用受到空间限制。热电制冷则受制于热电材料 的发展,尚未应用于高热流密度电子设备中。而射流冲击 与喷雾冷却结构复杂,对于空间的要求高,不适合于对芯 片的散热。相变储能冷却则具有时间迟滞性,相变材料具 有不易封装的缺点,不适用于高热流密度的场景。本文研 究内容主要为针对于功率芯片进行散热, 而微流道散热器 具有结构紧凑、对流换热面积大、重量轻的优点, 相对于 上述的其他技术,微通道散热器实用性更强,更能满足研

^{*}Corresponding author

^{**}These authors contributed to the work equllly and should be regarded as co-first authors.

 $Email\ address: \ {\tt hhy7844@guet.edu.cn}\ ({\tt Hongyan\ HUANG})$

 $^{^{1}\}mathrm{This}$ is the specimen author footnote.

究内容的需求。

综上所述,当前半导体行业对散热技术发展有着迫切的需求。先进制程技术的快速发展推动了芯片的小型化和高集成化,但同时也导致芯片工作时温度的急剧上升,影响了芯片正常运行。高热流密度集成电路的发展也加剧了散热问题。在传统的散热方式如风冷和液体对流散热等存在技术限制的情况下,微通道散热器应运而生,因其结构紧凑、对流换热面积大、重量轻等优点,更适用于高热流密度集成电路的散热需求。但是,尽管微通道散热器具有更优的散热性能,但在某些情况下,比如 LTCC 基板中,仍然无法满足芯片高热流密度引起的散热需求。因此,半导体行业仍需不断推动散热技术的发展,以满足不断增长的芯片散热需求。

本次研究提出的基于嵌入式散热模块的微通道散热技术,解决了工程项目中 LTCC 基板上由于芯片高热流密度所引起的散热问题,对集成电路、人工智能和第五代移动通信技术等应用领域的发展有着积极的意义。该项技术具有散热性能高、温度均匀性好、可应用于多热源散热等优点,可有效增强芯片的散热性能,提高芯片的可靠性。在半导体行业当前对芯片小型化和高集成化的需求下,该研究工作具有重要的实用意义和发展前景。在此基础上作者通过一系列研究,提出了基于 MC-RPF 的多热源散热结构,并进行了优化设计和综合分析,为电子芯片散热问题的解决提供一种有效的技术方案和参考。

2. Mathematical modeling of the microchannel heat sink

shows the schematic design of MCHS-SR and the proposed MCHS-RPFEM.

3. Numerical method

In this study, the following assumptions were made to simplify the numerical model

- 1. Newtonian fluid flow and steady laminar flow are used, and the fluid follows the Hagen-Poiseuille equation.
- 2. The walls of the channel are rigid.
- 3. Neglecting the effects of interaction forces, viscous heat, surface tension, and radiative heat transfer.
- 3.1. Governing equations and boundary conditions

 The governing equations are as follow:

Continuity equation:

$$\nabla \cdot \left(\rho_f \vec{u} \right) = 0 \tag{1}$$

Momentum equation:

$$\vec{u} \cdot \nabla \left(\rho_f \vec{u} \right) = -\nabla p + \nabla \cdot \left(\mu_f \nabla \vec{u} \right) \tag{2}$$

Energy equation for the fluid domain:

$$\vec{u} \cdot \nabla \left(\rho_f C_f T_f \right) = \nabla \cdot \left(k_f \nabla T_f \right) \tag{3}$$

Energy conservation equation for the solid domain:

$$\nabla \left(k_s \nabla T_s \right) = 0 \tag{4}$$

where T is the temperature (${}^{\circ}C$)

- 3.2. Data reduction
- 3.3. Grid independence

4. Results and discussion

- 4.1. Numerical validations
- 4.2. Effect of geometric prameters on hydrothermal performance

In order to explore the influence of the geometric parameters of MCHS-RPFEM on the flow and heat transfer performance, several parameters were selected for research: the relative rib height (α) , the relative pin-fin height (β) , and the relative number of auxiliary channels (γ) .

4.2.1. The effect of relative rib height (α)

The ratio of rib height to channel height is defined as relative rib height:

4.2.2. Performance analysis

5. Conclusion

To investigate the effect of

Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

Nomenclatu	ire		
$egin{array}{l} A \ H_{pf} \ H_{rib} \ L_{rib} \ N_{mc} \ N_{oc} \ Re \ S_{pf} \ W_{rib} \ d_{rib} \ \end{array}$	area on the chip's upper surface (m^2) height of the pin-fin (μm) height of the rib (μm) length of the rib (μm) number of main channels number of auxiliary channels Reynolds number Side length of the pin-fin (μm) width of the rib (μm) distance between the rib (μm)	ch env f in max mc oc pf rib s	channel environmental fluid inlet maximum value main channels auxiliary channels pin-fin rib solid
Greek symb α β γ Λ μ_f ρ_f θ Subscripts ave c	Relative rib height Relative pin-fin height Relative number of auxiliary channels molecule Knudsen number of water (m) dynamic viscosity of fluid $(kg/(m \cdot s))$ density of fluid (kg/m^3) MATD (Mean Absolute Temperature Deviation) (K)	Abbreviation LED LTCC MATD MCHS MCHS-SR MCHS-REM MCHS-PFEM MCHS-RPFEM	light-emitting diod Low temperature cofired ceramic mean absolute temperature deviation microchannel heat sink straight rectangular microchannel microchannels with rib embedded mod- ule

Table 1: MCHS-RPFEM geometric parameter table

Geometrical parameters	W_{rib}	H_{rib}	d_{rib}	S_{pf}	H_{pf}	H_{ch}
Value, µm	400	1000	1600	300	1000	1000

Formatting of funding sources

This research did not receive any specific grant from funding agencies in the public, commercial, or not-forprofit sectors.

References

- J. H. Lau, Recent Advances and Trends in Advanced Packaging, IEEE Transactions on Components, Packaging and Manufacturing Technology 12 (2) (2022) 228–252.
- [2] H. Sadique, Q. Murtaza, Samsher, Heat transfer augmentation in microchannel heat sink using secondary flows: A review, International Journal of Heat and Mass Transfer 194 (2022) 123063.
- [3] H. Tan, P. Du, K. Zong, G. Meng, X. Gao, Y. Li, Investigation on the temperature distribution in the two-phase spider netted microchannel network heat sink with non-uniform heat flux, International Journal of Thermal Sciences 169 (2021) 107079.
- [4] 刘一凡, 张志勇, 后摩尔时代的碳基电子技术: 进展、应用与挑战, 物理学报 71 (6) (2022) 7-42.

- [5] 王若达, 先进封装推动半导体产业新发展, 中国集成电路 31 (4) (2022) 26-29+42.
- [6] J. H. Lau, Recent Advances and Trends in Multiple System and Heterogeneous Integration With TSV Interposers, IEEE Transactions on Components, Packaging and Manufacturing Technology 13 (1) (2023) 3–25.
- [7] W.-C. Chen, C.-W. Lee, H.-C. Kuo, M.-H. Chung, C.-C. Wang, S.-K. Huang, Y.-S. Liao, C.-C. Wang, D. Tarng, Development of Novel Fine Line 2.1 D Package with Organic Interposer Using Advanced Substrate-Based Process, in: 2018 IEEE 68th Electronic Components and Technology Conference (ECTC), 2018, pp. 601–606.
- [8] C.-Y. Huang, Y.-H. Hsu, Y.-J. Lu, K.-H. Yu, W.-S. Tsai, C.-F. Lin, C. K. Chung, Analysis of Warpage and Stress Behavior in a Fine Pitch Multi-Chip Interconnection with Ultrafine-Line Organic Substrate (2.1D), in: 2018 IEEE 68th Electronic Components and Technology Conference (ECTC), 2018, pp. 631–637.
- [9] S. Zhang, C. L. Gan, P. He, K.-W. Paik, Guest editorial: Heterogeneous integration and chiplets interconnection, Microelectronics International 40 (2) (2023) 69–69.
- [10] J. H. Lau, G. C.-F. Chen, J. Y.-C. Huang, R. T.-S. Chou, C. C.-L. Yang, H.-N. Liu, T.-J. Tseng, Hybrid Substrate by Fan-Out RDL-First Panel-Level Packaging, IEEE Transactions on Components, Packaging and Manufacturing Technology 11 (8) (2021) 1301–1309.
- [11] H. Cai, S. Ma, W. Wang, Y. Jin, J. Chen, J. Zhang, W. Xiang, L. Hu, S. He, Thermal and Electrical Characterization of TSV

$$\rho_f(T) = 999.9 + 9.561 \times 10^{-2} T - 1.013 \times 10^{-2} T^2 + 8.459 \times 10^{-5} T^3 - 3.496 \times 10^{-7} T^4$$
(5)

$$C_f(T) = 4217 - 3.452T + 1.155 \times 10^{-1}T^2 - 1.862 \times 10^{-3}T^3 + 1.538 \times 10^{-5}T^4 - 4.850 \times 10^{-8}T^5$$
 (6)

$$k_f(T) = 5.698 \times 10^{-1} + 1.772 \times 10^{-3}T - 4.870 \times 10^{-6}T^2 - 2.915 \times 10^{-8}T^3 + 1.094 \times 10^{-10}T^4$$
(7)

$$\mu_f(T) = 1.750 \times 10^{-3} - 5.558 \times 10^{-5}T + 1.172 \times 10^{-6}T^2 - 1.579 \times 10^{-8}T^3 + 1.169 \times 10^{-10}T^4 - 3.535 \times 10^{-13}T^5$$
(8)

- Interposer Embedded with Microchannel for 2.5D Integration of GaN RF Devices, in: 2018 IEEE 68th Electronic Components and Technology Conference (ECTC), 2018, pp. 2156–2162.
- [12] S. Bhuvanendran Nair Gourikutty, Y. Meng Chow, J. Alton, R. Bhimrao Umralkar, H. Bai, K. Keng Chua, S. Bhattacharya, Defect Localization in Through-Si-Interposer Based 2.5D ICs, in: 2020 IEEE 70th Electronic Components and Technology Conference (ECTC), 2020, pp. 1180–1185.
- [13] Q. Ding, H. Liu, Y. H. Yew, J. Jiang, High Bandwidth Low Power 2.5D Interconnect Modeling and Design, in: 2020 IEEE 70th Electronic Components and Technology Conference (ECTC), 2020, pp. 1832–1837.
- [14] B. Ding, Z.-H. Zhang, L. Gong, C.-Y. Zhu, M.-H. Xu, Coupling management optimization of temperature and thermal stress inside 3D-IC with multi-cores and various power density, International Communications in Heat and Mass Transfer 120 (2021) 105021.
- [15] J. Huang, Z. He, H. Gutierrez, L. Zhao, X. Lu, Heat Dissipation Derivation and Optimization of the Fan-Out 3-D Package Model, IEEE Transactions on Components, Packaging and Manufacturing Technology 11 (9) (2021) 1461–1470.
- [16] S. D. Marshall, P. S. Lee, 3D topology optimisation of liquidcooled microchannel heat sinks, Thermal Science and Engineering Progress 33 (2022) 101377.
- [17] Z. He, Thermal management and temperature uniformity enhancement of electronic devices by micro heat sinks: A review, Energy 216 (2021) 119223.
- [18] 齐文亮, 赵亮, 王婉人, 刘琦, 高热流密度电子设备液冷技术研究进展, 科学技术与工程 22 (11) (2022) 4261-4270.
- [19] I. El Ghandouri, A. El Maakoul, S. Saadeddine, M. Meziane, Design and numerical investigations of natural convection heat transfer of a new rippling fin shape, Applied Thermal Engineering 178 (2020) 115670.
- [20] 段斐帆, 涂淑平, 电子设备散热的新技术, 工业加热 50 (11) (2021) 63-68.
- [21] N. G. Patil, T. K. Hotta, A Combined Liquid Cold Plate and Heat Sink Based Hybrid Cooling Approach for the Temperature Control of Integrated Circuit Chips, Journal of Thermal Science and Engineering Applications (2022) 1–32.
- [22] B. Markal, B. Kul, M. Avci, R. Varol, Effect of gradually expanding flow passages on flow boiling of micro pin fin heat sinks,

- International Journal of Heat and Mass Transfer 197 (2022) 123355
- [23] B. Markal, B. Kul, Combined influence of artificial nucleation site and expanding cross section on flow boiling performance of micro pin fins, International Communications in Heat and Mass Transfer 135 (2022) 106081.
- [24] 吕静,黄伶俐,刘洪芝,吕航昶,冲击射流冷却高功率电子元件的试验研究,流体机械49(6)(2021)1-8.
- [25] Z. Li, J. Sun, J. Li, B. Fan, M. Zhong, N. Jiang, W. Wang, Y. Wang, Flow and heat transfer performance of array finned channel coupled jet heat sink, Applied Thermal Engineering 221 (2023) 119813.
- [26] U. Manda, Y. Peles, S. Putnam, Comparison of heat transfer characteristics of flow of supercritical carbon dioxide and water inside a square microchannel, in: 2021 20th IEEE Intersociety Conference on Thermal and Thermomechanical Phenomena in Electronic Systems (iTherm), 2021, pp. 1207–1213.
- [27] F. Zhang, B. Wu, B. Du, Heat transfer optimization based on finned microchannel heat sink, International Journal of Thermal Sciences 172 (2022) 107357.
- [28] D. Ansari, K.-Y. Kim, Hotspot management using a hybrid heat sink with stepped pin-fins, Numerical Heat Transfer, Part A: Applications 75 (6) (2019) 359–380.
- [29] D. Ansari, K.-Y. Kim, Hotspot thermal management using a microchannel-pinfin hybrid heat sink, International Journal of Thermal Sciences 134 (2018) 27–39.
- [30] Z. Soleymani, M. Rahimi, M. Gorzin, Y. Pahamli, Performance analysis of hotspot using geometrical and operational parameters of a microchannel pin-fin hybrid heat sink, International Journal of Heat and Mass Transfer 159 (2020) 120141.
- [31] N. Zhou, H. Feng, Y. Guo, H. Chen, W. Liu, H. Peng, Y. Lei, S. Deng, Y. Xu, Experimental study on the spray cooling heat transfer performance and dimensionless correlations for ethylene glycol water solution, Applied Thermal Engineering 214 (2022) 118824.
- [32] 王苑瑾, 张赛, 刘兵, 朱正鹏, 杨文良, 基于高性能信息处理模块的相变散热技术研究, 航天控制 38 (1) (2020) 51-56.
- [33] R. Kalbasi, Introducing a novel heat sink comprising PCM and air - Adapted to electronic device thermal management, International Journal of Heat and Mass Transfer 169 (2021) 120914.