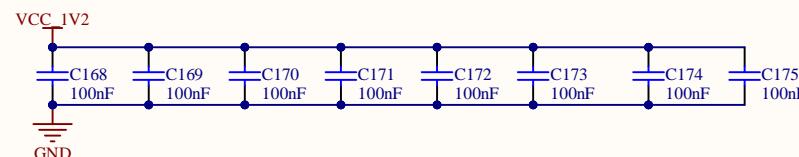
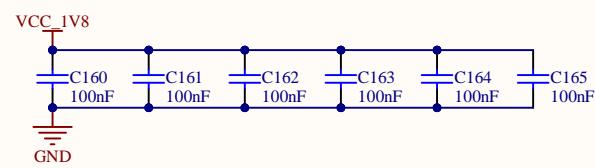
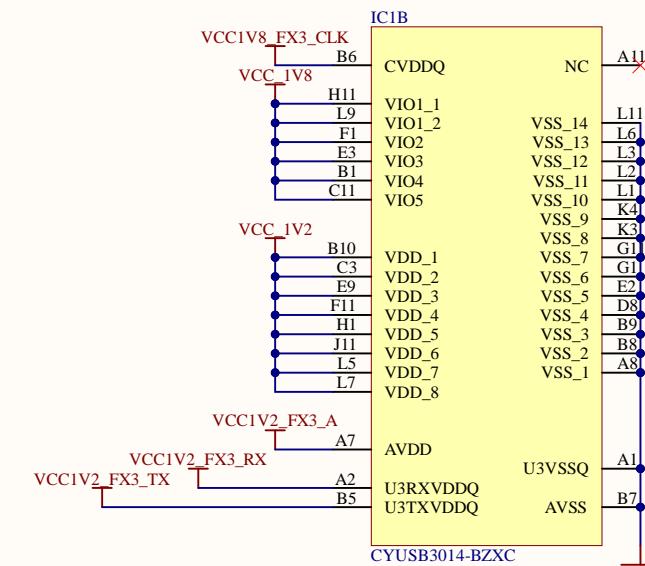
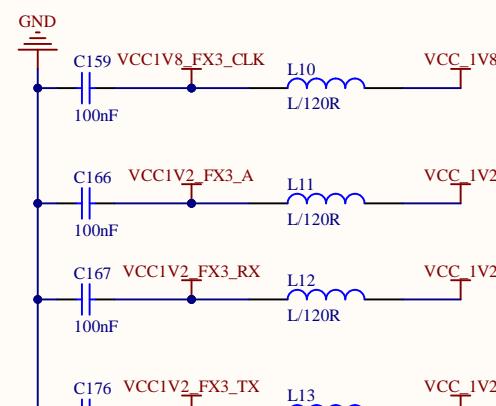
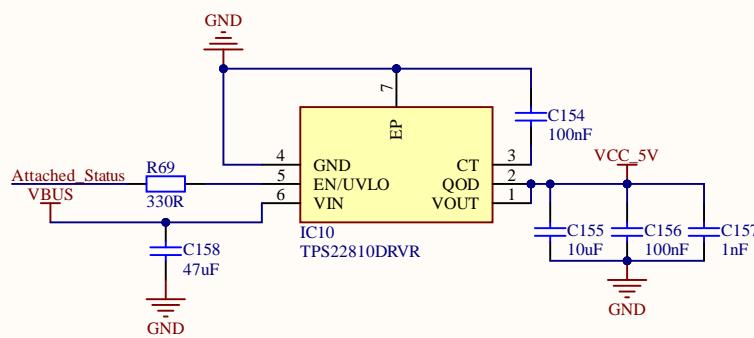
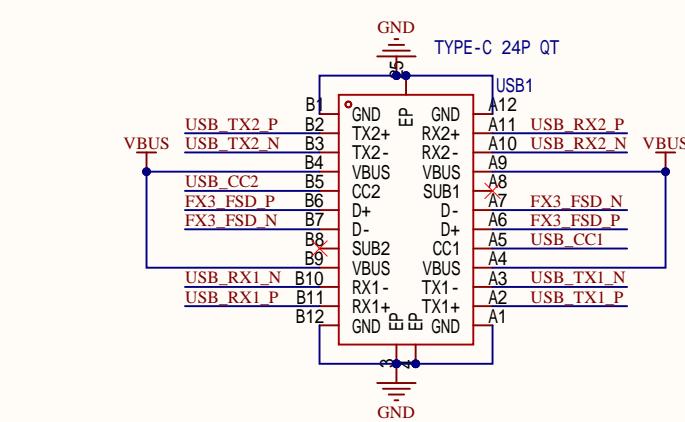


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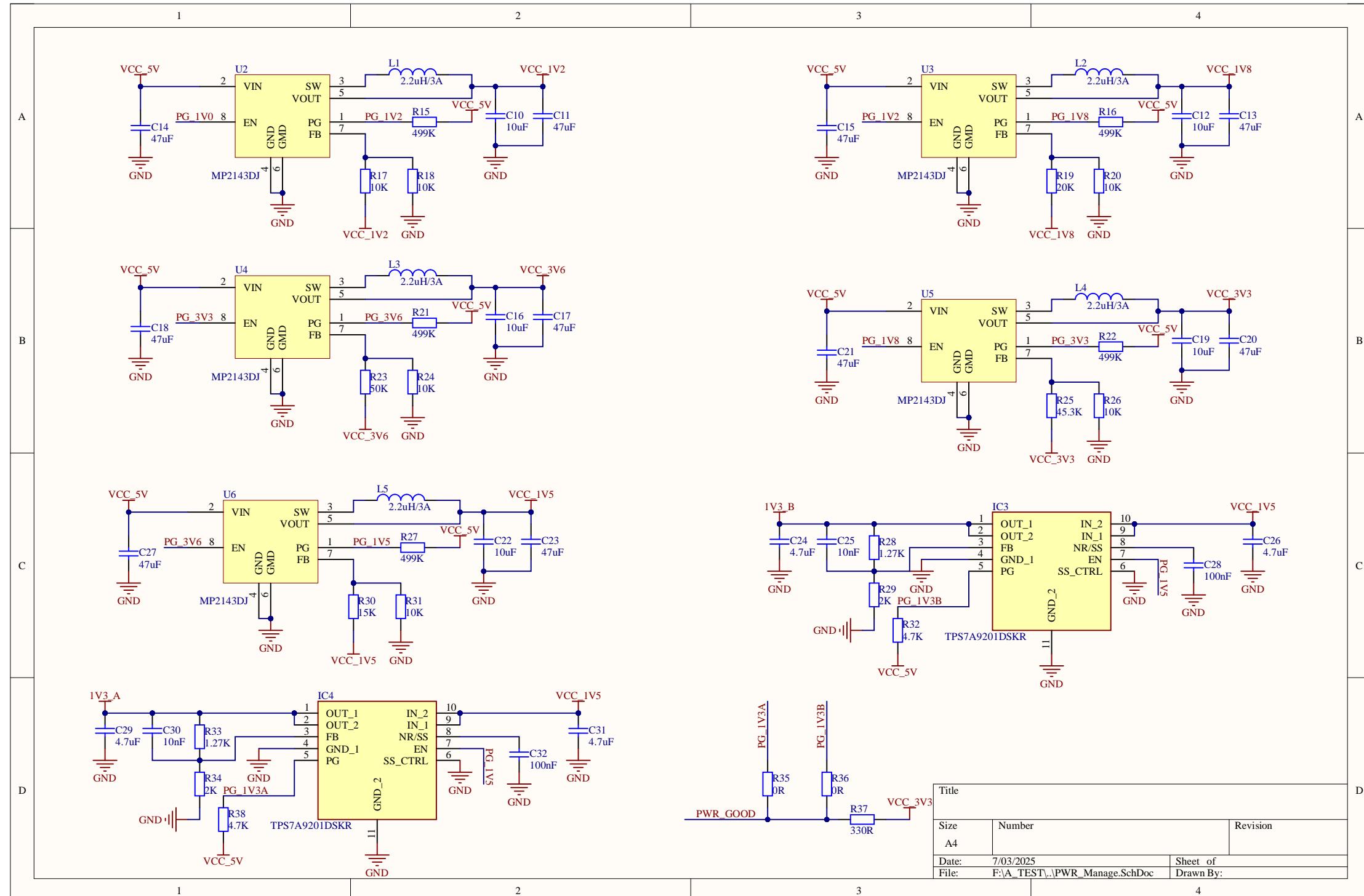
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Size	Number	Revision
A4		
Date: 7/03/2025	Sheet of	
File: F:\A_TEST\..\USB3_PWR.SchDoc		Drawn By:

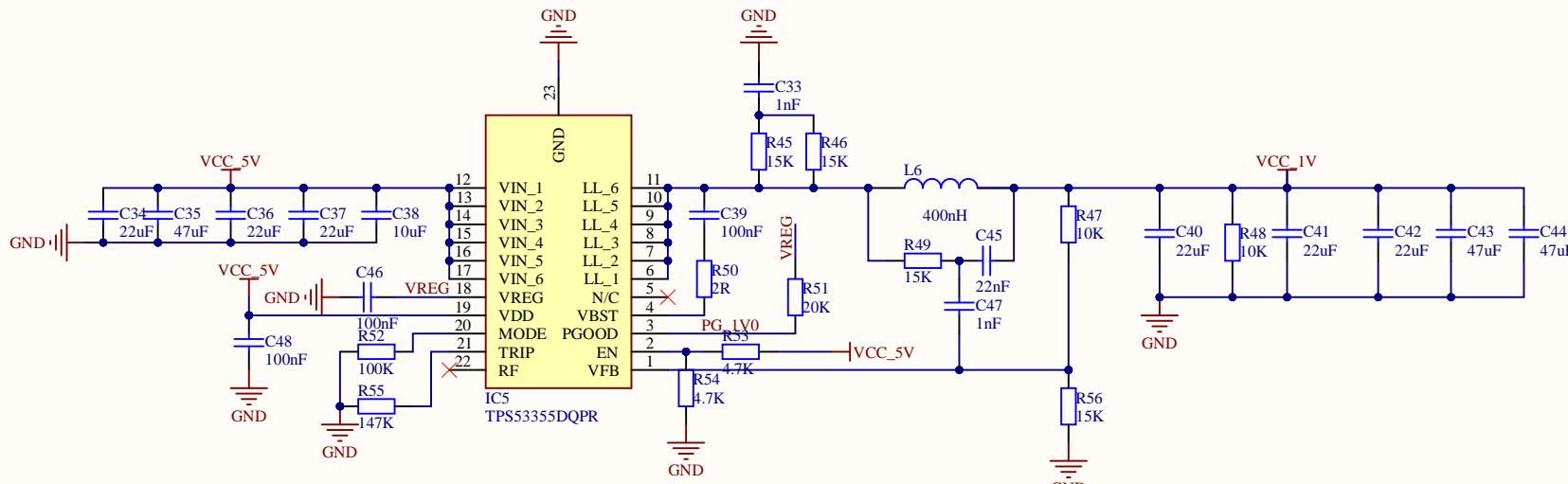
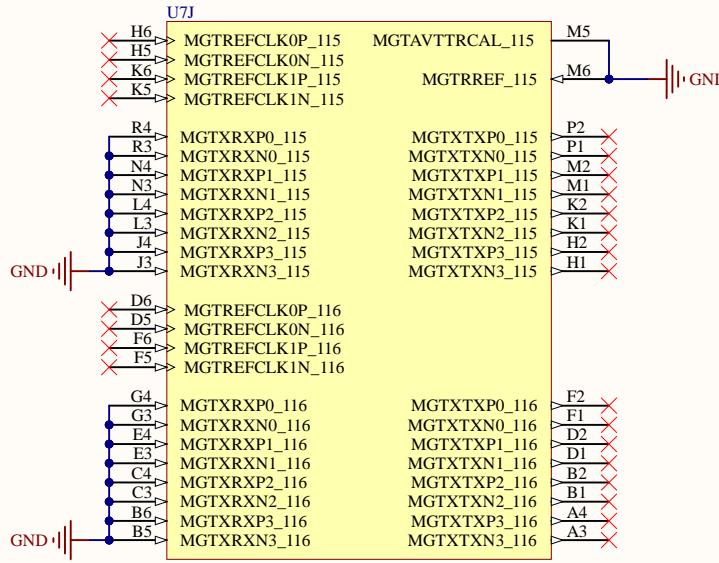
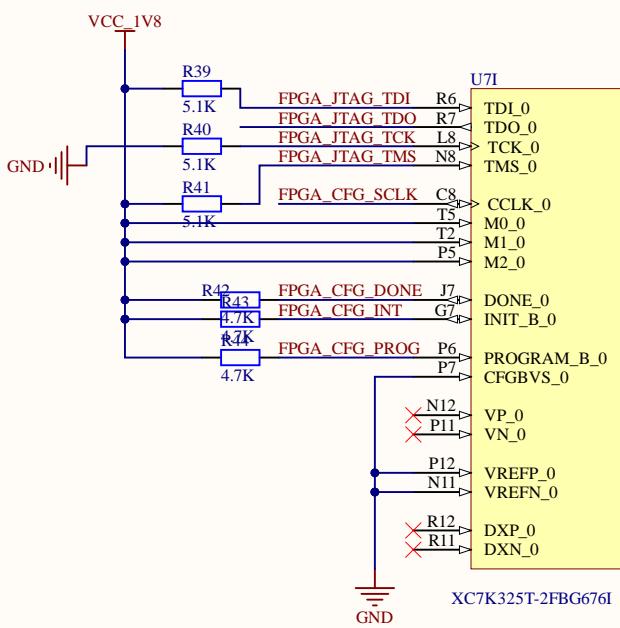
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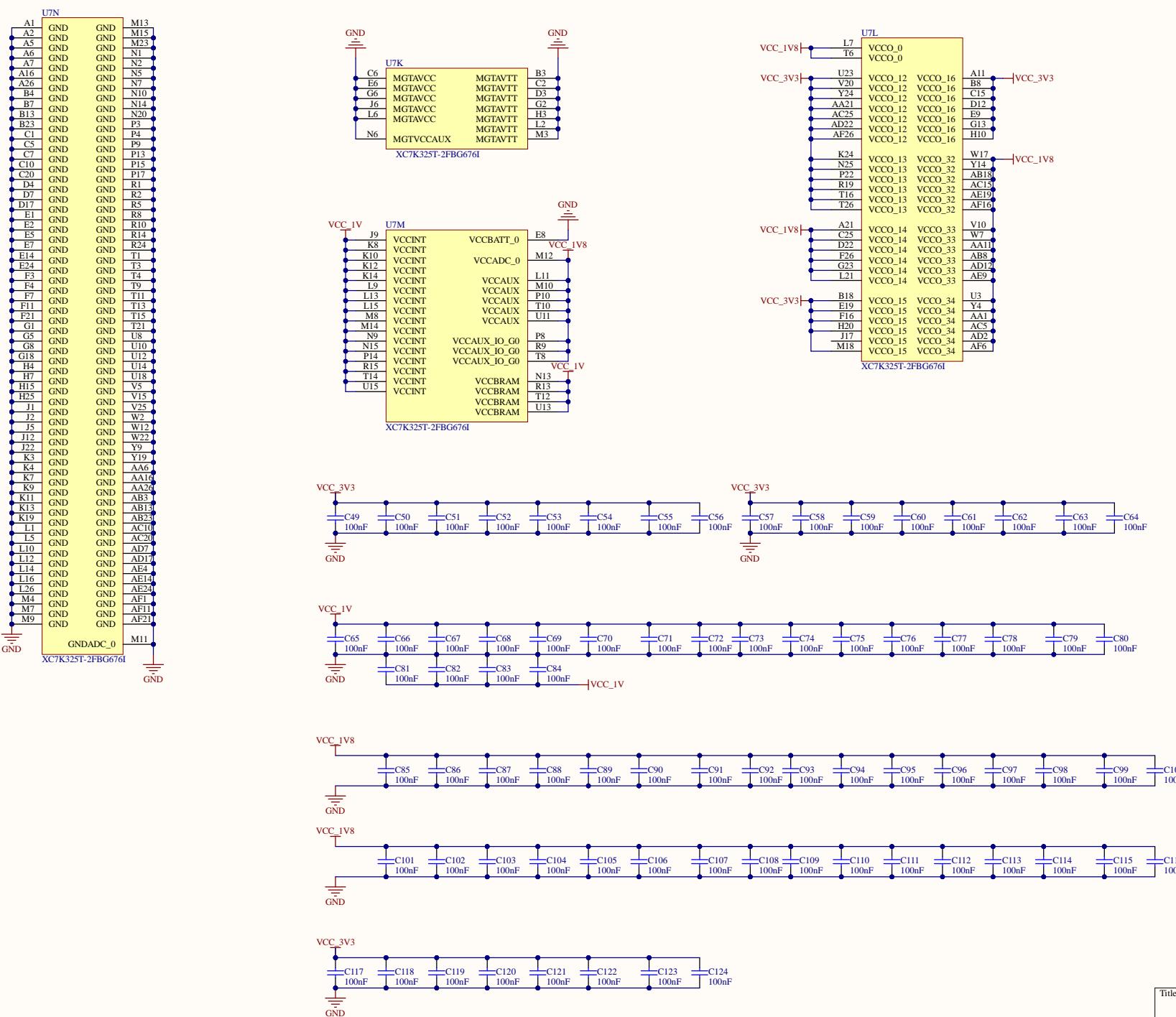
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Title		
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Date:	7/03/2025	Sheet of
File:	E:\A\TEST\..\FPGA\Config\SchDoc	Drawn By:



Title		
Size	Number	Revision
A3		
Date:	7/03/2025	Sheet of
File:	E:\A\TEST\AFPGA_PWP.SchDoc	Design By:

BANK 12

IO_0_12 U21
 IO_L1P_T0_I2 U22
 IO_L1N_T0_I2 V22
 IO_L2P_T0_I2 U24
 IO_L2N_T0_I2 U25
 IO_L3P_T0_DQS_I2 V23
 IO_L3N_T0_DQS_I2 V24
 IO_L4P_T0_I2 U26
 IO_L4N_T0_I2 V26
 IO_L5P_T0_I2 W25
 IO_L5N_T0_I2 W26
 IO_L6P_T0_I2 V21
 IO_L6N_T0_VREF_I2 W21
 IO_L7P_T1_I2 AA25
 IO_L7N_T1_I2 AB25
 IO_L8P_T1_I2 W23
 IO_L8N_T1_I2 W24
 IO_L9P_T1_DQS_I2 AB26
 IO_L9N_T1_DQS_I2 AC26
 IO_L10P_T1_I2 Y25
 IO_L10N_T1_I2 Y26
 IO_L11P_T1_SRCC_I2 AA23
 IO_L11N_T1_SRCC_I2 AB24
 IO_L12P_T1_MRCC_I2 Y23
 IO_L12N_T1_MRCC_I2 AA24
 IO_L13P_T2_MRCC_I2 Y22
 IO_L13N_T2_MRCC_I2 AA22
 IO_L14P_T2_SRCC_I2 AC23
 IO_L14N_T2_SRCC_I2 W20
 IO_L15P_T2_DQS_I2 Y21
 IO_L15N_T2_DQS_I2 AD23
 IO_L16P_T2_I2 AD24
 IO_L16N_T2_I2 AB22
 IO_L17P_T2_I2 AC22
 IO_L17N_T2_I2 AB21
 IO_L18P_T2_I2 AC21
 IO_L18N_T2_I2 AD21
 IO_L19P_T3_I2 AE21
 IO_L19N_T3_VREF_I2 AF24
 IO_L20P_T3_I2 AF25
 IO_L20N_T3_I2 AD26
 IO_L21P_T3_DQS_I2 AE26
 IO_L21N_T3_DQS_I2 AE23
 IO_L22P_T3_I2 AF23
 IO_L22N_T3_I2 AD25
 IO_L23P_T3_I2 AF25
 IO_L23N_T3_I2 AE22
 IO_L24P_T3_I2 AF22
 IO_L24N_T3_I2 AF22
 IO_25_I2 Y20

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BANK 13

IO_0_13 N16
 IO_L1P_T0_I2 K25
 IO_L1N_T0_I2 K26
 IO_L2P_T0_I2 R26
 IO_L2N_T0_I2 P26
 IO_L3P_T0_DQS_I3 M25
 IO_L3N_T0_DQS_I3 L25
 IO_L4P_T0_I3 P24
 IO_L4N_T0_I3 N24
 IO_L5P_T0_I3 M26
 IO_L5N_T0_I3 M26
 IO_L6P_T0_I3 R25
 IO_L6N_T0_VREF_I3 P25
 IO_L7P_T1_I3 N19
 IO_L7N_T1_I3 M20
 IO_L8P_T1_I3 M24
 IO_L8N_T1_I3 L24
 IO_L9P_T1_DQS_I3 P19
 IO_L9N_T1_DQS_I3 P20
 IO_L10P_T1_I3 M21
 IO_L10N_T1_I3 M22
 IO_L11P_T1_SRCC_I3 P23
 IO_L11N_T1_SRCC_I3 N23
 IO_L12P_T1_MRCC_I3 N21
 IO_L12N_T1_MRCC_I3 N22
 IO_L13P_T2_MRCC_I3 R21
 IO_L13N_T2_MRCC_I3 R22
 IO_L14P_T2_SRCC_I3 R23
 IO_L14N_T2_SRCC_I3 T24
 IO_L15P_T2_DQS_I3 T25
 IO_L15N_T2_DQS_I3 T20
 IO_L16P_T2_I3 R20
 IO_L16N_T2_I3 T22
 IO_L17P_T2_I3 T23
 IO_L17N_T2_I3 T19
 IO_L18P_T2_I3 T20
 IO_L18N_T2_I3 T18
 IO_L19P_T3_I3 T19
 IO_L19N_T3_VREF_I3 P16
 IO_L20P_T3_I3 N17
 IO_L20N_T3_I3 R16
 IO_L21P_T3_DQS_I3 R17
 IO_L21N_T3_DQS_I3 N18
 IO_L22P_T3_I3 M19
 IO_L22N_T3_I3 U17
 IO_L23P_T3_I3 T17
 IO_L23N_T3_I3 R18
 IO_L24P_T3_I3 P18
 IO_L24N_T3_I3 U16
 IO_25_I3 U16

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BANK 14

IO_0_14 E21
 IO_L1P_T0_D00_MOSI_I4 E24
 IO_L1N_T0_D01_DIN_I4 A25
 IO_L2P_T0_D02_I4 E22
 IO_L2N_T0_D03_I4 A22
 IO_L3P_T0_DQS_PUDC_B_I4 B25
 IO_L3N_T0_DQS_EMCCLK_I4 B26
 IO_L4P_T0_D04_I4 A23
 IO_L4N_T0_D04_I4 A24
 IO_L5P_T0_D04_I4 C26
 IO_L5N_T0_D07_I4 C23
 IO_L6P_T0_FCS_B_I4 C24
 IO_L6N_T0_D08_VREF_I4 D21
 IO_L7P_T1_D05_I4 C22
 IO_L7N_T1_D10_I4 B20
 IO_L8P_T1_D11_I4 A20
 IO_L8N_T1_D12_I4 F21
 IO_L9P_T1_DQS_I4 F22
 IO_L10P_T1_D13_I4 F21
 IO_L10N_T1_D14_I4 B21
 IO_L11N_T1_D15_I4 D23
 IO_L11P_T1_SRCC_I4 D24
 IO_L11N_T1_SRCC_I4 F22
 IO_L12P_T1_MRCC_I4 F23
 IO_L12N_T1_MRCC_I4 G22
 IO_L13P_T2_MRCC_I4 F23
 IO_L14P_T2_SRCC_I4 G24
 IO_L14N_T2_SRCC_I4 F24
 IO_L15P_T2_DQS_RDWB_B_I4 D25
 IO_L15N_T2_DQS_DOUT_CSO_B_I4 G25
 IO_L16P_T2_CSI_B_I4 G26
 IO_L16N_T2_A15_D31_I4 G25
 IO_L17P_T2_A11_D30_I4 F26
 IO_L17N_T2_A13_D29_I4 F26
 IO_L18P_T2_A12_D28_I4 H26
 IO_L18N_T2_A11_D27_I4 H21
 IO_L19P_T3_A10_D26_I4 G21
 IO_L19N_T3_A09_D25_VREF_I4 H23
 IO_L20P_T3_A08_D24_I4 H24
 IO_L20N_T3_A07_D23_I4 H21
 IO_L21P_T3_DQS_I4 H22
 IO_L21N_T3_DQS_A06_D22_I4 H24
 IO_L22P_T3_A05_D21_I4 H25
 IO_L22N_T3_A04_D20_I4 H22
 IO_L23P_T3_A03_D19_I4 H22
 IO_L23N_T3_A02_D18_I4 H23
 IO_L24P_T3_A01_D17_I4 H23
 IO_L24N_T3_A00_D16_I4 H23
 IO_25_I4 H23

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BANK 15

IO_0_15 K15
 IO_L1P_T0_AD0P_I5 C16
 IO_L1N_T0_AD0N_I5 A18
 IO_L2P_T0_AD0P_I5 A19
 IO_L2N_T0_AD0N_I5 A17
 IO_L3P_T0_DQS_AD1P_I5 B17
 IO_L3N_T0_DQS_AD1N_I5 C19
 IO_L4P_T0_AD9P_I5 B19
 IO_L4N_T0_AD9N_I5 C17
 IO_L5P_T0_AD2P_I5 C18
 IO_L5N_T0_AD2N_I5 D15
 IO_L6P_T0_I5 D16
 IO_L6N_T0_VREF_I5 H16
 IO_L7P_T1_AD10P_I5 G16
 IO_L7N_T1_AD10N_I5 G16
 IO_L8P_T1_AD3P_I5 G15
 IO_L8N_T1_AD3N_I5 F15
 IO_L9P_T1_DQS_AD11P_I5 Q15
 IO_L9N_T1_DQS_AD11N_I5 Q16
 IO_L10P_T1_AD4P_I5 E15
 IO_L10N_T1_AD4N_I5 E16
 IO_L11P_T1_SRCC_AD12P_I5 G17
 IO_L11N_T1_SRCC_AD12N_I5 F17
 IO_L12P_T1_MRCC_AD5P_I5 E17
 IO_L12N_T1_MRCC_AD5N_I5 E18
 IO_L13P_T2_MRCC_I5 D18
 IO_L13N_T2_MRCC_I5 D18
 IO_L14P_T2_SRCC_I5 H17
 IO_L14N_T2_SRCC_I5 H18
 IO_L15P_T2_DQS_I5 D19
 IO_L15N_T2_DQS_ADV_B_I5 G19
 IO_L16P_T2_A28_I5 F20
 IO_L16N_T2_A27_I5 F19
 IO_L17P_T2_A26_I5 F20
 IO_L17N_T2_A25_I5 H19
 IO_L18P_T2_A24_I5 G20
 IO_L18N_T2_A23_I5 K20
 IO_L19P_T3_A22_I5 J20
 IO_L19N_T3_A21_VREF_I5 J18
 IO_L20P_T3_A20_I5 J19
 IO_L20N_T3_A19_I5 L19
 IO_L21P_T3_DQS_I5 L20
 IO_L21N_T3_DQS_A18_I5 K16
 IO_L22P_T3_A17_I5 K17
 IO_L22N_T3_A16_I5 M17
 IO_L23P_T3_FOE_B_I5 L18
 IO_L23N_T3_FWE_B_I5 L18
 IO_L24P_T3_RS1_I5 L17
 IO_L24N_T3_RS0_I5 K18
 IO_25_I5 M16

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BANK 16

IO_0_16 J8
 IO_L1P_T0_I6 J9
 IO_L1N_T0_I6 J10
 IO_L2P_T0_I6 J9
 IO_L2N_T0_I6 J10
 IO_L3P_T0_DQS_I6 J13
 IO_L3N_T0_DQS_I6 J13
 IO_L4P_T0_I6 J11
 IO_L4N_T0_I6 J10
 IO_L5P_T0_I6 J14
 IO_L5N_T0_I6 H12
 IO_L6P_T0_I6 H11
 IO_L6N_T0_VREF_I6 J11
 IO_L7P_T1_I6 F14
 IO_L7N_T1_I6 F15
 IO_L8P_T1_I6 D9
 IO_L8N_T1_I6 D8
 IO_L9P_T1_DQS_I6 A9
 IO_L9N_T1_DQS_I6 A8
 IO_L10P_T1_I6 C9
 IO_L10N_T1_I6 B9
 IO_L11P_T1_SRCC_I6 G11
 IO_L11N_T1_SRCC_I6 F10
 IO_L12P_T1_MRCC_I6 E10
 IO_L12N_T1_MRCC_I6 D10
 IO_L13P_T2_MRCC_I6 C12
 IO_L13N_T2_MRCC_I6 C11
 IO_L14P_T2_SRCC_I6 E11
 IO_L14N_T2_SRCC_I6 D11
 IO_L15P_T2_DQS_I6 F14
 IO_L15N_T2_DQS_I6 F13
 IO_L16P_T2_I6 G12
 IO_L16N_T2_I6 F12
 IO_L17P_T2_I6 D14
 IO_L17N_T2_I6 D13
 IO_L18P_T2_I6 E13
 IO_L18N_T2_I6 E12
 IO_L19P_T3_I6 C14
 IO_L19N_T3_VREF_I6 C13
 IO_L20P_T3_I6 B12
 IO_L20N_T3_I6 B11
 IO_L21P_T3_DQS_I6 B14
 IO_L21N_T3_DQS_I6 A14
 IO_L22P_T3_I6 B10
 IO_L22N_T3_I6 A10
 IO_L23P_T3_I6 B15
 IO_L23N_T3_I6 A15
 IO_L24P_T3_I6 A13
 IO_L24N_T3_I6 A12
 IO_25_I6 J14

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BANK 17

Title		
Size	Number	Revision
A3		
Date:	7/03/2025	Sheet of
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BANK 32

U7F
A
B
C
D

IO_0_VRN_32 V13
 IO_L1P_T0_32 AE17
 IO_LIN_T0_32 AF17
 IO_L2P_T0_32 AF14
 IO_L2N_T0_32 AF15
 IO_L3P_T0_DQS_32 AE18
 IO_L3N_T0_DQS_32 AF18
 IO_L4P_T0_32 AD15
 IO_L4N_T0_32 AE15
 IO_L5P_T0_32 AF19 **RF TX D0 P**
 IO_L5N_T0_32 AF20 **RF TX D0 N**
 IO_L6P_T0_32 AD16 **RF TX D2 P**
 IO_L6N_T0_VREF_32 AE16 **RF TX D2 N**
 IO_L7P_T1_32 AA14 **RF RX D2 P**
 IO_L7N_T1_32 AA15 **RF RX D2 N**
 IO_L8P_T1_32 AC14 **RF TX D4 P**
 IO_L8N_T1_32 AD14 **RF TX D4 N**
 IO_L9P_T1_DQS_32 Y15 **RF RX D4 P**
 IO_L9N_T1_DQS_32 AB14 **RF TX D3 P**
 IO_L10P_T1_32 AB15 **RF TX D3 N**
 IO_L10N_T1_32 AA17 **FPGA CLK P**
 IO_L11P_T1_SRCC_32 AA18 **FPGA CLK N**
 IO_L11N_T1_SRCC_32 AB16 **RF DATA CLK P**
 IO_L12P_T1_MRCC_32 AC16 **RF DATA CLK N**
 IO_L12N_T1_MRCC_32 AC17 **RF FB CLK P**
 IO_L13P_T2_MRCC_32 AD18 **RF FB CLK N**
 IO_L13N_T2_MRCC_32 AB17 **RF TX FRAME P**
 IO_L14P_T2_SRCC_32 AC17 **RF TX FRAME N**
 IO_L14N_T2_SRCC_32 AD20 **RF TX D1 P**
 IO_L15P_T2_DQS_32 AE20 **RF TX D1 N**
 IO_L15N_T2_DQS_32 AA19 **RF RX FRAME P**
 IO_L16P_T2_32 AA20 **RF RX FRAME N**
 IO_L17P_T2_32 AC19 **RF TX D5 P**
 IO_L17N_T2_32 AD19 **RF TX D5 N**
 IO_L18P_T2_32 AB19 **RF RX D5 P**
 IO_L18N_T2_32 AB20 **RF RX D5 N**
 IO_L19P_T3_32 Y17 **RF RX D0 P**
 IO_L19N_T3_VREF_32 Y18 **RF RX D0 N**
 IO_L20P_T3_32 V16 **RF RX D1 P**
 IO_L20N_T3_32 V17 **RF RX D1 N**
 IO_L21P_T3_DQS_32 W18 **RF RX D3 P**
 IO_L21N_T3_DQS_32 W19 **RF RX D3 N**
 IO_L22P_T3_32 W15
 IO_L22N_T3_32 V16
 IO_L23P_T3_32 V18
 IO_L23N_T3_32 V19
 IO_L24P_T3_32 V14
 IO_L24N_T3_32 W14
 IO_25_VRP_32 W13

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BANK 33

U7G
A
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D

IO_0_VRN_33 U9
 IO_L1P_T0_33 V11
 IO_LIN_T0_33 W11
 IO_L2P_T0_33 V8
 IO_L2N_T0_33 V7
 IO_L3P_T0_DQS_33 W10
 IO_L3N_T0_DQS_33 Y9
 IO_L4P_T0_33 Y8
 IO_L4N_T0_33 Y7
 IO_L5P_T0_33 Y11
 IO_L5N_T0_33 Y10
 IO_L6P_T0_33 W8
 IO_L6N_T0_VREF_33 AE7
 IO_L7P_T1_33 AF7
 IO_L7N_T1_33 AA8
 IO_L8P_T1_33 AA7
 IO_L8N_T1_33 AC8
 IO_L9P_T1_DQS_33 AD8
 IO_L9N_T1_DQS_33 AB7
 IO_L10P_T1_33 AC7
 IO_L10N_T1_33 AA9
 IO_L11P_T1_SRCC_33 AB9
 IO_L11N_T1_SRCC_33 AC9
 IO_L12P_T1_MRCC_33 AD9
 IO_L12N_T1_MRCC_33 AB11
 IO_L13P_T2_MRCC_33 AC11
 IO_L13N_T2_MRCC_33 AA10
 IO_L14P_T2_SRCC_33 AB10
 IO_L14N_T2_SRCC_33 AB12 **FPGA GPIO8**
 IO_L15P_T2_DQS_33 AC12 **FPGA GPIO7**
 IO_L15N_T2_DQS_33 AA13 **FPGA GPIO5**
 IO_L16P_T2_33 AA12 **FPGA GPIO9**
 IO_L16N_T2_33 AC13 **FPGA GPIO1**
 IO_L17P_T2_33 AD13 **FPGA GPIO0**
 IO_L17N_T2_33 Y13
 IO_L18P_T2_33 Y12
 IO_L18N_T2_33 AD11 **FPGA GPIO6**
 IO_L19P_T3_33 AE11 **FPGA GPIO4**
 IO_L19N_T3_VREF_33 AD10
 IO_L20P_T3_33 AE10
 IO_L20N_T3_33 AE12 **FPGA GPIO3**
 IO_L21P_T3_DQS_33 AF12 **FPGA GPIO2**
 IO_L21N_T3_DQS_33 AE8
 IO_L22P_T3_33 AF8
 IO_L22N_T3_33 AE13
 IO_L23P_T3_33 AF13
 IO_L23N_T3_33 AF10
 IO_L24P_T3_33 AF9
 IO_L24N_T3_33 V12
 IO_25_VRP_33 V12

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BANK 34

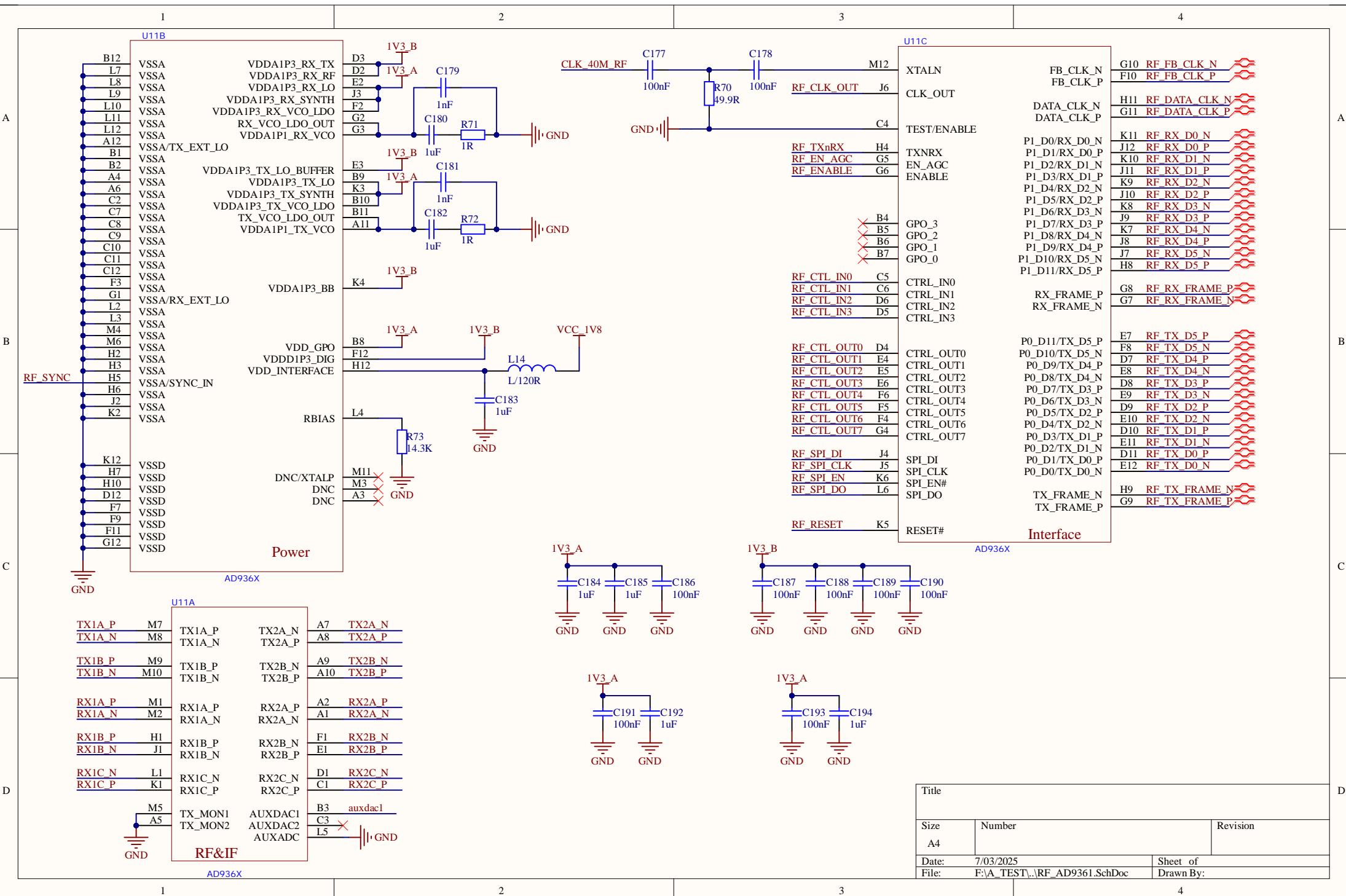
U7H
A
B
C
D

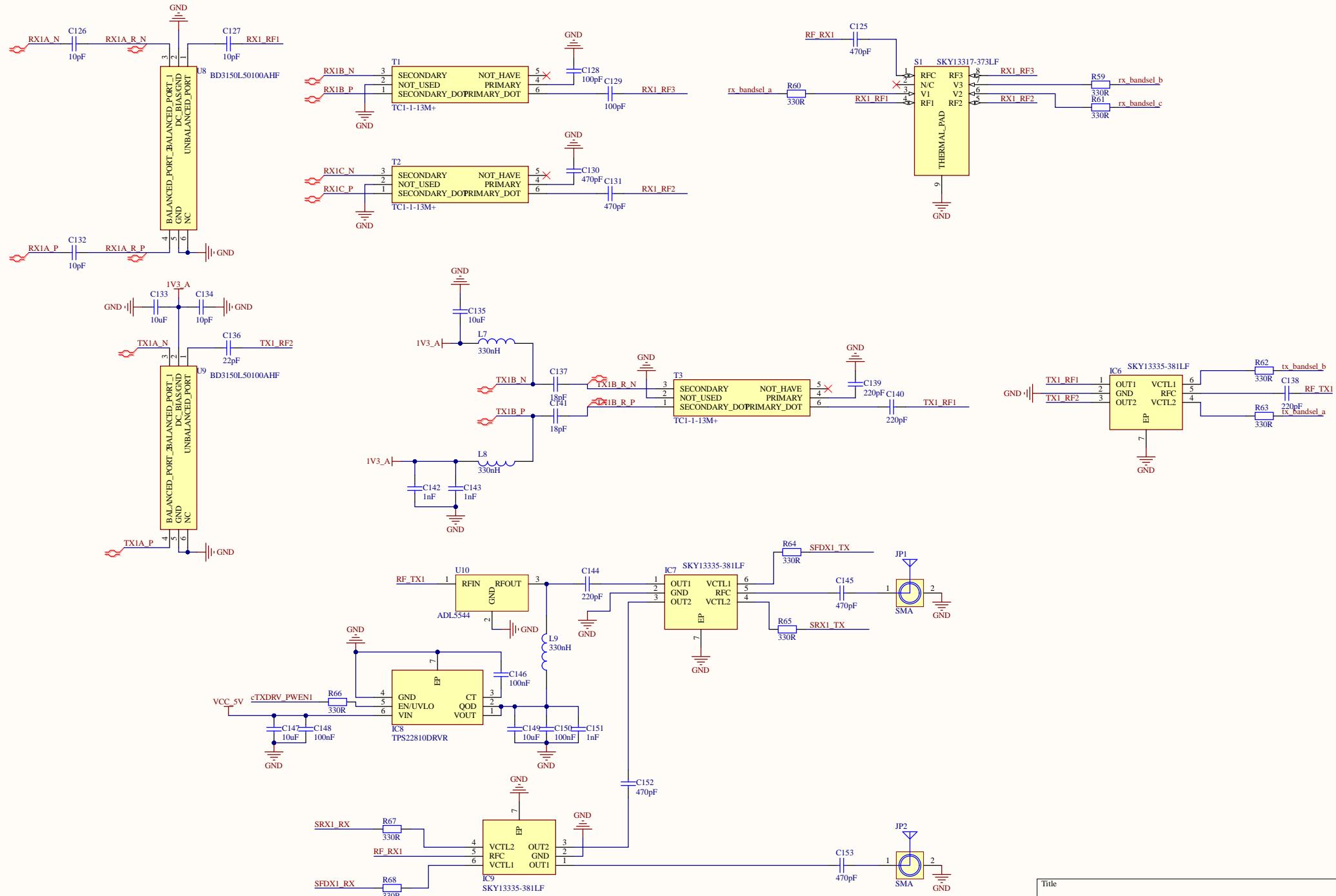
IO_0_VRN_34 U4 **FX3 GPIO54**
 IO_L1P_T0_34 U6 **FX3 DQ0**
 IO_LIN_T0_34 U5 **FX3 DQ3**
 IO_L2P_T0_34 U2 **AUX_PWR_ON**
 IO_L2N_T0_34 U1 **FX3 DQ5**
 IO_L3P_T0_DQS_34 W6 **FX3 DQ8**
 IO_L3N_T0_DQS_34 W5 **SHDN_SW**
 IO_L4P_T0_34 V3 **FX3 DQ4**
 IO_L5P_T0_34 W3 **FX3 DQ24**
 IO_L5N_T0_34 U7 **FX3 DQ29**
 IO_L6P_T0_34 V6 **FX3 DQ2**
 IO_L6N_T0_VREF_34 V4 **FX3 DQ6**
 IO_L7P_T1_34 W4 **FX3 DQ7**
 IO_L7N_T1_34 Y3 **FX3 CTL13**
 IO_L8P_T1_34 V2 **FX3 DQ30**
 IO_L8N_T1_34 V1 **FX3 DQ31**
 IO_L9P_T1_DQS_34 AB1 **FX3 CTL9**
 IO_L9N_T1_DQS_34 AC1 **FX3 DQ14**
 IO_L10P_T1_34 W1 **FX3 DQ9**
 IO_L10N_T1_34 Y1 **FX3 DQ28**
 IO_L11P_T1_SRCC_34 AB2 **FX3 DQ11**
 IO_L11N_T1_SRCC_34 AC2 **FX3 CTL3**
 IO_L12P_T1_MRCC_34 AA3 **FX3 DQ1**
 IO_L12N_T1_MRCC_34 AA2 **FX3 DQ15**
 IO_L13P_T2_MRCC_34 AA4 **FX3 CTL0**
 IO_L13N_T2_MRCC_34 AB4 **FX3 CTL12**
 IO_L14P_T2_SRCC_34 AC4 **FX3 CTL11**
 IO_L14N_T2_SRCC_34 AC3 **FX3 CTL8**
 IO_L15P_T2_DQS_34 AA5 **FX3 DQ12**
 IO_L15N_T2_DQS_34 AB5 **FX3 INT**
 IO_L16P_T2_34 AB6 **FX3 CTL4**
 IO_L16N_T2_34 AC6 **FX3 DQ19**
 IO_L17P_T2_34 Y6 **FX3 DQ10**
 IO_L17N_T2_34 Y5 **FX3 CTL7**
 IO_L18P_T2_34 AD6 **FX3 DQ18**
 IO_L18N_T2_34 AD5 **FX3 DQ17**
 IO_L19P_T3_34 AD4 **FX3 DQ26**
 IO_L19N_T3_VREF_34 AD3 **FX3 CTL6**
 IO_L20P_T3_34 AD1 **FX3 PCLK**
 IO_L20N_T3_34 AE1 **FX3 CTL2**
 IO_L21P_T3_DQS_34 AF5 **FX3 DQ27**
 IO_L21N_T3_DQS_34 AF4 **FX3 DQ25**
 IO_L22P_T3_34 AE3 **FX3 DQ22**
 IO_L22N_T3_34 AE2 **FX3 CTL1**
 IO_L23P_T3_34 AE6 **FX3 DQ16**
 IO_L23N_T3_34 AE5 **FX3 DQ20**
 IO_L24P_T3_34 AF3 **FX3 DQ21**
 IO_L24N_T3_34 AF2 **FX3 DQ23**
 IO_25_VRP_34 T7 **FX3 GPIO55**

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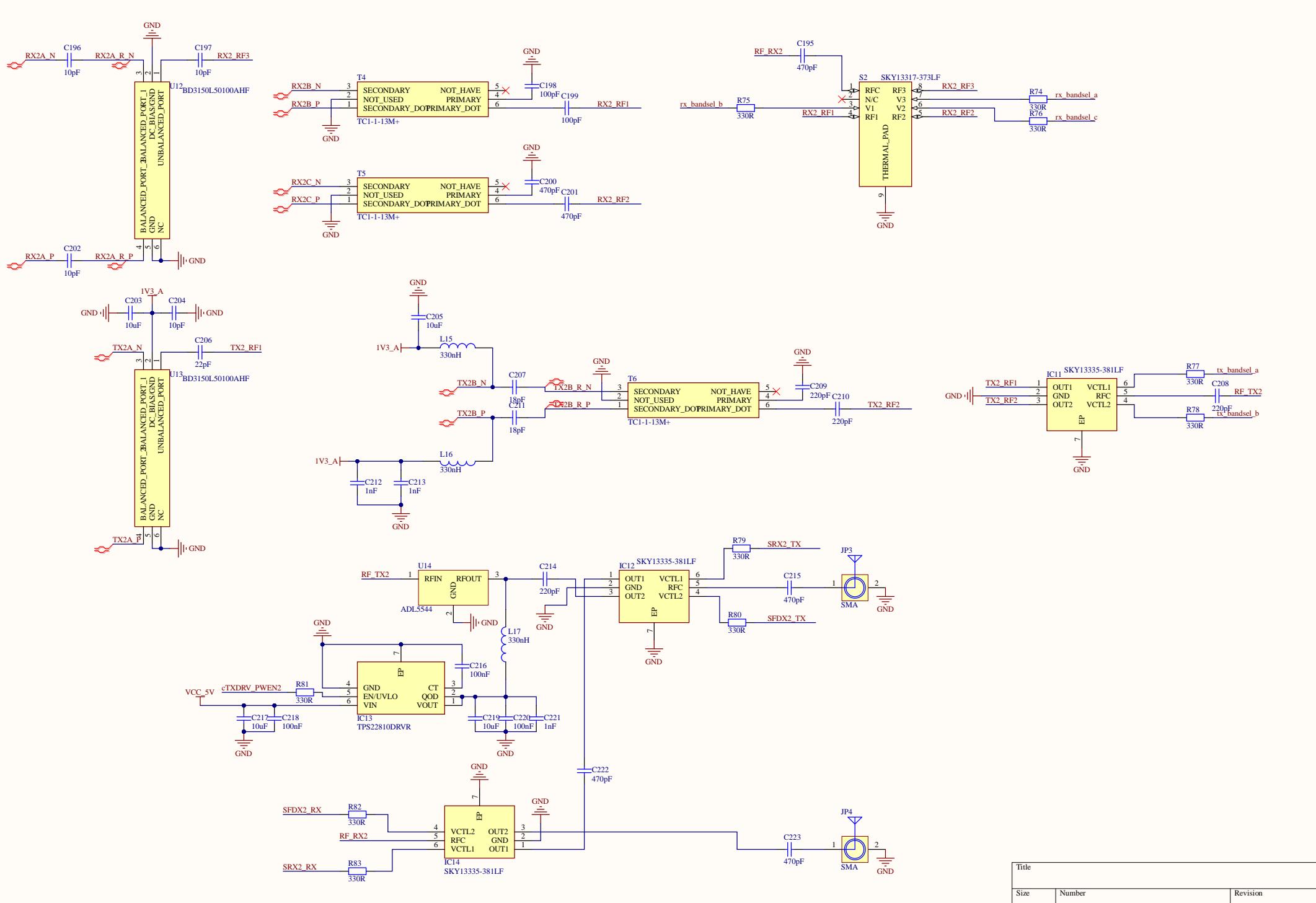
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Title		
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