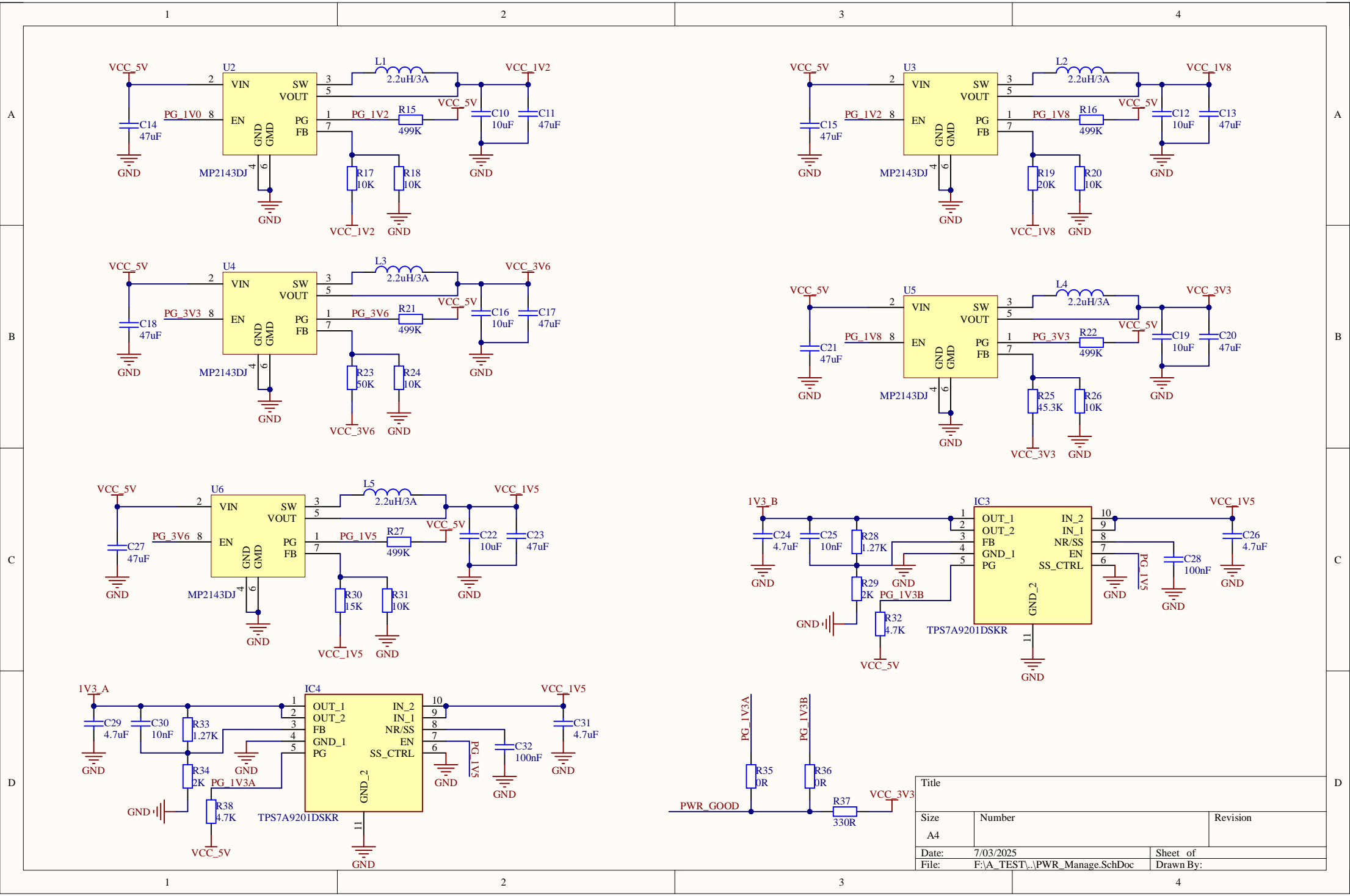
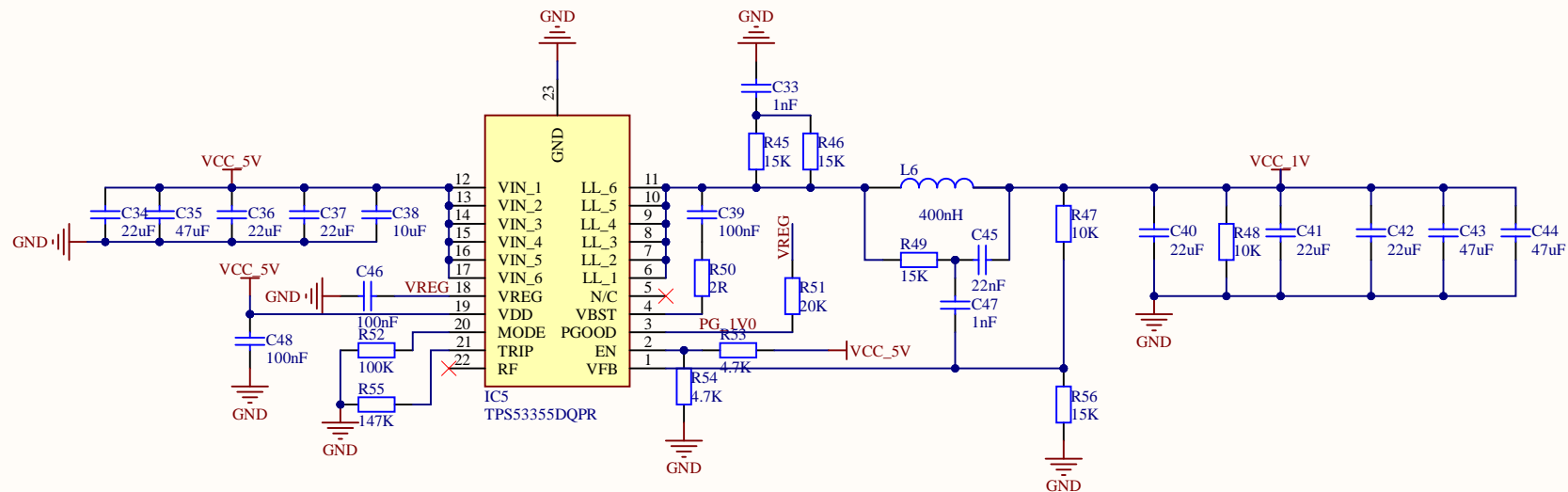
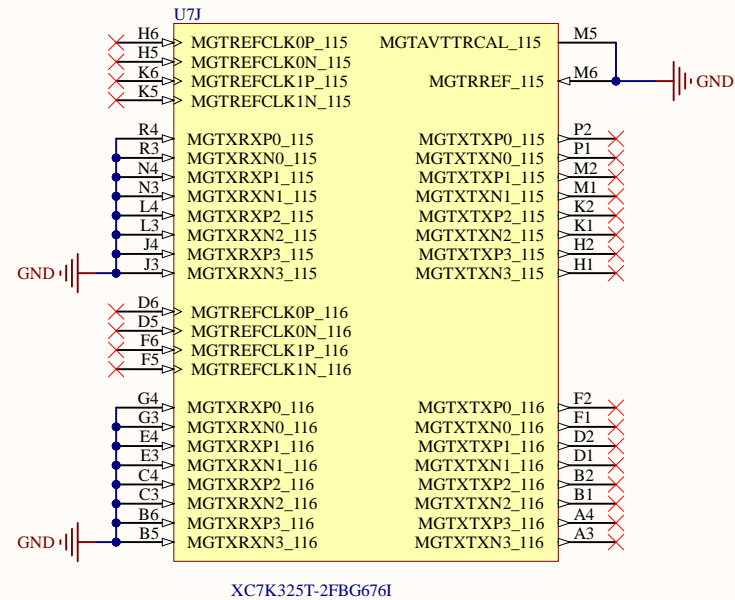
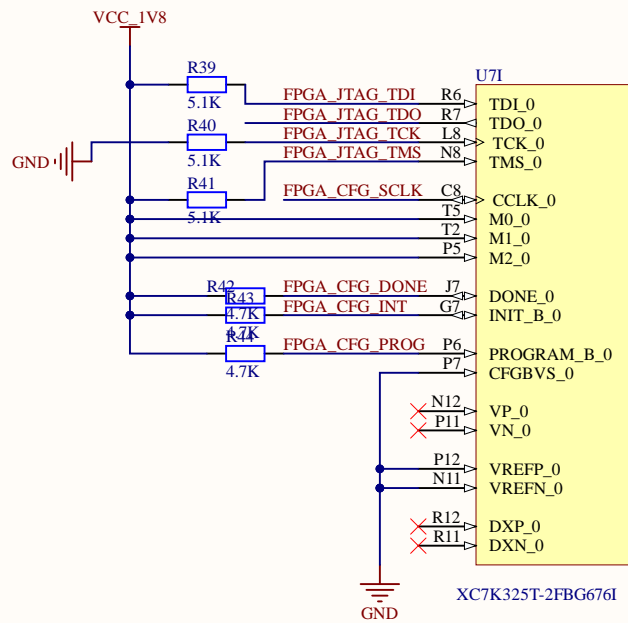


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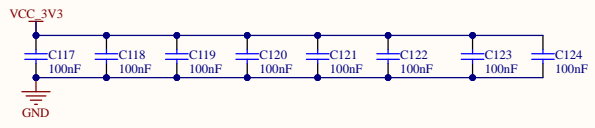
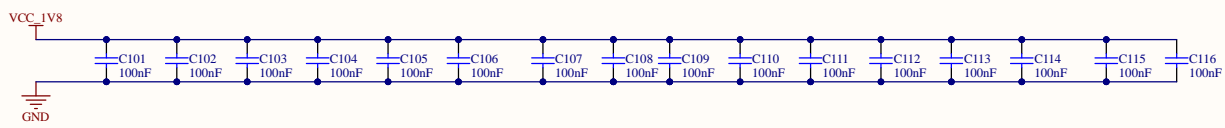
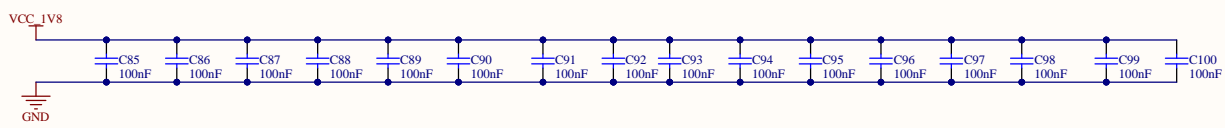
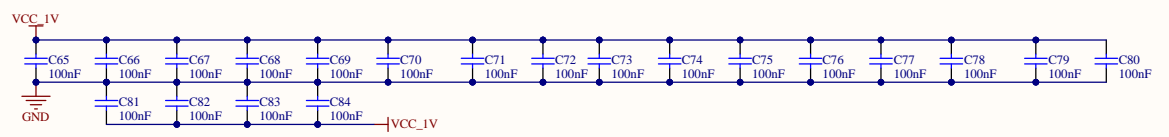
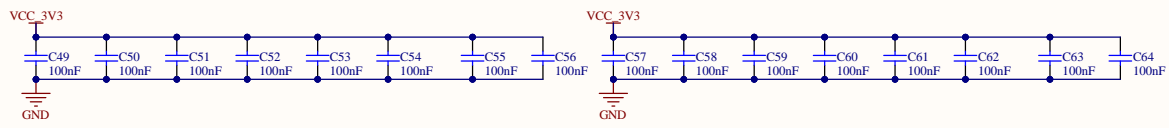
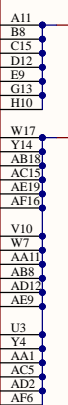
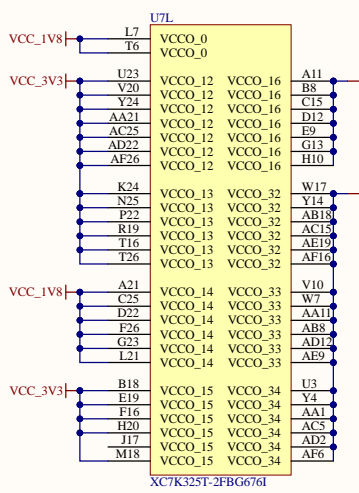
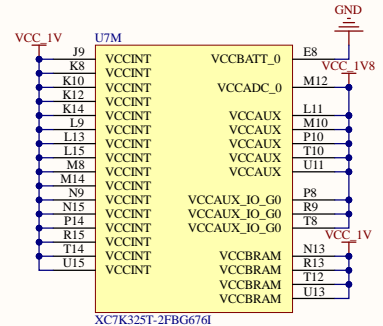
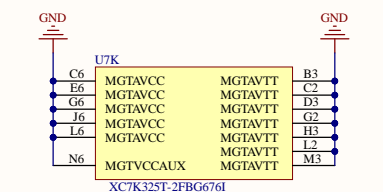
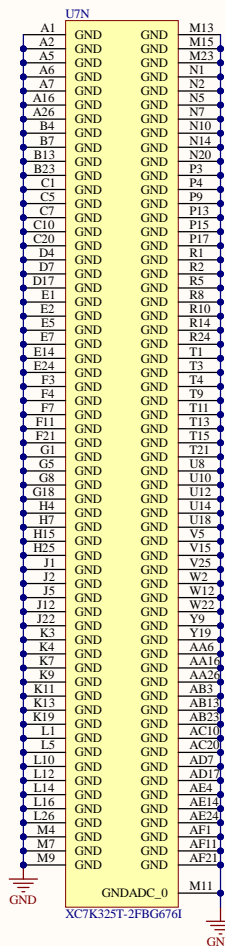
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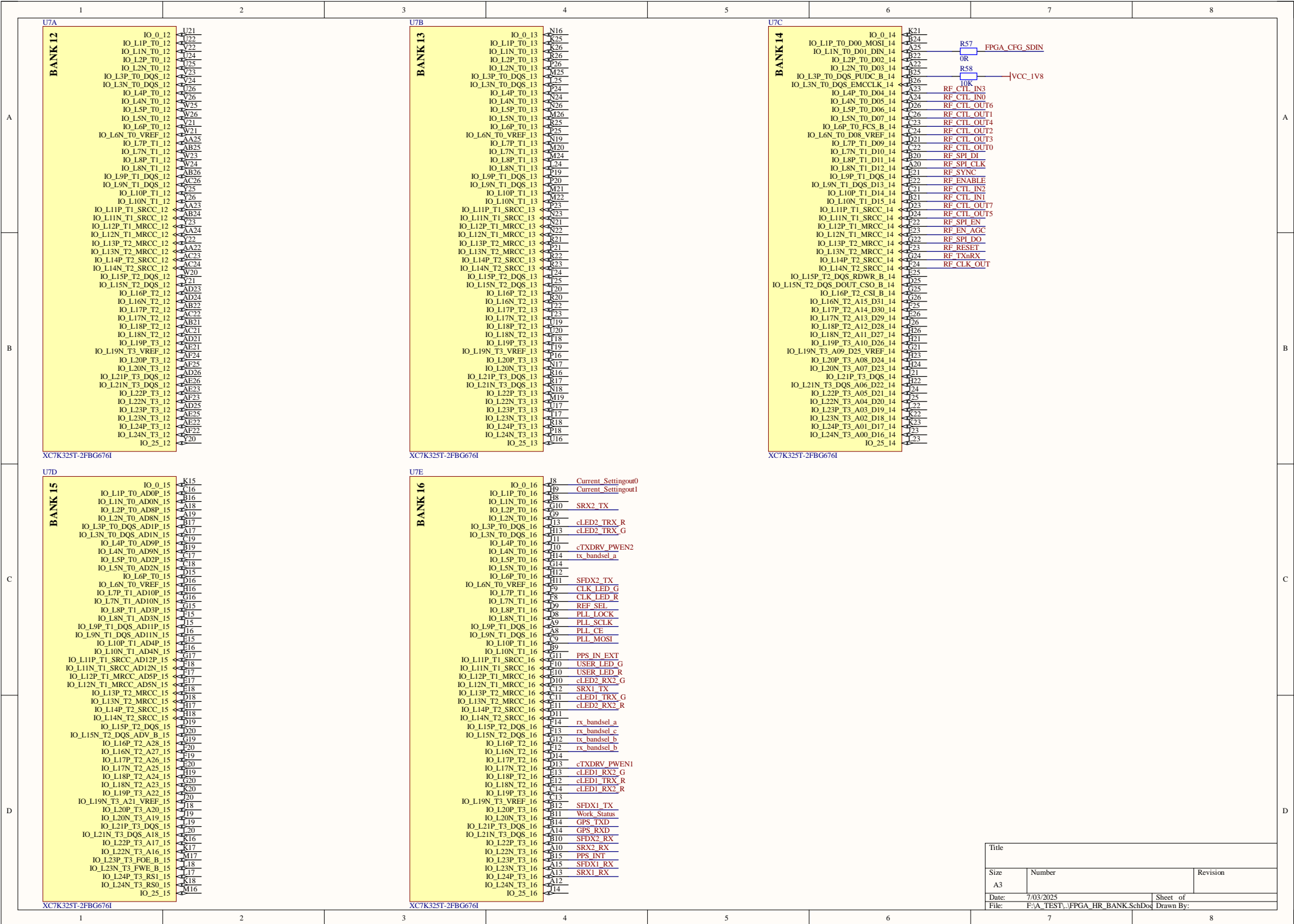
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U7F

BANK 32

IO_0_VRN_32 V13
IO_L1P_T0_32 AE17
IO_L1N_T0_32 AF17
IO_L2P_T0_32 AF14
IO_L2N_T0_32 AF15
IO_L3P_T0_DQS_32 AE18
IO_L3N_T0_DQS_32 AF18
IO_L4P_T0_32 AD15
IO_L4N_T0_32 AE15
IO_L5P_T0_32 AF19 RF TX D0 P
IO_L5N_T0_32 AF20 RF TX D0 N
IO_L6P_T0_32 AD16 RF TX D2 P
IO_L6N_T0_VREF_32 AE16 RF TX D2 N
IO_L7P_T1_32 AA14 RF RX D2 P
IO_L7N_T1_32 AA15 RF RX D2 N
IO_L8P_T1_32 AC14 RF TX D4 P
IO_L8N_T1_32 AD14 RF TX D4 N
IO_L9P_T1_DQS_32 Y15 RF RX D4 P
IO_L9N_T1_DQS_32 Y16 RF RX D4 N
IO_L10P_T1_32 AB14 RF TX D3 P
IO_L10N_T1_32 AB15 RF TX D3 N
IO_L11P_T1_SRCC_32 AA17 FPGA CLK P
IO_L11N_T1_SRCC_32 AA18 FPGA CLK N
IO_L12P_T1_MRCC_32 AB16 RF DATA CLK P
IO_L12N_T1_MRCC_32 AC16 RF DATA CLK N
IO_L13P_T2_MRCC_32 AD18 RF FB CLK N
IO_L13N_T2_MRCC_32 AB17 RF TX FRAME P
IO_L14P_T2_SRCC_32 AC17 RF TX FRAME N
IO_L14N_T2_SRCC_32 AD20 RF TX D1 P
IO_L15P_T2_DQS_32 AE20 RF TX D1 N
IO_L15N_T2_DQS_32 AA19 RF RX FRAME P
IO_L16P_T2_32 AA20 RF RX FRAME N
IO_L16N_T2_32 AC19 RF TX D5 P
IO_L17P_T2_32 AD19 RF TX D5 N
IO_L17N_T2_32 AB19 RF RX D5 P
IO_L18P_T2_32 AB20 RF RX D5 N
IO_L18N_T2_32 Y17 RF RX D0 P
IO_L19P_T3_32 Y18 RF RX D0 N
IO_L19N_T3_VREF_32 V16 RF RX D1 P
IO_L20P_T3_32 V17 RF RX D1 N
IO_L20N_T3_32 W18 RF RX D3 P
IO_L21P_T3_DQS_32 W19 RF RX D3 N
IO_L21N_T3_DQS_32 W15
IO_L22P_T3_32 W16
IO_L22N_T3_32 V18
IO_L23P_T3_32 V19
IO_L23N_T3_32 V14
IO_L24P_T3_32 W14
IO_L24N_T3_32 W13
IO_25_VRP_32

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U7G

BANK 33

IO_0_VRN_33 U9
IO_L1P_T0_33 V11
IO_L1N_T0_33 W11
IO_L2P_T0_33 V8
IO_L2N_T0_33 V7
IO_L3P_T0_DQS_33 W10
IO_L3N_T0_DQS_33 W9
IO_L4P_T0_33 Y8
IO_L4N_T0_33 Y7
IO_L5P_T0_33 Y11
IO_L5N_T0_33 Y10
IO_L6P_T0_33 V9
IO_L6N_T0_VREF_33 W8
IO_L7P_T1_33 AE7
IO_L7N_T1_33 AF7
IO_L8P_T1_33 AA8
IO_L8N_T1_33 AA7
IO_L9P_T1_DQS_33 AC8
IO_L9N_T1_DQS_33 AD8
IO_L10P_T1_32 AB7
IO_L10N_T1_33 AC7
IO_L11P_T1_SRCC_33 AA9
IO_L11N_T1_SRCC_33 AB9
IO_L12P_T1_MRCC_33 AC9
IO_L12N_T1_MRCC_33 AD9
IO_L13P_T2_MRCC_33 AB11
IO_L13N_T2_MRCC_33 AC11
IO_L14P_T2_SRCC_33 AA10
IO_L14N_T2_SRCC_33 AB10
IO_L15P_T2_DQS_33 AB12 FPGA GPIO8
IO_L15N_T2_DQS_33 AC12 FPGA GPIO7
IO_L16P_T2_33 AA13 FPGA GPIO5
IO_L16N_T2_33 AA12 FPGA GPIO9
IO_L17P_T2_33 AC13 FPGA GPIO1
IO_L17N_T2_33 AD13 FPGA GPIO0
IO_L18P_T2_33 Y13
IO_L18N_T2_33 Y12
IO_L19P_T3_33 AD11 FPGA GPIO6
IO_L19N_T3_VREF_33 AE11 FPGA GPIO4
IO_L20P_T3_33 AD10
IO_L20N_T3_33 AE10
IO_L21P_T3_DQS_33 AE12 FPGA GPIO3
IO_L21N_T3_DQS_33 AF12 FPGA GPIO2
IO_L22P_T3_33 AE8
IO_L22N_T3_32 AF8
IO_L23P_T3_32 AE13
IO_L23N_T3_33 AF13
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IO_25_VRP_33 V12

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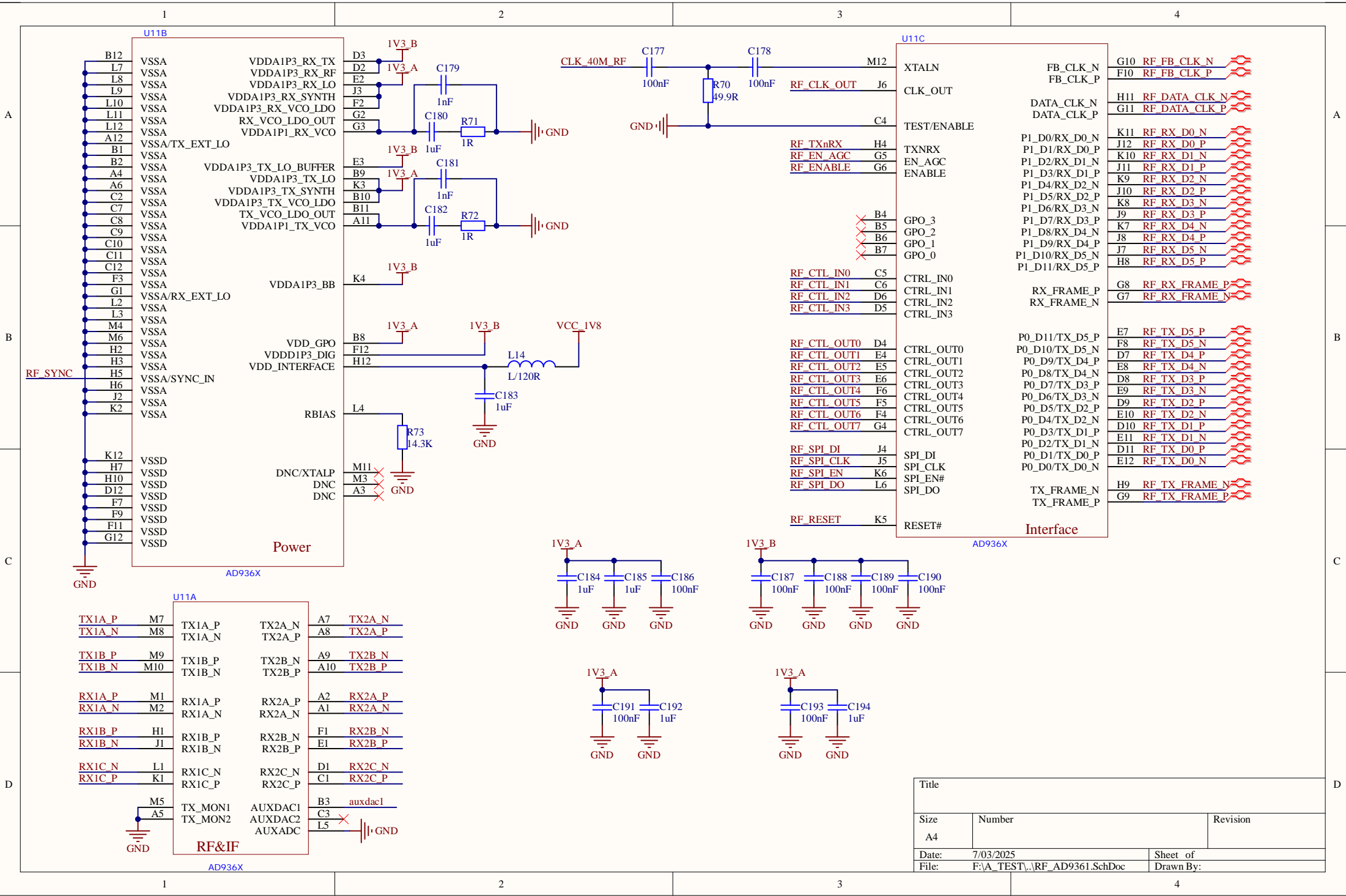
U7H

BANK 34

IO_0_VRN_34 U4 FX3 GPIO54
IO_L1P_T0_34 U6 FX3 DQ0
IO_L1N_T0_34 U5 FX3 DQ3
IO_L2P_T0_34 U2 AUX_PWR_ON
IO_L2N_T0_34 U1 FX3 DQ5
IO_L3P_T0_DQS_34 W6 FX3 DQ8
IO_L3N_T0_DQS_34 W5 SHDN SW
IO_L4P_T0_34 V3 FX3 DQ4
IO_L4N_T0_34 W3 FX3 DQ24
IO_L5P_T0_34 U7 FX3 DQ29
IO_L5N_T0_34 V6 FX3 DQ2
IO_L6P_T0_34 V4 FX3 DQ6
IO_L6N_T0_VREF_34 W4 FX3 DQ7
IO_L7P_T1_34 Y3 FX3 DQ13
IO_L7N_T1_34 Y2 FX3 CTL5
IO_L8P_T1_34 V2 FX3 DQ30
IO_L8N_T1_34 V1 FX3 DQ31
IO_L9P_T1_DQS_34 AB1 FX3 CTL9
IO_L9N_T1_DQS_34 AC1 FX3 DQ14
IO_L10P_T1_34 W1 FX3 DQ9
IO_L10N_T1_34 Y1 FX3 DQ28
IO_L11P_T1_SRCC_34 AB2 FX3 DQ11
IO_L11N_T1_SRCC_34 AC2 FX3 CTL3
IO_L12P_T1_MRCC_34 AA3 FX3 DQ1
IO_L12N_T1_MRCC_34 AA2 FX3 DQ15
IO_L13P_T2_MRCC_34 AA4 FX3 CTL0
IO_L13N_T2_MRCC_34 AB4 FX3 CTL12
IO_L14P_T2_SRCC_34 AC4 FX3 CTL11
IO_L14N_T2_SRCC_34 AC3 FX3 CTL8
IO_L15P_T2_DQS_34 AA5 FX3 DQ12
IO_L15N_T2_DQS_34 AB5 FX3 INT
IO_L16P_T2_34 AB6 FX3 CTL4
IO_L16N_T2_34 AC6 FX3 DQ19
IO_L17P_T2_34 Y6 FX3 DQ10
IO_L17N_T2_34 Y5 FX3 CTL7
IO_L18P_T2_34 AD6 FX3 DQ18
IO_L18N_T2_34 AD5 FX3 DQ17
IO_L19P_T3_34 AD4 FX3 DQ26
IO_L19N_T3_VREF_34 AD3 FX3 CTL6
IO_L20P_T3_34 AD1 FX3 PCLK
IO_L20N_T3_34 AE1 FX3 CTL2
IO_L21P_T3_DQS_34 AF5 FX3 DQ27
IO_L21N_T3_DQS_34 AF4 FX3 DQ25
IO_L22P_T3_34 AE2 FX3 DQ22
IO_L22N_T3_34 AE6 FX3 DQ16
IO_L23P_T3_34 AE5 FX3 DQ20
IO_L23N_T3_34 AF3 FX3 DQ21
IO_L24P_T3_34 AF2 FX3 DQ23
IO_L24N_T3_34 T7 FX3 GPIO55
IO_25_VRP_34

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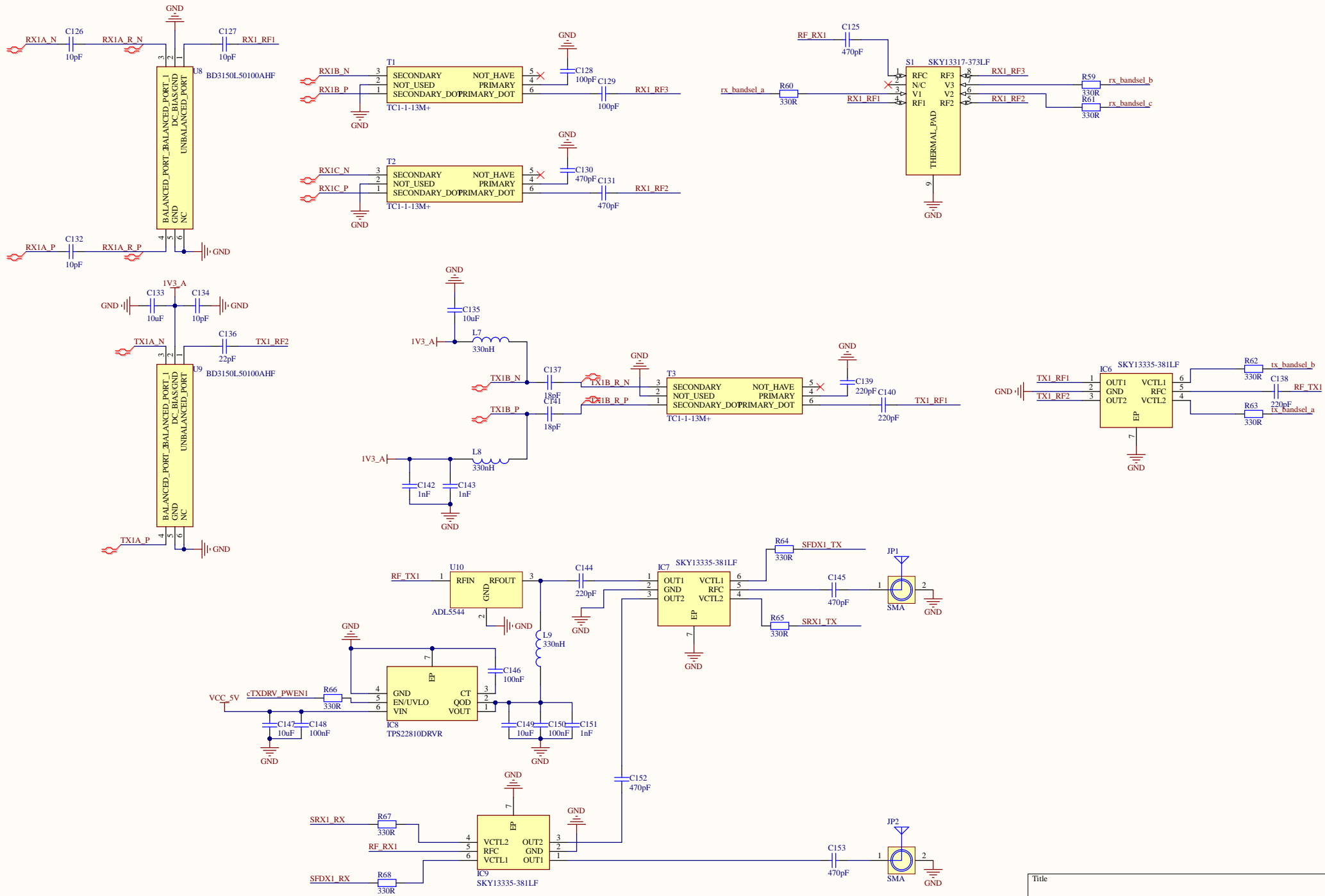
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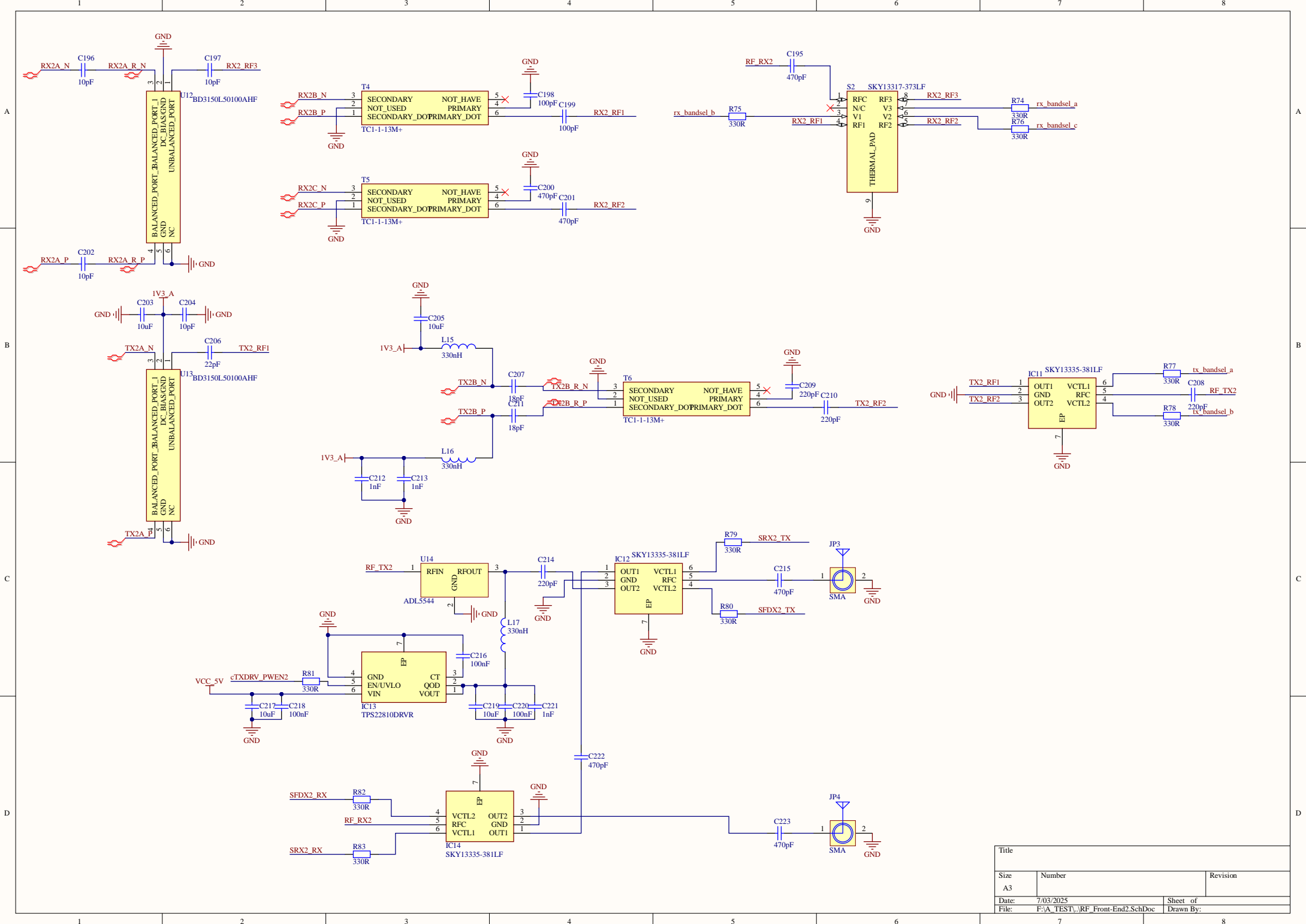
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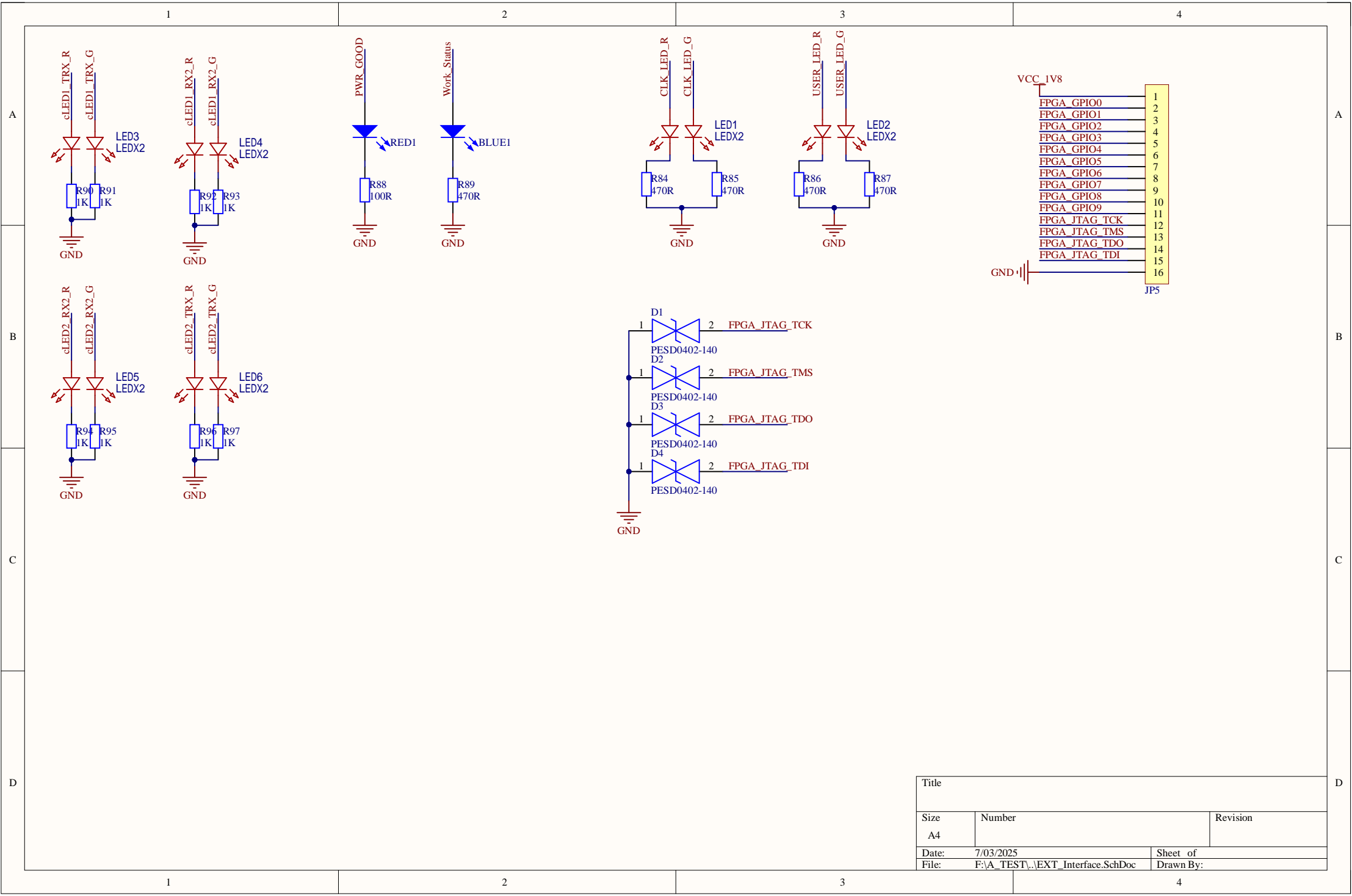
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