

Practical Implementation of Delayed Signal Cancellation Method for Phase-Sequence Separation

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Abstract—In this paper, the use of a method for online detection of positive- and negative-sequence components of three-phase quantities, named the delayed signal cancellation (DSC) method, is investigated. Problems that arise in practical implementation of the DSC method in computer-controlled systems are investigated. Expressions of the detection error due to nonideal discretization are derived and calculations are verified experimentally. Two methods for reducing the detection error are presented and verified. It is also shown that the given expressions and proposed methods for reducing the detection errors can also be applied to the case of grid frequency variations.

Index Terms—Control, filter, measurement, sequence detection, unbalance, voltage-source converter.

I. INTRODUCTION

EFFECTIVE and robust methods for the detection of positive- and negative-sequence components of voltages and currents are necessary in a vast range of applications using voltage-source converters (VSCs). All kinds of power-conditioning equipment, for example, need accurate information on the positive-sequence voltage in order to achieve unity power factor and load voltage regulation, as well as on the negative-sequence voltage in order to compensate for unbalance conditions. This issue is even more critical when a fast transient response is required, such as in voltage dip compensation by means of the static series compensator (SSC) or dynamic voltage restorer (DVR). Most dips are caused by short-circuit faults, many of which last only a few tens or hundreds of milliseconds and are unbalanced (i.e., involve a negative-sequence component [1]). Hence, the detection system must provide accurate information on the voltage sequence components in a very short time (in the order of milliseconds) for obtaining satisfactory performance. Finally, in many applications in which the VSC provides the interface to the grid, such as wind power plants and many adjustable speed drives, the VSC is often subjected to disturbances that can severely affect its performance and lead to the shutdown of the drive or wind turbine [2]. In many cases,

the undesired shutdown can be prevented by designing a more robust current controller, which separately controls positive- and negative-sequence components of the grid voltage [3].

Controllers for the VSC are usually designed in a synchronous reference frame (SRF) (i.e., in a rotating coordinate system synchronized with the grid frequency). A positive-sequence vector appears as a dc component in this frame, while a negative-sequence vector appears as a double-frequency component (i.e., its components oscillate at twice the grid frequency). To isolate the dc component from the double-frequency ripple, filtering techniques may be used. An example is a notch filter with a very deep notch to cut off the oscillatory component of the signal. However, as described in [4], with a high bandwidth for the filter, large transients can be experienced in the estimated sequence components. On the other hand, a low bandwidth for the filter slows down the response.

The method proposed in [5], referred to as delayed signal cancellation (DSC), is considered here. This technique, based on a combination of positive- and negative-sequence component vectors, allows achieving accurate information on sequence components with a time delay of one-quarter of a period (5 ms at 50 Hz). The method has been successfully applied for flicker mitigation by using a shunt-connected VSC [6], series compensation by using a SSC [7], and for shunt-connected VSC used as a front-end rectifier [8].

In this paper, issues concerning the practical application of the DSC method are treated. Implementation in the discrete time domain is studied: the effect of variations in the sampling frequency is analyzed and the consequent error is calculated. Countermeasures to reduce the error in the estimated sequence components are investigated. Moreover, the influence of variations of the grid frequency is analyzed. Simulation and experimental verification of the analytical results are shown and discussed.

II. PRINCIPLE OF THE DSC METHOD

In the general case of an unsymmetrical grid, the grid voltage vector $\underline{e}^{(\alpha\beta)}(t)$ in a fixed two-coordinate system (called $\alpha\beta$ plane) can be written as

$$\underline{e}^{(\alpha\beta)}(t) = \underline{e}_p^{(\alpha\beta)}(t) + \underline{e}_n^{(\alpha\beta)}(t) = E_p e^{j(\omega_g t + \varphi_p)} + E_n e^{-j(\omega_g t + \varphi_n)} \quad (1)$$

where E_p and E_n are the amplitudes of positive and negative phase-sequence voltage vectors, respectively, and φ_p and φ_n are their phase displacements. The DSC method applied in the $\alpha\beta$ plane is defined by the expressions

$$\underline{\hat{e}}_p^{(\alpha\beta)}(t) = \frac{1}{2} \left[\underline{e}^{(\alpha\beta)}(t) + j \underline{e}^{(\alpha\beta)}(t - T_g/4) \right] \quad (2)$$

$$\underline{\hat{e}}_n^{(\alpha\beta)}(t) = \frac{1}{2} \left[\underline{e}^{(\alpha\beta)}(t) - j \underline{e}^{(\alpha\beta)}(t - T_g/4) \right] \quad (3)$$

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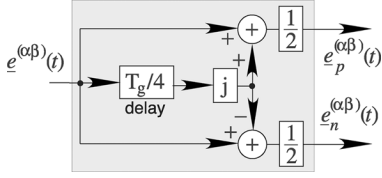


Fig. 1. Block scheme of DSC in continuous time domain.

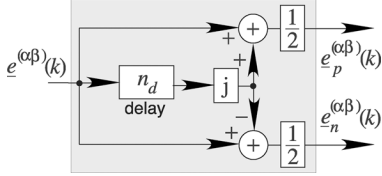


Fig. 2. Block scheme of the DSC in discrete time domain.

where $\hat{e}_p^{(\alpha\beta)}(t)$ and $\hat{e}_n^{(\alpha\beta)}(t)$ are the estimated positive and negative phase-sequence voltages, respectively, and T_g is the period of the grid voltage. The block scheme of the DSC is shown in Fig. 1. It is straightforward to verify that, by substituting (1) into (2) and (3), the estimated phase-sequence voltage vectors result in

$$\hat{e}_p^{(\alpha\beta)}(t) = E_p e^{j(\omega_g t + \varphi_p)} \quad (4)$$

$$\hat{e}_n^{(\alpha\beta)}(t) = E_n e^{-j(\omega_g t + \varphi_n)} \quad (5)$$

(i.e., equal to the corresponding sequence voltage vectors). The method implies delaying the signal by one-quarter of period at the fundamental frequency, which then constitutes its inherent time delay.

III. DIGITAL IMPLEMENTATION OF THE DSC METHOD

A. Influence of Sampling Frequency

Equations (2) and (3), defining the DSC method, are expressed in the continuous time domain. However, to be implemented in a computer-controlled system as is necessary in most practical applications, the method has to be expressed in the discrete time domain. The block scheme of the discrete DSC is shown in Fig. 2. Assuming a sampling frequency equal to f_s , (2) and (3) can be rewritten in the discrete time domain as

$$\hat{e}_p^{(\alpha\beta)}(kT_s) = \frac{1}{2} \left[\underline{e}^{(\alpha\beta)}(kT_s) + j \underline{e}^{(\alpha\beta)}(kT_s - n_d T_s) \right] \quad (6)$$

$$\hat{e}_n^{(\alpha\beta)}(kT_s) = \frac{1}{2} \left[\underline{e}^{(\alpha\beta)}(kT_s) - j \underline{e}^{(\alpha\beta)}(kT_s - n_d T_s) \right] \quad (7)$$

where $T_s = 1/f_s$ is the sampling time and the delay n_d , expressed as a number of samples, is equal to

$$n_d = f_s / 4 f_g. \quad (8)$$

Ideally, the sampling frequency should be chosen so that n_d resulting from (8) is an integer. Unfortunately, the value of the switching frequency (and, consequently, of the sampling frequency) is normally selected based on other considerations. This may result in a nonideal sample delay, which occurs if the result from (8) is not an integer. In this case, if the sampling frequency

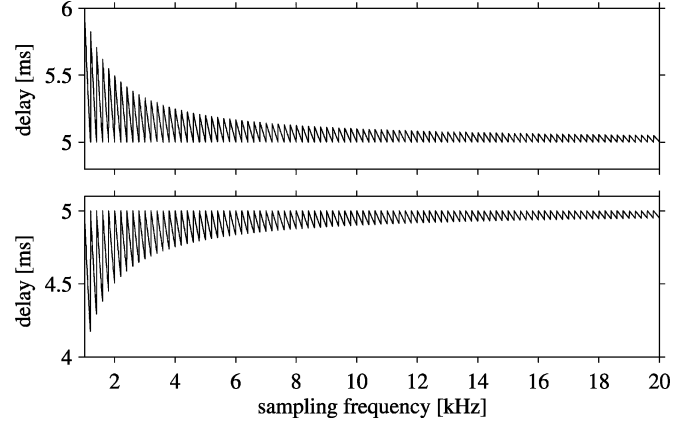


Fig. 3. Time delay as a function of sampling frequency for fixed grid frequency ($f_g = 50$ Hz). Top: Delay rounded up to the nearest higher integer n_{d2} . Bottom: Delay rounded down to the nearest lower integer n_{d1} .

is set and the grid frequency is constant, the result of (8) must be rounded off to the nearest lower or higher integer, denoted by n_{d1} and n_{d2} , respectively. This results in a time delay equal to or smaller than the correct value, or in a time delay equal or larger than the correct value, respectively. This is shown in Fig. 3 for different values of the sampling frequency.

B. Error Calculation

The expression of the detection error introduced by the non-ideal delay time due to discretization will now be calculated. The discrete time delay can be written as

$$n_d T_s = T_c + \Delta T \quad (9)$$

(i.e., as the sum of the correct time delay T_c), equal to $T_g/4$, and a time delay error ΔT . Substituting (9) into (6), the estimated positive-sequence component $\hat{e}_p^{(\alpha\beta)}(kT_s)$ can be expressed as

$$\begin{aligned} \hat{e}_p^{(\alpha\beta)}(kT_s) = & \frac{1}{2} \left[E_p e^{j(\omega_g k T_s + \varphi_p)} + E_n e^{-j(\omega_g k T_s + \varphi_n)} \right] \\ & + j \frac{1}{2} \left[E_p e^{j(\omega_g (k T_s - T_c) + \varphi_p)} e^{-j(\omega_g \Delta T)} \right. \\ & \left. + E_n e^{-j(\omega_g (k T_s - T_c) + \varphi_n)} e^{j(\omega_g \Delta T)} \right] \end{aligned} \quad (10)$$

which can be further simplified to obtain

$$\begin{aligned} \hat{e}_p^{(\alpha\beta)}(kT_s) = & \underline{e}_p^{(\alpha\beta)}(kT_s) + E_p \lambda e^{j(\omega_g k T_s - \frac{\pi}{2} + \varphi_p + \gamma)} \\ & + E_n \lambda e^{-j(\omega_g k T_s - \frac{\pi}{2} + \varphi_n + \gamma)} \end{aligned} \quad (11)$$

where

$$\lambda = \sqrt{\frac{1}{2} \left(1 - \cos \left(\frac{\pi}{2} \kappa \right) \right)} \quad (12)$$

and

$$\gamma = \arctan \left[\frac{\cos \left(\frac{\pi}{2} \kappa \right) - 1}{\sin \left(\frac{\pi}{2} \kappa \right)} \right]. \quad (13)$$

In (12) and (13), κ is the relative time error, defined by

$$\kappa = \Delta T / T_c. \quad (14)$$

From (11), the estimation error for the positive-sequence component is

$$\Delta e_p^{(\alpha\beta)}(kT_s) = E_p \lambda e^{j(\omega_g kT_s - \frac{\pi}{2} + \varphi_p + \gamma)} + E_n \lambda e^{-j(\omega_g kT_s - \frac{\pi}{2} + \varphi_n + \gamma)}. \quad (15)$$

Similarly, the estimation error for the negative-sequence detection algorithm can be expressed as

$$\Delta e_n^{(\alpha\beta)}(kT_s) = E_p \lambda e^{j(\omega_g kT_s - \frac{\pi}{2} + \varphi_p - \gamma)} + E_n \lambda e^{-j(\omega_g kT_s - \frac{\pi}{2} + \varphi_n - \gamma)}. \quad (16)$$

By applying the transformation

$$e_p^{(dqp)}(kT_s) = e_p^{(\alpha\beta)}(kT_s) e^{-j(\omega_g kT_s)} \quad (17)$$

the estimated positive-sequence component in (10) can be transformed into a positively-rotating SRF (dqp -frame) where the voltage vector is aligned with the direction of the d -axis during steady state. The estimation error for the positive-sequence voltage in the dqp -coordinate system results as

$$\begin{aligned} \Delta e_p^{(dqp)}(kT_s) &= E_p \lambda e^{j(\varphi_p + \gamma - \frac{\pi}{2})} + E_n \lambda e^{-j(\omega_g kT_s - \frac{\pi}{2} + \varphi_n + \gamma)} \\ &= \Delta e_{p,off}^{(dqp)} + \Delta e_{p,osc}^{(dqp)} \cos(2\omega_g kT_s - \delta_p) \end{aligned} \quad (18)$$

which contains a constant term (offset, denoted as $\Delta e_{p,off}^{(dqp)}$) and a fluctuating part ($\Delta e_{p,osc}^{(dqp)}$), oscillating at twice the grid frequency. The same holds for the negative-sequence component, which is transformed into a negatively rotating SRF by using the transformation

$$e_n^{(dq n)}(kT_s) = e_n^{(\alpha\beta)}(kT_s) e^{j(\omega_g kT_s)} \quad (19)$$

yielding

$$\begin{aligned} \Delta e_n^{(dq n)}(kT_s) &= E_p \lambda e^{j(2\omega_g kT_s - \frac{\pi}{2} + \varphi_p - \gamma)} + E_n \lambda e^{-j(\varphi_n - \gamma - \frac{\pi}{2})} \\ &= \Delta e_{n,osc}^{(dq n)} \cos(2\omega_g kT_s - \delta_n) + \Delta e_{n,off}^{(dq n)}. \end{aligned} \quad (20)$$

Equations (18) and (20) can further be separated into their components, yielding, for example, for the positive-sequence d - and q -components

$$\Delta e_{pd}^{(dqp)}(kT_s) = \Delta e_{pd,off}^{(dqp)} + \Delta e_{pd,osc}^{(dqp)} \cos(2\omega_g kT_s - \delta_{pd}) \quad (21)$$

$$\Delta e_{pq}^{(dqp)}(kT_s) = \Delta e_{pq,off}^{(dqp)} + \Delta e_{pq,osc}^{(dqp)} \cos(2\omega_g kT_s - \delta_{pq}) \quad (22)$$

and, similarly, for the negative sequence

$$\Delta e_{nd}^{(dq n)}(kT_s) = \Delta e_{nd,off}^{(dq n)} + \Delta e_{nd,osc}^{(dq n)} \cos(2\omega_g kT_s - \delta_{nd}) \quad (23)$$

$$\Delta e_{nq}^{(dq n)}(kT_s) = \Delta e_{nq,off}^{(dq n)} + \Delta e_{nq,osc}^{(dq n)} \cos(2\omega_g kT_s - \delta_{nq}). \quad (24)$$

C. Detection Error Due to Rounding

The time delay due to discretization can be written as

$$n_{d1}T_s = T_c + \Delta T_1 \quad (25)$$

$$n_{d2}T_s = T_c + \Delta T_2 \quad (26)$$

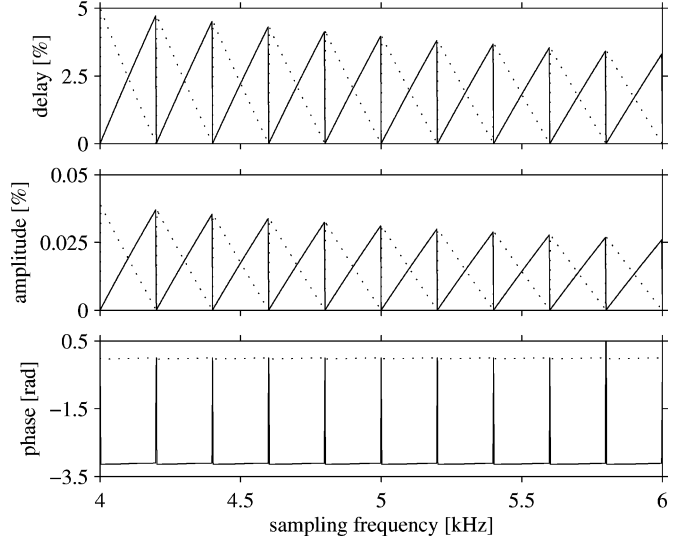


Fig. 4. Top: Absolute value of relative time delay error obtained by rounding down (κ_1 , solid) or up (κ_2 , dotted) to the nearest integer. Middle: Amplitude of relative estimation error for System 1 (λ_1 , solid) and System 2 (λ_2 , dotted). Bottom: Phase of the relative estimation error for System 1 (γ_1 , solid) and System 2 (γ_2 , dotted). Grid frequency $f_g = 50$ Hz.

when rounding down or up to the nearest integer, respectively. Thus, $\Delta T_1 < 0$ and $\Delta T_2 > 0$. The positive- and negative-sequence estimation errors for “System 1” using ΔT_1 are indicated as $\Delta e_{p1}^{(\alpha\beta)}(kT_s)$ and $\Delta e_{n1}^{(\alpha\beta)}(kT_s)$, respectively. These are obtained by replacing λ and γ in (15) and (16) with λ_1 and γ_1 , obtained from (12) and (13) with $\kappa = \kappa_1 = \Delta T_1/T_c$. The errors in the dqp - and dqn -frame $\Delta e_{p1}^{(dqp)}(kT_s)$ and $\Delta e_{n1}^{(dq n)}(kT_s)$ are obtained similarly from (18) and (20). The same formulation holds as in (21)–(24) for the components, with an additional subscript “1” for System 1.

For “System 2,” which uses ΔT_2 , the positive- and negative-sequence estimation errors $\Delta e_{p2}^{(\alpha\beta)}(kT_s)$ and $\Delta e_{n2}^{(\alpha\beta)}(kT_s)$ have analogous expressions, with λ_1 and γ_1 replaced by λ_2 and γ_2 , obtained from (12) and (13) with $\kappa = \kappa_2 = \Delta T_2/T_c$. The same holds for the errors in the positive and negative SFRs and their components.

Fig. 4 shows the amplitude and phase of the positive-sequence estimation error when rounding up or down to the nearest integer. The amplitudes λ_1 and λ_2 of the error are equal when f_s is an even multiple of f_g , in particular, being zero when f_s is a multiple of $4f_g$, as expected. Otherwise, the amplitude of the error is lowest for the lowest relative time error κ .

IV. EXPERIMENTAL VERIFICATION

In order to demonstrate the validity of the previous derivation, experimental results are presented in this section. Measurements have been carried out both with balanced and unbalanced voltage.

A. Laboratory Setup

A scheme of the experimental setup is displayed in Fig. 5. The setup consists of an analog 400-V grid model and a control computer, dSPACE 1103 [9]. The analog model consists of six identical Π -sections connected in series, each constituted by a series inductor of 2.05 mH and two shunt capacitors of 46 μ F.

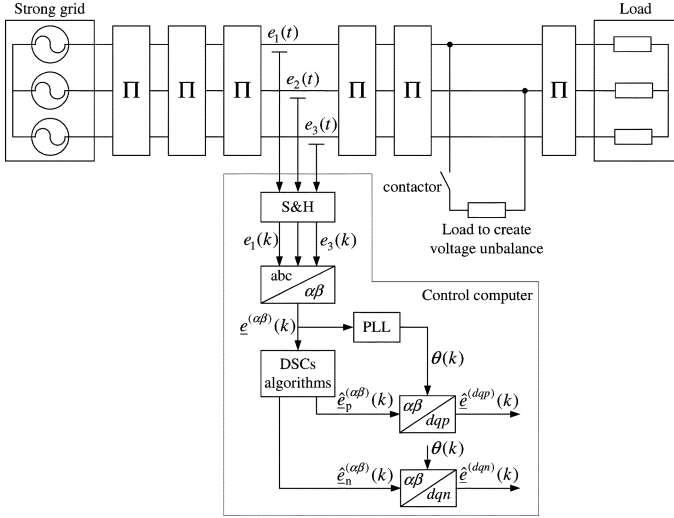


Fig. 5. Scheme of laboratory setup and controller algorithm.

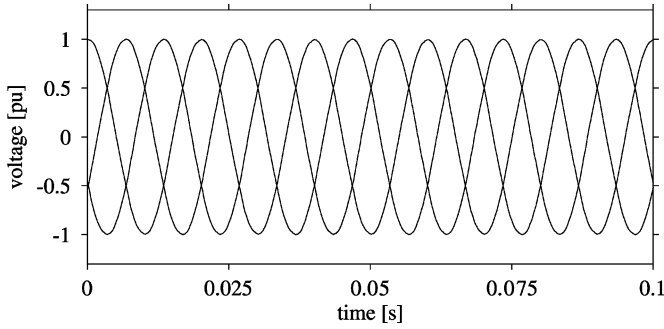


Fig. 6. Measured balanced three-phase grid voltages.

The model is loaded with a three-phase resistor of $8.8 \, \Omega$ (18 kW). In order to generate unbalanced voltage at the measurement point, an additional resistor of $4.2 \, \Omega$ was connected between phases 1 and 2 by closing a contactor (shown in Fig. 5).

The measured phase voltages are sampled and transformed into the fixed $\alpha\beta$ -plane obtaining $\underline{e}^{(\alpha\beta)}(k)$, used as input to the DSC algorithm. Estimated positive- and negative-sequence components of the grid voltage are then transformed into the corresponding rotating SRFs. The phase-locked loop (PLL) [10] is synchronized with the grid voltage vector $\underline{e}^{(\alpha\beta)}(k)$ and the transformation angle, denoted as $\theta(k)$, is equal to the grid voltage angle in steady state. The bandwidth of the phase-locked loop (PLL) is set to 30 rad/s, so that it tracks the fundamental component of the grid voltage without sensing the harmonics [11].

B. Balanced Grid Voltage

A first set of tests was carried out during steady state, with balanced voltage at nominal value. The measured three-phase voltages are shown in Fig. 6. The measured grid frequency is approximately 50 Hz, while the sampling frequency is set to 5060 Hz. In these conditions, $n_d = 25.3$. The relevant parameters for System 1 and 2 are given in Table I. The error components in (21)–(24) are calculated for System 1 and 2 and reported in Tables II and III, respectively. The measured error of the estimated positive- and negative-sequence components of the grid

TABLE I
DSC PARAMETERS FOR SYSTEM 1 AND 2 FOR STUDY CASE

$E_p = 1.0 \text{ pu}$	$E_n = 0 \text{ pu}$	$\varphi_p = 0^\circ$	$\varphi_n = 0^\circ$
System 1			
$n_{d1} = 25$	$\kappa_1 = -1.19\%$	$\lambda_1 = 0.93\%$	$\gamma_1 = 180.5^\circ$
System 2			
$n_{d2} = 26$	$\kappa_2 = 2.77\%$	$\lambda_2 = 2.17\%$	$\gamma_2 = 358.8^\circ$

TABLE II
COMPONENTS OF DETECTION ERROR FOR STUDY CASE
WITH BALANCED VOLTAGE, SYSTEM 1

Positive sequence				
$\Delta e_{pd1,off}^{(dqp)}$	-0.01%	$\Delta e_{pd1,osc}^{(dqp)}$	0%	δ_{pd1} -
$\Delta e_{pq1,off}^{(dqp)}$	0.93%	$\Delta e_{pq1,osc}^{(dqp)}$	0%	δ_{pq1} -
Negative sequence				
$\Delta e_{nd1,off}^{(dq n)}$	0%	$\Delta e_{nd1,osc}^{(dq n)}$	0.93%	δ_{nd1} 270.5°
$\Delta e_{nq1,off}^{(dq n)}$	0%	$\Delta e_{nq1,osc}^{(dq n)}$	0.93%	δ_{nq1} 0.5°

TABLE III
COMPONENTS OF DETECTION ERROR FOR STUDY CASE
WITH BALANCED VOLTAGE, SYSTEM 2

Positive sequence				
$\Delta e_{pd2,off}^{(dqp)}$	-0.05%	$\Delta e_{pd2,osc}^{(dqp)}$	0%	δ_{pd2} -
$\Delta e_{pq2,off}^{(dqp)}$	-2.17%	$\Delta e_{pq2,osc}^{(dqp)}$	0%	δ_{pq2} -
Negative sequence				
$\Delta e_{nd2,off}^{(dq n)}$	0%	$\Delta e_{nd2,osc}^{(dq n)}$	2.17%	δ_{nd2} 88.8°
$\Delta e_{nq2,off}^{(dq n)}$	0%	$\Delta e_{nq2,osc}^{(dq n)}$	2.17%	δ_{nq2} 178.8°

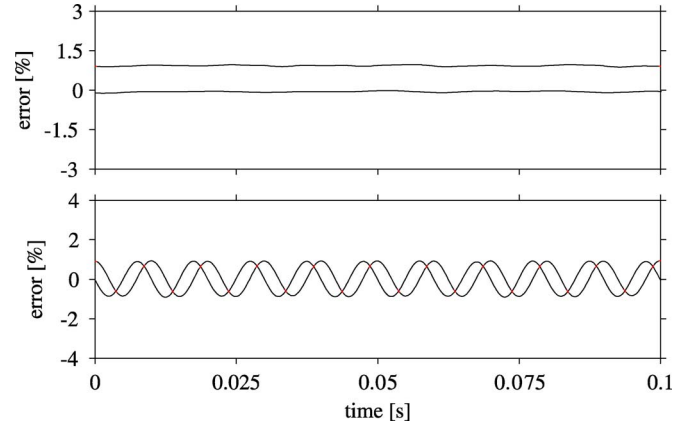


Fig. 7. Measured error in phase-sequence estimation with balanced steady-state voltage using System 1 $f_g = 50 \text{ Hz}$, $f_s = 5060 \text{ Hz}$. Top: Positive-sequence components in positive SRF (black: d -component; red: q -component). Bottom: Negative-sequence components in negative SRF (black: d -component; red: q -component).

voltage, plotted in Figs. 7 and 8 for System 1 and 2, respectively, confirm the calculations above. As shown, the error is larger for System 2 due to higher λ .

C. Unbalanced Grid Voltage

Further tests have been carried out with the same sampling frequency under unbalanced grid conditions. The unbalanced current circulating in the Π sections of the network model creates the three-phase voltages in Fig. 9. The measured sequence components of the grid voltage are $E_p = 0.896 \text{ p.u.}$ and $E_n = 0.058 \text{ p.u.}$, corresponding to a degree of unbalance $E_n/E_p =$

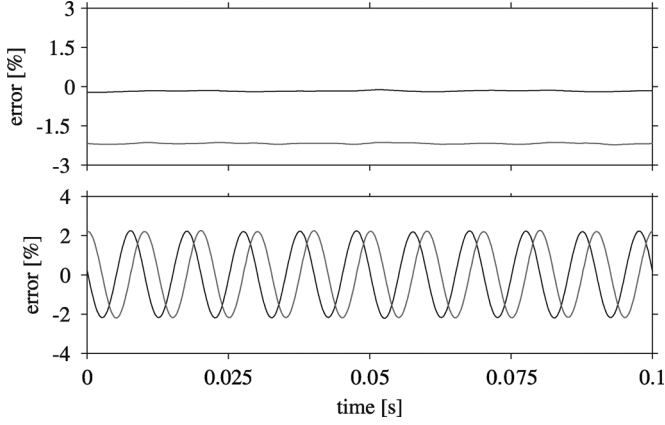


Fig. 8. Measured error in phase-sequence estimation with balanced steady-state voltage using System 2 $f_g = 50$ Hz, $f_s = 5060$ Hz. Top: Positive-sequence components in positive SRF (black: d -component; red: q -component). Bottom: Negative-sequence components in negative SRF (black: d -component; red: q -component).

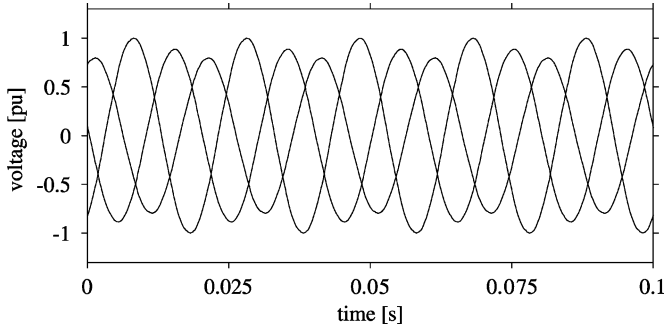


Fig. 9. Measured unbalanced three-phase grid voltages.

TABLE IV
CALCULATED COMPONENTS OF DETECTION ERROR FOR STUDY CASE
WITH UNBALANCED VOLTAGE, SYSTEM 1

Positive sequence					
$\Delta e_{pd1,off}^{(dqp)}$	-0.01%	$\Delta e_{pd1,osc}^{(dqp)}$	0.05%	δ_{pd1}	176.7°
$\Delta e_{pq1,off}^{(dqp)}$	0.83%	$\Delta e_{pq1,osc}^{(dqp)}$	0.05%	δ_{pq1}	86.7°
Negative sequence					
$\Delta e_{nd1,off}^{(dq n)}$	-0.05%	$\Delta e_{nd1,osc}^{(dq n)}$	0.84%	δ_{nd1}	270.5°
$\Delta e_{nq1,off}^{(dq n)}$	0.002%	$\Delta e_{nq1,osc}^{(dq n)}$	0.84%	δ_{nq1}	0.5°

6.49%. As for the balanced case, the positive-sequence voltage vector is taken as reference, thus $\varphi_p = 0^\circ$. For the negative sequence, $\varphi_n = 92.8^\circ$.

The voltage unbalance, together with nonideal sampling frequency, results in additional oscillatory error for the positive-sequence voltage. Calculated components of the detection error for System 1 and 2 are shown in Tables IV and V, respectively. The calculated errors displayed in Table IV can be compared with the measured errors of the estimated components of the grid voltage in the corresponding SRFs, displayed in Fig. 10 for System 1. It is of interest to observe that the oscillations in the measured error in the estimated positive-sequence components are not constant, which is in disagreement with (18). These oscillations present a maximum peak-to-peak value of 0.14% and

TABLE V
CALCULATED COMPONENTS OF DETECTION ERROR FOR STUDY CASE
WITH UNBALANCED VOLTAGE, SYSTEM 2

Positive sequence					
$\Delta e_{pd2,off}^{(dqp)}$	-0.04%	$\Delta e_{pd2,osc}^{(dqp)}$	0.13%	δ_{pd2}	358.4°
$\Delta e_{pq2,off}^{(dqp)}$	-1.95%	$\Delta e_{pq2,osc}^{(dqp)}$	0.13%	δ_{pq2}	268.4°
Negative sequence					
$\Delta e_{nd2,off}^{(dq n)}$	0.13%	$\Delta e_{nd2,osc}^{(dq n)}$	1.95%	δ_{nd2}	88.8°
$\Delta e_{nq2,off}^{(dq n)}$	-0.01%	$\Delta e_{nq2,osc}^{(dq n)}$	1.95%	δ_{nq2}	178.8°

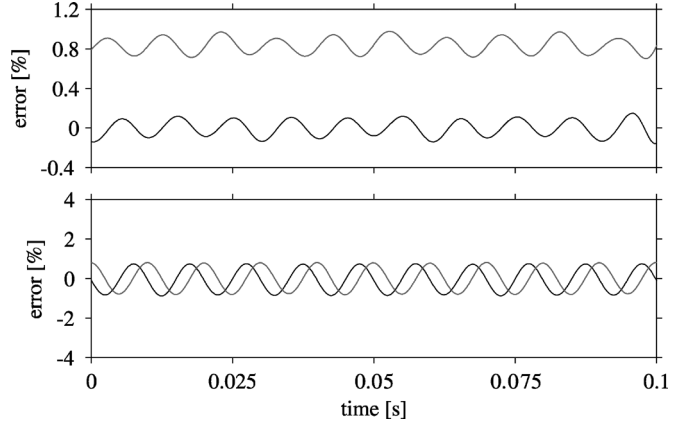


Fig. 10. Measured error in phase-sequence estimation with unbalanced steady-state voltage using System 1 $f_g = 50$ Hz, $f_s = 5060$ Hz. Top: Positive-sequence components in positive SRF (black: d -component; red: q -component). Bottom: Negative-sequence components in negative SRF (black: d -component; red: q -component).

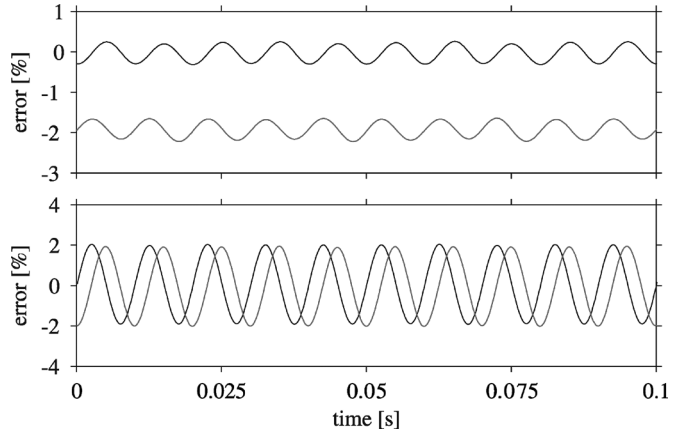


Fig. 11. Measured error in phase-sequence estimation with unbalanced steady-state voltage using System 2 $f_g = 50$ Hz, $f_s = 5060$ Hz. Top: Positive-sequence components in positive SRF (black: d -component; red: q -component). Bottom: Negative-sequence components in negative SRF (black: d -component; red: q -component).

a minimum value of 0.12%. This is because the analysis has been carried out assuming the ideal supply voltage and accurate knowledge of the grid voltage angle. However, in a real system, the supply voltage is affected by low-order harmonics, which results in an error in the estimated grid voltage angle [12], leading to a modulated signal in the dq -coordinate system. Analogous results have been obtained using System 2, as shown in Fig. 11.

V. METHODS TO REDUCE THE DETECTION ERROR

A. Reduction of Detection Error by Average Value

The variation of the error with the sampling frequency seen in Fig. 4 suggests the error can be reduced by averaging. Consider the average error defined as

$$\Delta e_{p,av}^{(\alpha\beta)}(kT_s) = \frac{1}{2} \left[\Delta e_{p1}^{(\alpha\beta)}(kT_s) + \Delta e_{p2}^{(\alpha\beta)}(kT_s) \right] \quad (27)$$

for the positive sequence and, similarly, as

$$\Delta e_{n,av}^{(\alpha\beta)}(kT_s) = \frac{1}{2} \left[\Delta e_{n1}^{(\alpha\beta)}(kT_s) + \Delta e_{n2}^{(\alpha\beta)}(kT_s) \right] \quad (28)$$

for the negative sequence. By substituting (15) for System 1 and 2 into (27), the average error for the positive-sequence results after some rewriting as

$$\Delta e_{p,av}^{(\alpha\beta)}(kT_s) = E_p e^{j(\omega_g kT_s - \frac{\pi}{2} + \varphi_p)} \lambda_{av} e^{j\gamma_{av}} + E_n e^{-j(\omega_g kT_s - \frac{\pi}{2} + \varphi_n)} \lambda_{av} e^{-j\gamma_{av}} \quad (29)$$

with

$$\lambda_{av} = \frac{1}{2} \sqrt{[\lambda_1^2 + \lambda_2^2 + 2\lambda_1\lambda_2 \cos(\gamma_1 - \gamma_2)]} \quad (30)$$

$$\gamma_{av} = \arctan \left(\frac{\lambda_1 \sin \gamma_1 + \lambda_2 \sin \gamma_2}{\lambda_1 \cos \gamma_1 + \lambda_2 \cos \gamma_2} \right). \quad (31)$$

Let the system characterized by λ_{av} and γ_{av} be called “System 3.” By using (12) and (13), (30) and (31) can be rewritten as

$$\lambda_{av} = \sqrt{\frac{1}{8} \left[3 - 2 \cos \frac{\pi}{2} \kappa_1 - 2 \cos \frac{\pi}{2} \kappa_2 + \cos \frac{\pi}{2} (\kappa_1 - \kappa_2) \right]} \quad (32)$$

$$\gamma_{av} = \arctan \left(\frac{\cos \frac{\pi}{2} \kappa_1 + \cos \frac{\pi}{2} \kappa_2 - 2}{\sin \frac{\pi}{2} \kappa_1 + \sin \frac{\pi}{2} \kappa_2} \right). \quad (33)$$

Fig. 12 shows amplitude and phase of the positive-sequence estimation error for System 1, 2, and 3. There is a rather large range of frequencies for which the average error λ_{av} is considerably lower than λ_1 and λ_2 . Averaging should thus lead to reduce the detection error due to discretization.

Equation (29) can be further written in the positive SRF as

$$\Delta e_{p,av}^{(dqp)}(kT_s) = E_p \lambda_{av} e^{j(\varphi_p + \gamma_{av} - \frac{\pi}{2})} + E_n \lambda_{av} e^{-j(2\omega_g kT_s - \frac{\pi}{2} + \varphi_n + \gamma_{av})}. \quad (34)$$

With similar derivation, the average error for the negative sequence in the negatively rotating SRF can be expressed as

$$\Delta e_{n,av}^{(dqn)}(kT_s) = E_p \lambda_{av} e^{j(2\omega_g kT_s - \frac{\pi}{2} + \varphi_p - \gamma_{av})} + E_n \lambda_{av} e^{-j(\varphi_n - \gamma_{av} - \frac{\pi}{2})}. \quad (35)$$

Similar expressions as in (21)–(24) hold for the components, with an additional subscript “av” indicating System 3.

B. Reduction of Detection Error by Weighted Mean Value

When using weighted mean averaging, the error is defined as

$$\Delta e_{p,w}^{(\alpha\beta)}(kT_s) = a \Delta e_{p1}^{(\alpha\beta)}(kT_s) + (1 - a) \Delta e_{p2}^{(\alpha\beta)}(kT_s) \quad (36)$$

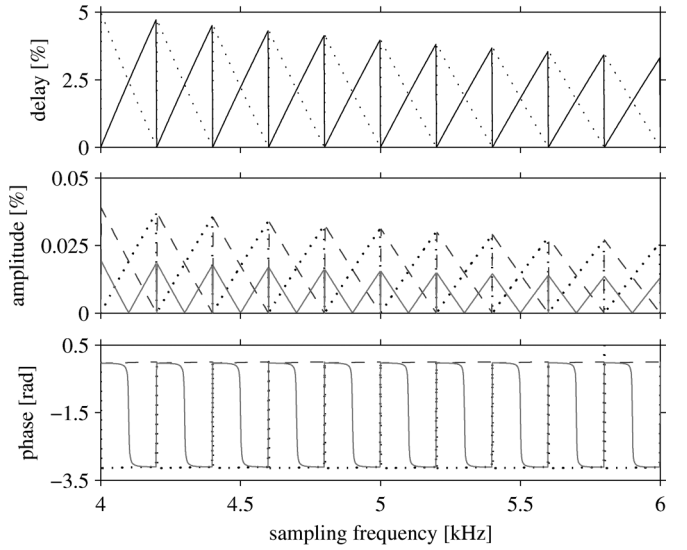


Fig. 12. Top: relative time delay error obtained by rounding down (κ_1 , solid) or up (κ_2 , dotted) to nearest integer. Middle: amplitude of relative estimation error for System 1 (λ_1 , black dotted), System 2 (λ_2 , blue dashed) and System 3 (λ_{av} , solid). Bottom: phase of relative estimation error for System 1 (γ_1 , black dotted), System 2 (γ_2 , blue dashed) and System 3 (γ_{av} , solid).

for the positive sequence and

$$\Delta e_{n,w}^{(\alpha\beta)}(kT_s) = a \Delta e_{n1}^{(\alpha\beta)}(kT_s) + (1 - a) \Delta e_{n2}^{(\alpha\beta)}(kT_s) \quad (37)$$

for the negative sequence, where the parameter $0 \leq a \leq 1$ is chosen to minimize the error, which depends on ΔT_1 and ΔT_2 . To reduce the complexity of the coming derivation, the delay error Δn , defined as $\Delta n = -\Delta T_1/T_s = 1 - \Delta T_2/T_s$, is used. The relative time errors become

$$\kappa_1 = n_{d1}/(n_{d1} + \Delta n) - 1 \quad (38)$$

$$\kappa_2 = (n_{d1} + 1)/(n_{d1} + \Delta n) - 1. \quad (39)$$

The positive-sequence error in (36) is rewritten as

$$\Delta e_{p,w}^{(\alpha\beta)}(kT_s) = E_p e^{j(\omega_g kT_s - \frac{\pi}{2} + \varphi_p)} \lambda_w e^{j\gamma_w} + E_n e^{-j(\omega_g kT_s - \frac{\pi}{2} + \varphi_p)} \lambda_w e^{-j\gamma_w} \quad (40)$$

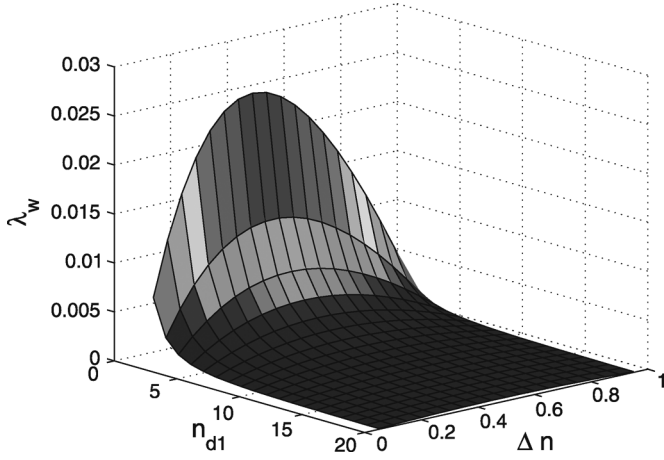
where the amplitude and phase error λ_w and γ_w are a function of n_{d1} , Δn and a . To find the value of a that minimizes λ_w for each n_{d1} and Δn , the derivative of λ_w is nullified. The obtained expression for the parameter a can then be simplified using Taylor expansion and considering the first term only, giving

$$a = 1 - \Delta n. \quad (41)$$

Amplitude and phase of the relative estimation error λ_w and γ_w can be written as (42)–(43), shown at the bottom of the next page, where

$$v_1 = \frac{\pi}{2} \frac{n_{d1}}{n_{d1} + \Delta n} \quad v_2 = \frac{\pi}{2} \frac{n_{d1} + 1}{n_{d1} + \Delta n}.$$

It is of interest to observe that the maximum error in λ_w is less than 0.2% when $f_s/f_g \geq 16$. As shown in Fig. 13, the error decreases quickly with increasing n_{d1} .

Fig. 13. Trend of λ_w as a function of Δn and n_{d1} .

Similar to the previous sections, the estimated weighted error for the positive and negative sequences in the corresponding rotating SRF is derived as

$$\Delta e_{p,w}^{(dqp)}(kT_s) = E_p \lambda_w e^{j(\varphi_p + \gamma_w - \frac{\pi}{2})} + E_n \lambda_w e^{-j(2\omega_g kT_s - \frac{\pi}{2} + \varphi_n + \gamma_w)} \quad (44)$$

$$\Delta e_{n,w}^{(dqn)}(kT_s) = E_p \lambda_w e^{j(2\omega_g kT_s - \frac{\pi}{2} + \varphi_p - \gamma_w)} + E_n \lambda_w e^{-j(\varphi_n - \gamma_w - \frac{\pi}{2})}. \quad (45)$$

Similar expressions as in (21)–(24) hold for the error components, with the additional subscript “w” indicating System 4.

VI. EXPERIMENTAL VERIFICATION USING AVERAGING AND WEIGHTED MEAN AVERAGING

The proposed methods have been tested experimentally under the same unbalanced condition of the grid voltage as in Section IV-C. DSC parameters and calculated components of the detection error when using System 3 and 4 are tabulated in Tables VI and VII, respectively. The measured error of the estimated positive- and negative-sequence components of the grid voltage when using System 3 and 4 are plotted in Figs. 14 and 15, respectively. The measurement results confirm the calculations above. In Fig. 15, only the error introduced by the PLL can be observed in the measured errors.

For System 3, the error in the estimated sequence components is reduced compared with System 1 (see Table IV). The oscillatory term is reduced by about one third for both sequences. However, the offsets are still not zero and it can be noticed that both $\Delta e_{pdw,off}^{(dqp)}$ and $\Delta e_{ngav,off}^{(dqn)}$ have increased compared with the values obtained using System 1. As expected, a better estimation can be achieved by using weighted mean average of the

TABLE VI
DSC PARAMETERS AND CALCULATED DETECTION ERRORS FOR STUDY CASE WITH UNBALANCED VOLTAGE, SYSTEM 3

$\lambda_{av} = 0.62\%$			$\gamma_{av} = 357.4^\circ$		
Positive sequence					
$\Delta e_{pdav,off}^{(dqp)}$	-0.03%	$\Delta e_{pdav,osc}^{(dqp)}$	0.04%	δ_{pdav}	359.8°
$\Delta e_{pqav,off}^{(dqp)}$	-0.56%	$\Delta e_{pqav,osc}^{(dqp)}$	0.04%	δ_{pqav}	269.8°
Negative sequence					
$\Delta e_{ndav,off}^{(dqn)}$	0.04%	$\Delta e_{ndav,osc}^{(dqn)}$	0.56%	δ_{ndav}	87.4°
$\Delta e_{ngav,off}^{(dqn)}$	-0.003%	$\Delta e_{ngav,osc}^{(dqn)}$	0.56%	δ_{ngav}	177.4°

TABLE VII
DSC PARAMETERS AND CALCULATED DETECTION ERRORS FOR STUDY CASE WITH UNBALANCED VOLTAGE, SYSTEM 4

$\lambda_w = 0.02\%$			$\gamma_w = 269.5^\circ$		
Positive sequence					
$\Delta e_{pdw,off}^{(dqp)}$	-0.02%	$\Delta e_{pdw,osc}^{(dqp)}$	0.001%	δ_{pdw}	87.7°
$\Delta e_{pqw,off}^{(dqp)}$	$\simeq 0\%$	$\Delta e_{pqw,osc}^{(dqp)}$	0.001%	δ_{pqw}	357.7°
Negative sequence					
$\Delta e_{ndw,off}^{(dqn)}$	$\simeq 0\%$	$\Delta e_{ndw,osc}^{(dqn)}$	0.02%	δ_{ndw}	359.5°
$\Delta e_{ngw,off}^{(dqn)}$	-0.001%	$\Delta e_{ngw,osc}^{(dqn)}$	0.02%	δ_{ngw}	89.5°

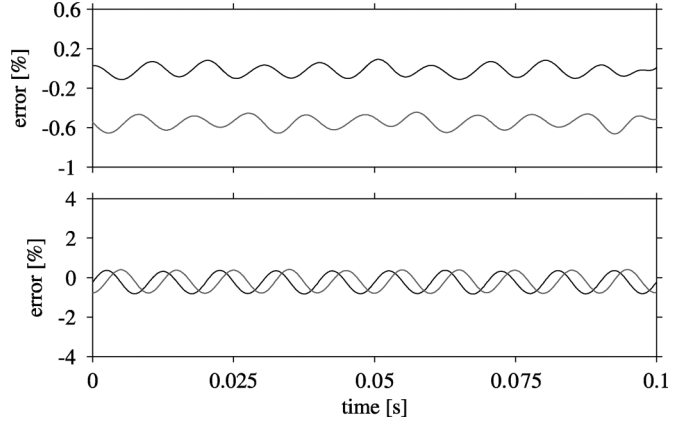


Fig. 14. Measured error in phase-sequence estimation during unbalanced steady-state voltage using System 3 $f_g = 50$ Hz, $f_s = 5060$ Hz. Top: positive-sequence components in positive SRF (black: d -component; red: q -component). Bottom: negative-sequence components in negative SRF (black: d -component; red: q -component).

error. From Table VII, it can be observed that both the constant and the oscillatory terms of the error have been drastically reduced as compared with System 1 and 3, with the exception of $\Delta e_{pdw,off}^{(dqp)}$, which is still slightly higher as compared with the value obtained for System 1.

Finally, Fig. 16 shows a comparison between measured and calculated amplitude of the relative estimation error when the

$$\lambda_w = \sqrt{\left(\frac{\Delta n - 1}{2} \cos v_1 - \frac{\Delta n}{2} \cos v_2\right)^2 + \left[\frac{1 - \Delta n}{2}(\sin v_1 - 1) + \frac{\Delta n}{2}(\sin v_2 - 1)\right]^2} \quad (42)$$

$$\gamma_w = \arctan\left(\frac{a\lambda_1 \sin \gamma_1 + (1-a)\lambda_2 \sin \gamma_2}{a\lambda_1 \cos \gamma_1 + (1-a)\lambda_2 \cos \gamma_2}\right) \quad (43)$$

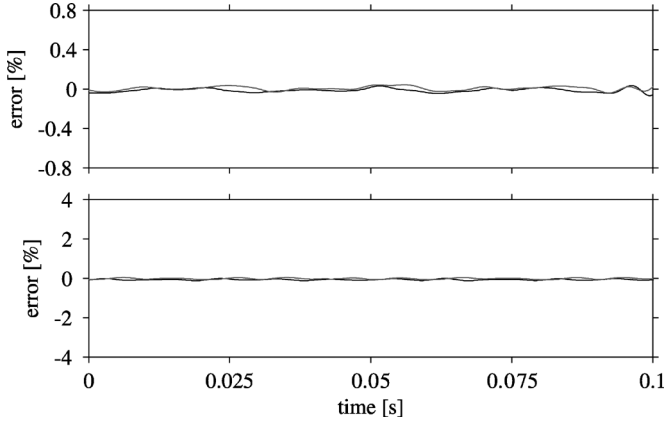


Fig. 15. Measured error in phase-sequence estimation during unbalanced steady-state voltage using System 4, $f_g = 50$ Hz, $f_s = 5060$ Hz. Top: Positive sequence components in positive SRF (black: d -component; red: q -component). Bottom: Negative sequence components in negative SRF (black: d -component; red: q -component).

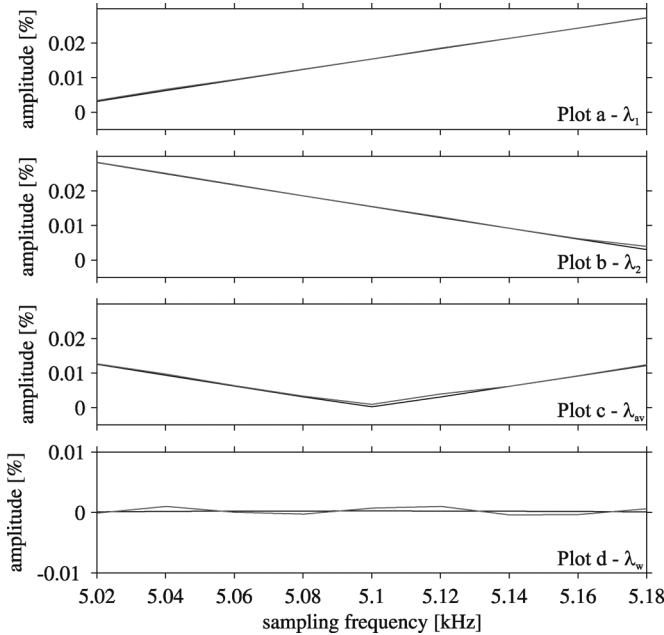


Fig. 16. Comparison between calculated (black) and measured (red) amplitude of relative estimation error when using System 1 (Plot a), System 2 (Plot b), System 3 (Plot c), and System 4 (Plot d).

sampling frequency is varied between 5.02 and 5.18 kHz using System 1 (λ_1 , Plot a), System 2 (λ_2 , Plot b), System 3 (λ_{av} , Plot c) and System 4 (λ_w , Plot d). The measurement results confirm the analytical models very well. Only a small difference in the measurement results (more evident for λ_w , due to the lower scale of the figure), due to the nonideal grid voltage, can be observed.

VII. GRID FREQUENCY INFLUENCE ON DSC

In this section, the influence of the grid frequency on DSC is studied. The grid frequency always fluctuates. However, for

a stiff grid, the fluctuation is normally small. Exceptionally, during major grid disturbances or in island grids, large fluctuations in the grid frequency can be experienced and, therefore, it can be of interest to study their influence on the DSC method. Equation (1) describes the grid voltage vector in the $\alpha\beta$ -plane in the generic case of unsymmetric grid and in continuous time domain. The angular frequency ω_g can be written as

$$\omega_g = 2\pi f_g = 2\pi(f_{g0} + \Delta f_g) \quad (46)$$

where f_{g0} and Δf_g are nominal frequency and frequency deviation, respectively. The grid period can be written as

$$T_g = 1/f_g = T_{g0} + \Delta T_g \quad (47)$$

yielding

$$\Delta T_g/T_g = -\Delta f_g/f_{g0}. \quad (48)$$

The separation method uses the nominal grid period in order to separate the positive and the negative phase sequence components. Using (47) in (2), the estimated positive-sequence voltage in the $\alpha\beta$ -coordinate system becomes

$$\hat{\underline{e}}_p^{(\alpha\beta)}(t) = E_p \lambda e^{j(\omega_g t - \frac{\pi}{2} + \varphi_p + \gamma)} + E_n \lambda e^{-j(\omega_g t - \frac{\pi}{2} + \varphi_n + \gamma)} \quad (49)$$

where

$$\lambda = \sqrt{\frac{1}{2} \left(1 - \cos \left(\frac{\pi}{2} \kappa_T \right) \right)} \quad (50)$$

and

$$\gamma = \arctan \left[\frac{\cos \left(\frac{\pi}{2} \kappa_T \right) - 1}{\sin \left(\frac{\pi}{2} \kappa_T \right)} \right] \quad (51)$$

with κ_T the relative grid period error, defined by

$$\kappa_T = \Delta T_g/T_g. \quad (52)$$

Analogously, substituting from (47) into (3), the negative phase-sequence component becomes

$$\hat{\underline{e}}_n^{(\alpha\beta)}(t) = \underline{e}_n^{(\alpha\beta)}(t) + E_p \lambda e^{j(\omega_g t - \frac{\pi}{2} + \varphi_p + \gamma)} + \underline{e}_n^{(\alpha\beta)}(t) + E_n \lambda e^{-j(\omega_g t - \frac{\pi}{2} + \varphi_n + \gamma)}. \quad (53)$$

As shown in (49) and (53), the error due to grid frequency deviation has the same expression as the error due to nonideal discretization. Hence, the previous derivations can be applied to this case without any modifications. Thus, the previous figures can be reused in order to determine the estimation errors.

VIII. CONCLUSION

In this paper, the use of a method for online detection of positive- and negative-sequence components of three-phase quantities, named the delayed signal cancellation (DSC) method, has been investigated.

It has been shown that in practical implementation of the method in a computer-controlled system, a detection error may arise due to nonideal discretization, unless the sampling frequency can be chosen to be in a specific relation with the

grid frequency, which is normally not the case. Analytical expressions of the error have been derived and calculations have been verified experimentally via three-phase grid voltage measurements obtained with a state-of-the-art digital signal processor (DSP) system. Both cases of balanced and unbalanced grid voltage have been shown.

Furthermore, two methods have been presented to reduce the detection error. Key equations for both methods have been derived and again verified experimentally. It has been shown that the method based on weighted averaging allows achieving a drastic reduction of the detection error.

Finally, since the DSC method relies on accurate knowledge of the grid frequency, the effect of grid frequency variations has been considered. It has been demonstrated that the error due to inaccurate knowledge of the grid frequency and the error due to nonideal discretization have the same expression and, therefore, the given derivations, including the proposed methods for reducing the detection errors, can be applied without modifications to the case of grid frequency variations.

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