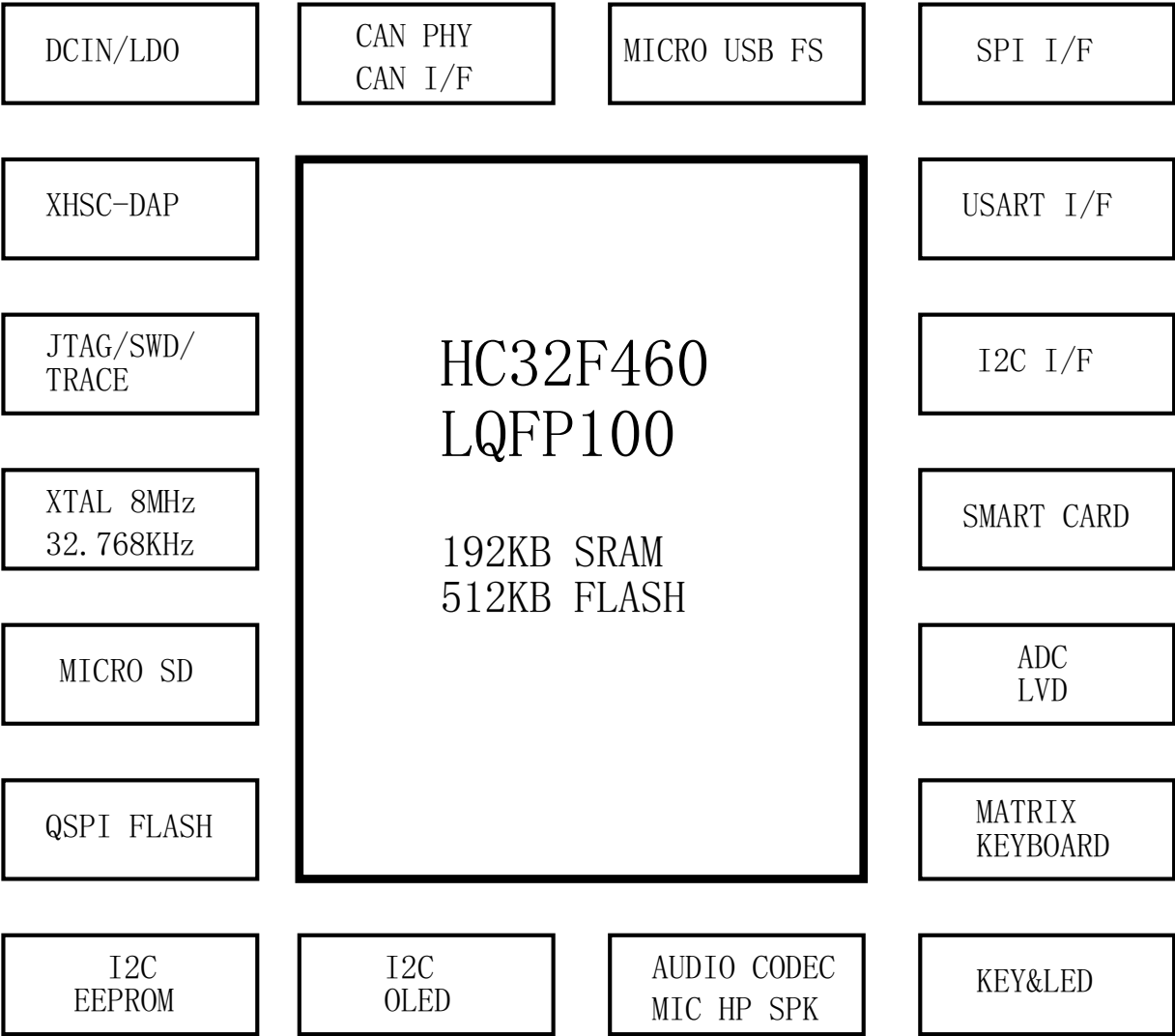


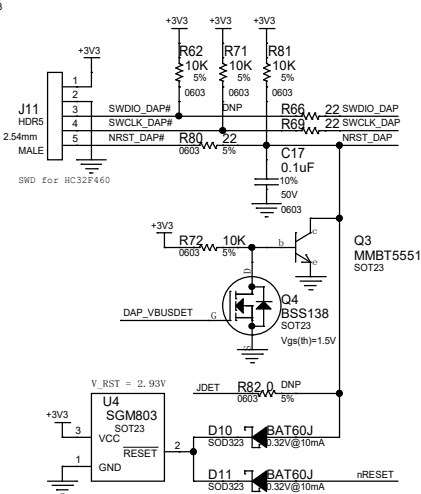
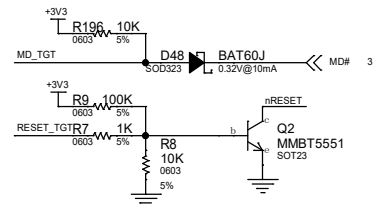
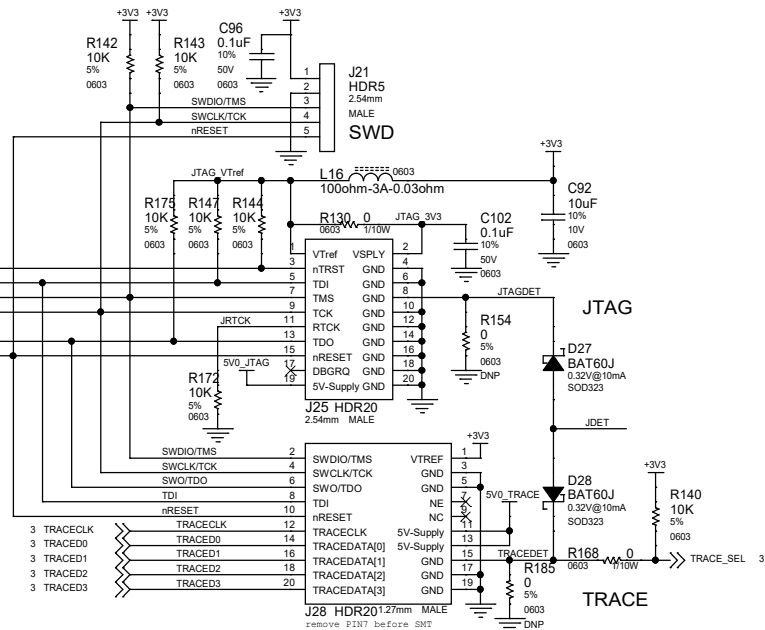
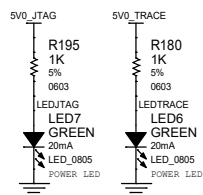
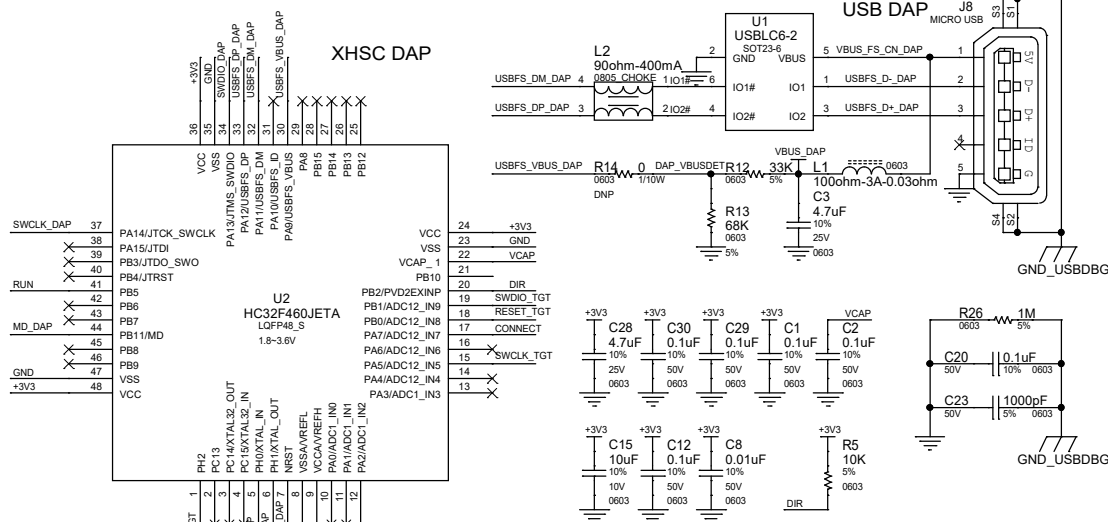
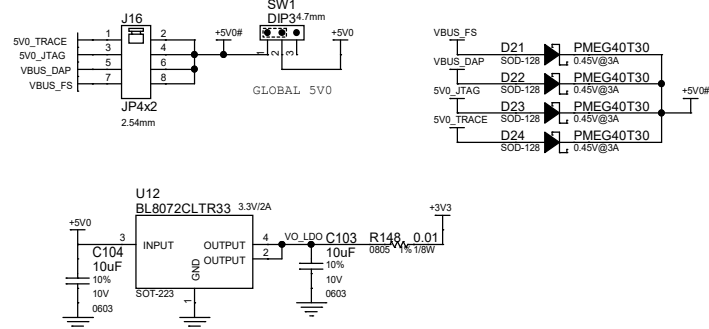
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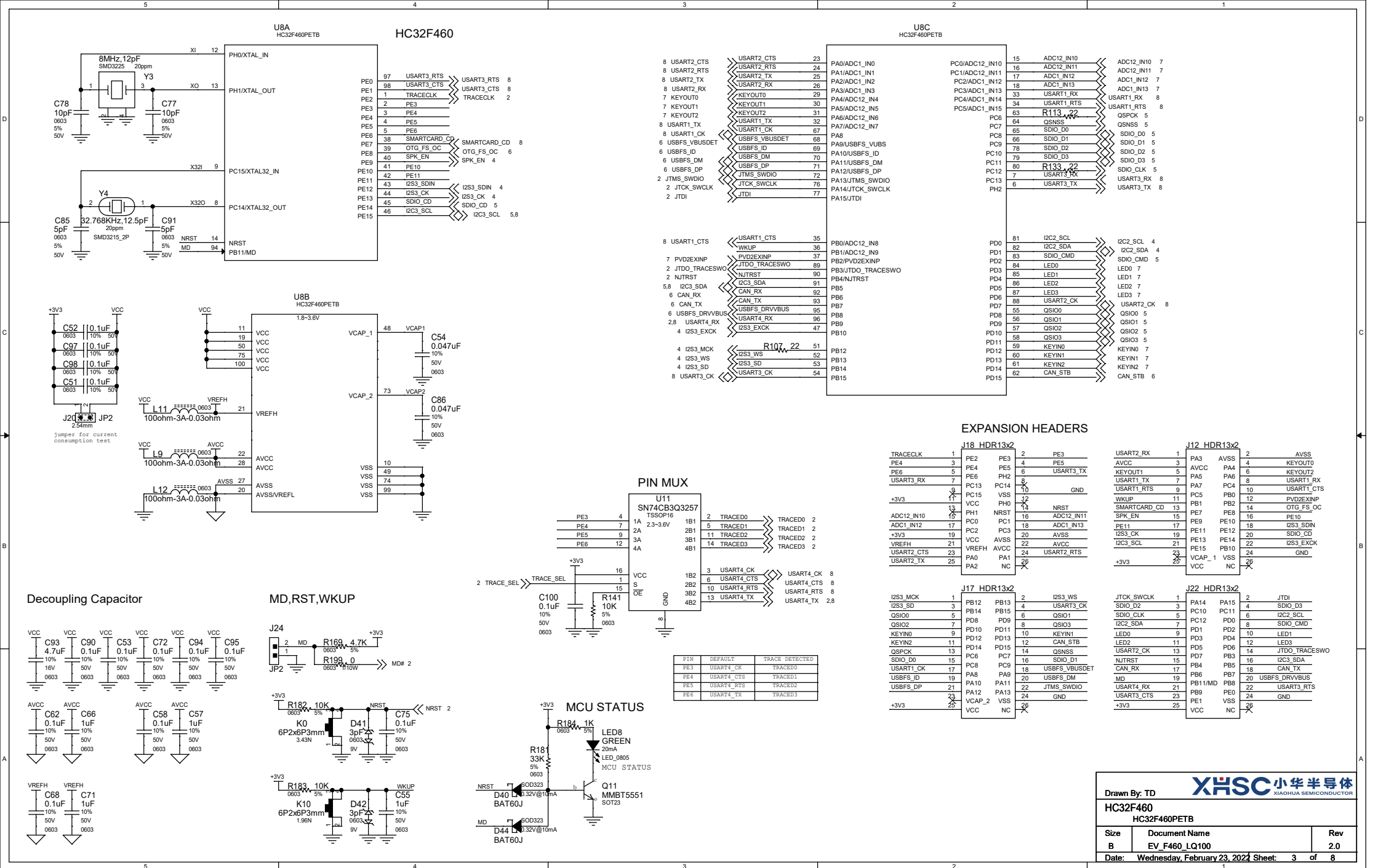
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Page 02	POWER,DEBUG
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Page 07	ADC,KEYS,LEDS
Page 08	USART,SPI,I2C,SMART CARD

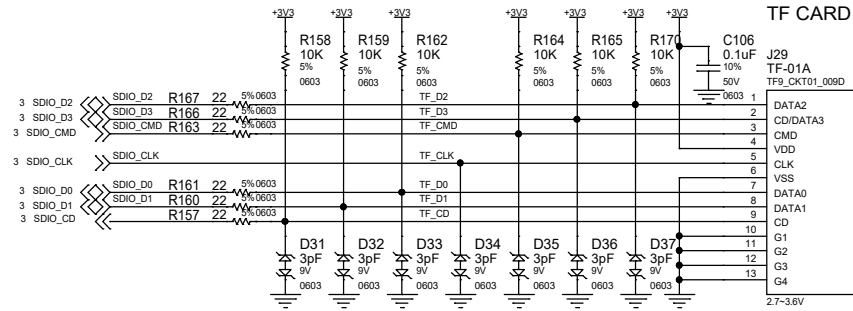
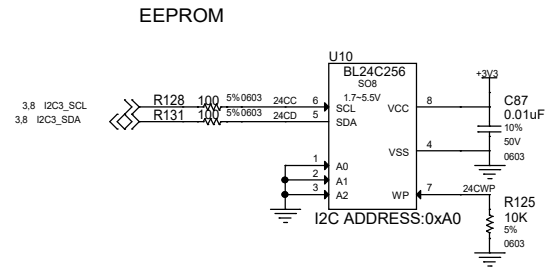
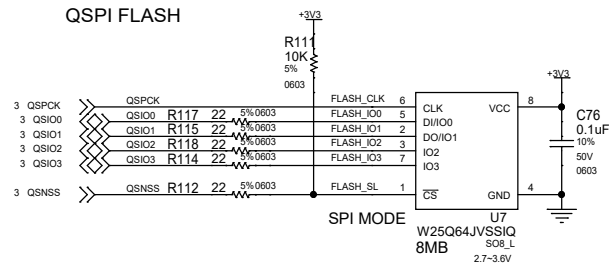
Revision History

Rev	Date	Description
1.0	08/21/2018	- New design.
1.1	12/12/2018	- Changed UART connector to match SmartCard. - Added R143~R147 to I2S signals. - Added R143 as pull-up resistor to SPI NSS. - Changed matrix keyboard output pin,usart2_ck pin,and LED1 control pin. - Changed CAN and I2C IO pin.
1.2	01/11/2019	- Update HC32F460 pin definition. - C28 is now DNP.
1.3	09/23/2019	- Replaced C72 by R148. - Added C94,C95,C96 to VCCA/VREF. - Removed R25,R26.
2.0	02/23/2022	- DAP module added. - Pin assignment modified. - Analog decoupling capacitor modified.





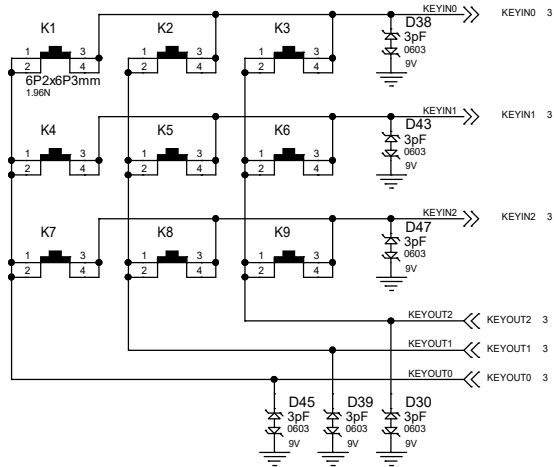




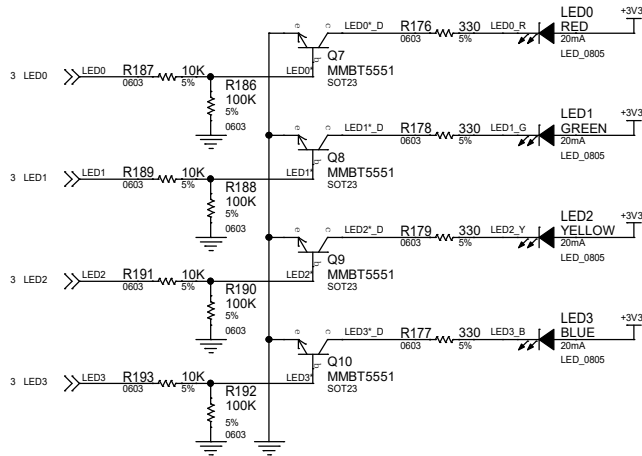
Drawn By: TD		
HC32F460 AUDIO		
Size B	Document Name EV_F460_LQ100	Rev 2.0
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XHSC 小华半导体
XIAOHUA SEMICONDUCTOR

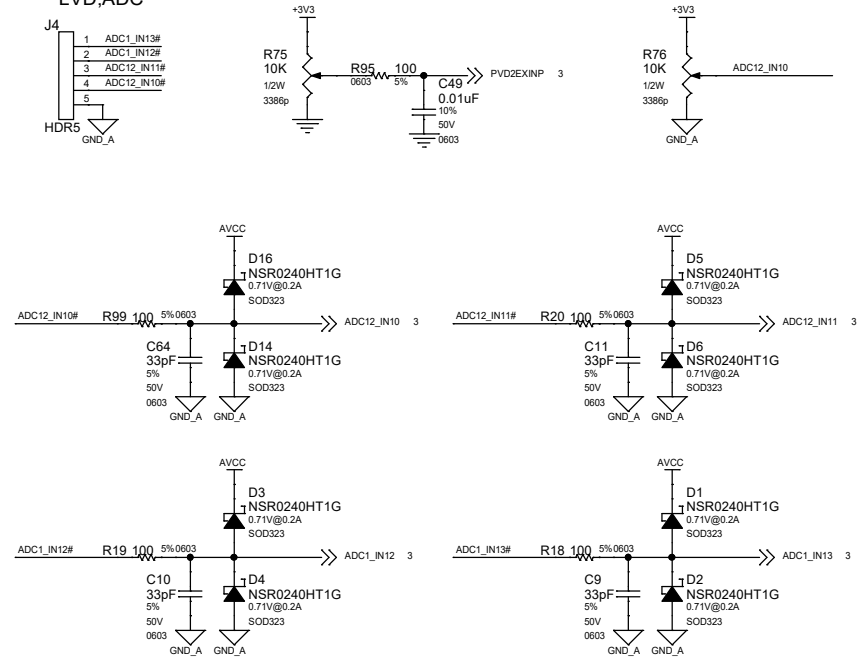
MATRIX KEYBOARD



LEDS

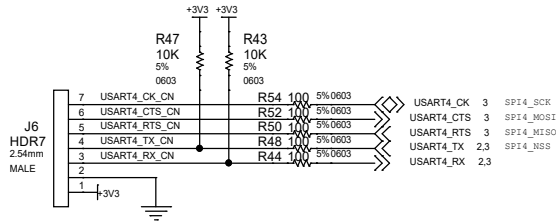
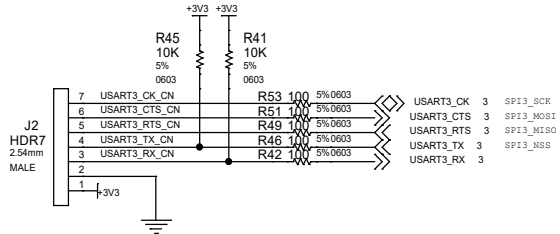
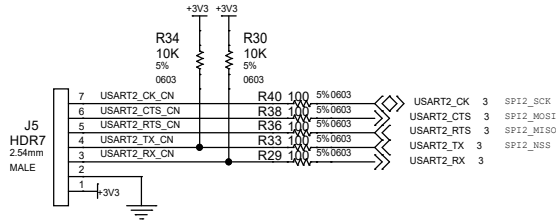
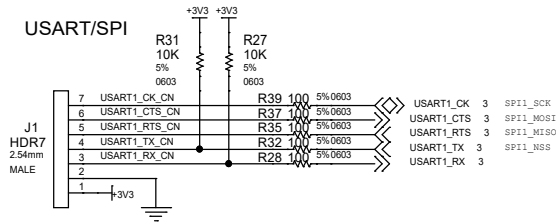


LVD,ADC

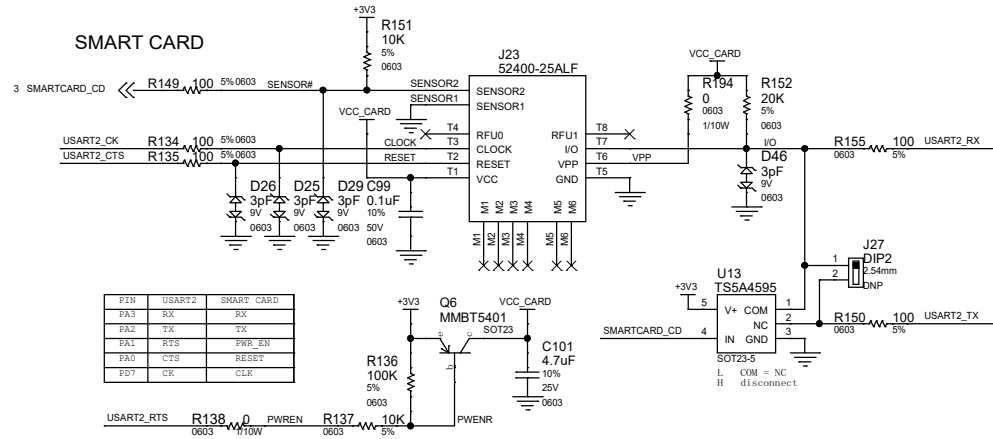


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HC32F460		
ADC,KEYS,LEDS		
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USART/SPI

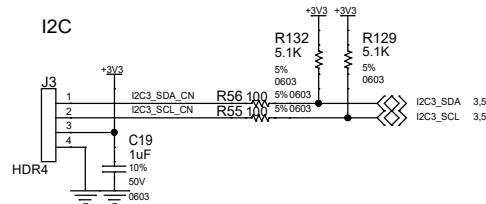


SMART CARD

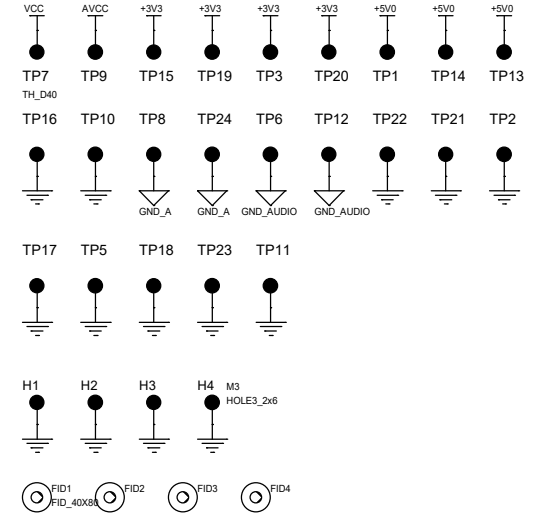


FIN	USART2	SMART CARD
PA3	RX	RX
PA2	TX	TX
PA1	RXS	PWR_EN
PA0	CTS	RESET
PD7	CK	CLK

I2C



TP/FID/FAB HOLES



Drawn By: TD		
HC32F460		
USART,SPI,I2C,SMART CARD		
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