

Proj.2

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I. INTRODUCTION

This is [1]

REFERENCES

- [1] Yufei Ma et al. “Optimizing the Convolution Operation to Accelerate Deep Neural Networks on FPGA”. In: *IEEE Transactions on Very Large Scale Integration (VLSI) Systems* 26.7 (July 2018), pp. 1354–1367. ISSN: 1557-9999. DOI: 10.1109/TVLSI.2018.2815603.