Runtime Profiling: Latencies and Scheduling on Floating-Point and Hybrid Float 6 Accelerators

Tensor Op	CPU (ms)	TP FP32 (ms)	Gain	TP HF6 (ms)	Gain
$Conv_A$	112.24				
$Conv_B$	213.13				
$Conv_C$	46.59				

CPU + TP (Floating-Point Xilinx LogiCORE IP @ 200 MHz)

Total = 48 ms (20.8 FPS)