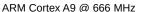
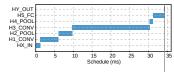
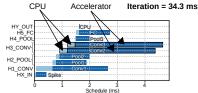
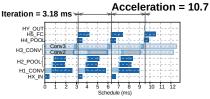
Deployment

Floating-point 32-bit









Hybrid logarithmic 4-bit

