

c) CPU + TP (Hybrid-Float64 @ 200 MHz)

Tensor operation

FULLY_CONNECTED

FULLY_CONNECTED

FULLY_CONNECTED

RESHAPE

MAX_POOL_2D

ADD

MUL

HARDWARE

DELEGATE

(Conv_c) CONV_2D

MAX_POOL_2D

ADD

MUL

HARDWARE

DELEGATE

(Conv_B) CONV_2D

MAX_POOL_2D

ADD

MUL

HARDWARE

DELEGATE

(Conv_A) CONV_2D

MODEL

