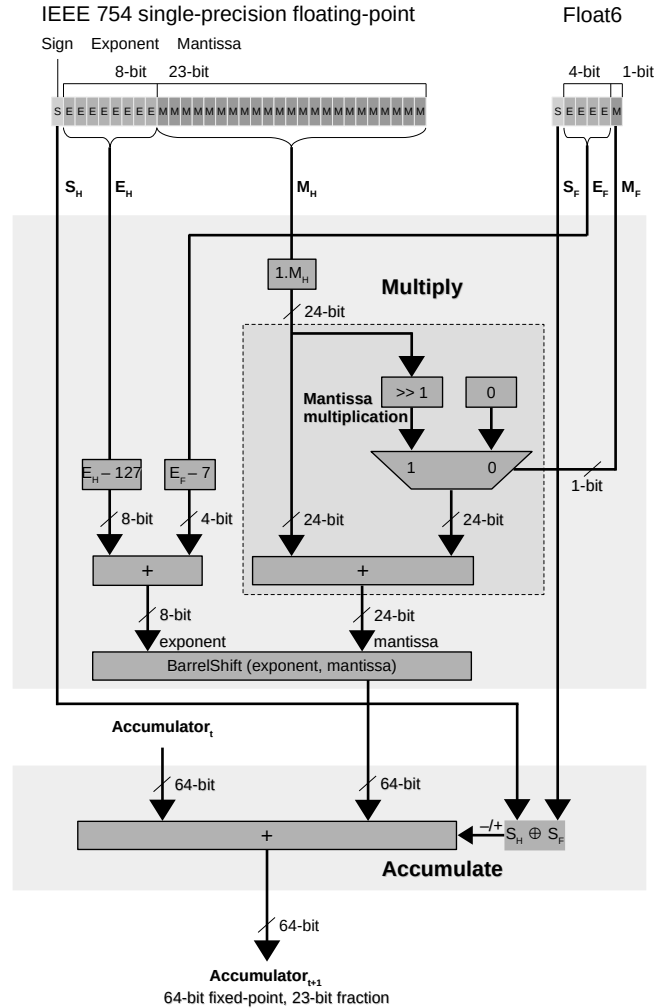


Hybrid Custom Floating-Point Multiply-Accumulate Unit



Loop pipelining:

$$\text{Latency} = (II \times (N - 1)) + IL$$

Latency = $N + 7$

Infinity and NaN: Not expected

Subnormal numbers are approximated as normal to simplify hardware design.

When $E_F = 0$ and $M_F = 1$
Approximation error = 3.9e-3

Leverage intrinsic error tolerance