

# Runtime Profiling: Latencies and Scheduling on Floating-Point and Hybrid Float 6 Accelerators

Tensor Op	CPU (ms)	TP FP32 (ms)	Gain	TP HF6 (ms)	Gain
Conv <sub>A</sub>	112.24				
Conv <sub>B</sub>					
Conv <sub>C</sub>					

CPU (ARM Cortex-A9 @ 666 MHz)

