Trans-Precision Neural Network Deployment for Low-Power Embedded Systems

K Keras

TensorFlow

HF6 Quantization

SD

Cost-optimized FPGAs $89

Train model → Convert to TF Lite → Deploy model → Run inference
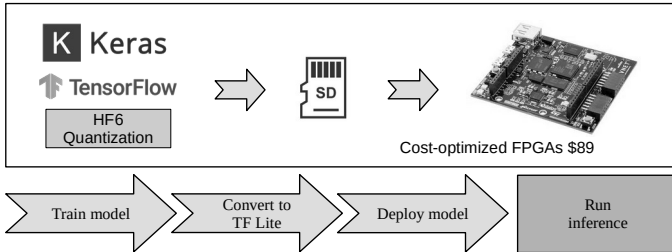
- TinyML

- Floating-point acceleration

- Resource-constrained FPGAs

- Research and general purpose