Runtime Profiling: Latencies and Scheduling on Floating-Point and Hybrid Float 6 Accelerators

Tensor Op	CPU (ms)	TP FP32 (ms) Gain	TP HF6 (ms)	Gain
Conv _A	112.24			
Conv _B	213.13			
Conv _c	46.59			

CPU + TP (Floating-Point Xilinx LogiCORE IP @ 200 MHz)

