Convolutional Neural Network Accelerator for TensorFlow Lite on Embedded FPGA

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Abstract—

*Index Terms—*Artificial intelligence, spiking neural networks, approximate computing, logarithmic, parameterisable floating-point, optimization, hardware accelerator, embedded systems, FPGA

I. Introduction

II. Related work

III. Background

IV. System Design

V. Experimental results

VI. Conclusions

Acknowledgments