

Yarib Israel Nevárez Esparza.

Place and date of birth: Durango, Mexico. 05/26/1986

Nationality: Mexican

Marital status: Single

Profession: Electronics engineer

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GitHub portfolio: [YaribNevarez \(Yarib Nevarez\)](https://github.com/YaribNevarez) <https://github.com/YaribNevarez>

Education.

Spring 2009	Bachelor of Science in Electronics engineering Instituto Tecnológico de Durango Durango, México.
Fall 2017	Master of Science in Embedded Systems Design University of Applied Sciences Bremerhaven Bremerhaven Bremen, Germany

Professional experience.

January 2009 – June 2011	Texas Instruments (<i>Embedded software engineer – contractor</i>)
Mexico	Support and development of embedded GUI applications. Software architecture and development of data structures for handling color and images for TI – NSPIRE Graphic Calculator.
June 2011 – March 2012	IBM (<i>Software engineer – contractor</i>)
Mexico	Software architecture and development of infrastructure for parsing compressed data. Design and development of Qt application tool for Anaconda 4 th generation storage library, and support of MFC application tool for TS3500 storage library.
March 2012 – March 2013	Continental Automotive (<i>Embedded software engineer</i>)
Mexico	Support of firmware bootloaders and drivers for automotive cluster panel of GM and Chrysler. Platform adaptation for cluster panel of Suzuki. Implementation of new features for Toyota bootloader.
March 2013 – September 2015	IBM TOSHIBA TGCS (<i>Staff software engineer</i>)
Mexico	Operating system field defect support, Linux-4690 POS machines. Field defect support and development of customer application for POS machines.
October 2015 – March 2017	University of Applied Sciences Bremerhaven (<i>Student - Master of Science In Embedded Systems Design</i>)
Germany	Real-time software, model-based software development, digital systems - VHDL, AP SoC design, discrete control, DSP design, and analog-digital instrumentation.
May 2017 – October 2017	E.I.S. Electronics GmbH (<i>Master thesis intern</i>)
Germany	Thesis: "Concept of smart avionics controller involving the integration of internet protocols, file systems, data acquisition, processing, and storage." Platform: FPGA SoC (ARM Cortex-A9), and embedded Linux.
January 2018 - May 2019	Carbon Robotics Inc (<i>Embedded Systems Engineer</i>)
The USA remote work	Hardware/Software Co-Design of platform based on Xilinx Zynq device. VHDL hardware implementation. Linux device drivers development for dedicated hardware. Support and development of middleware and low-level layers of robotic drivers for UNIX systems. Support and development of GUI Qt application tools for robotics.



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