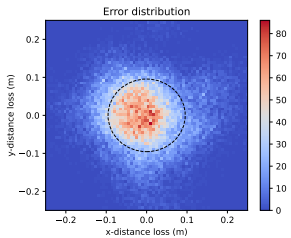
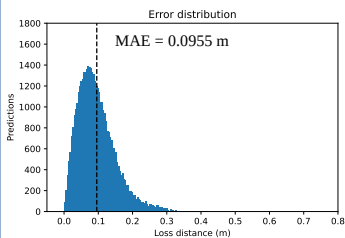
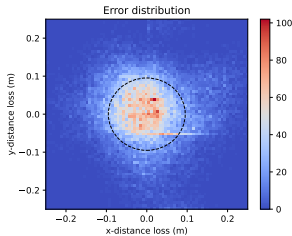
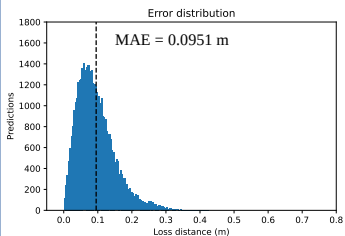


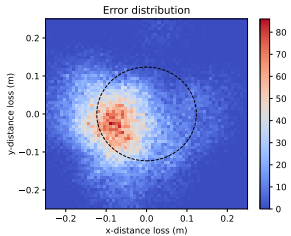
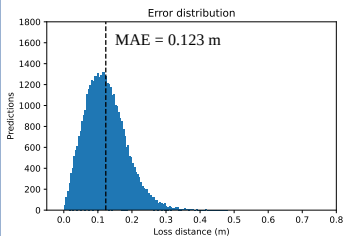
a) Floating-Point 32-bit



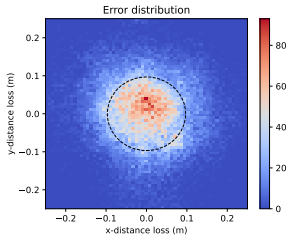
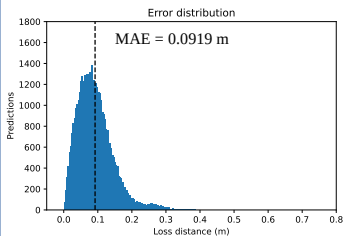
b) Fixed-Point 8-bit



c) Hybrid-Float6 (E4M1) without quantization aware training



d) Hybrid-Float6 (E4M1) with quantization aware training



e) Hybrid-Logarithmic 6-bit (E5M0) with quantization aware training

