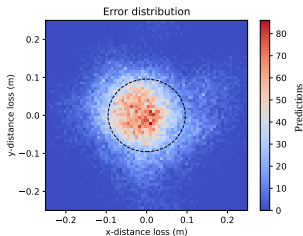
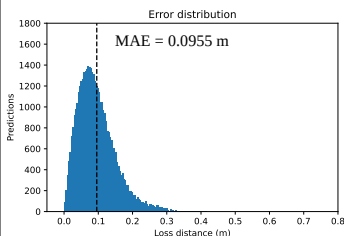
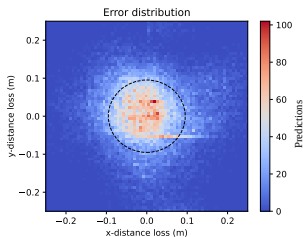
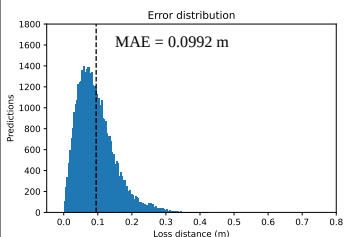


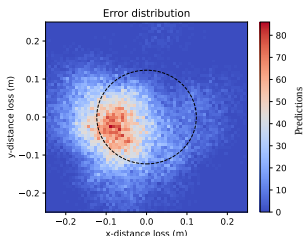
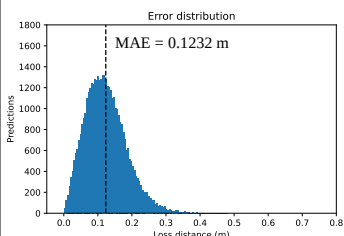
a) Floating-Point 32-bit



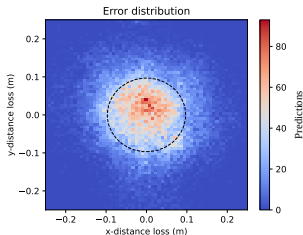
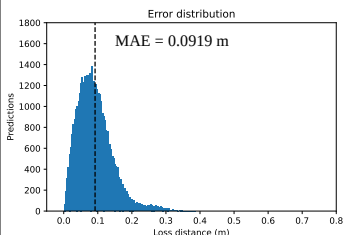
b) Fixed-Point 8-bit



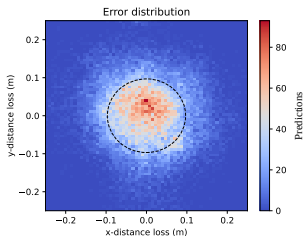
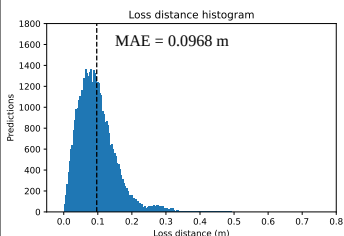
c) Hybrid-Float6 (E4M1) without QAT



d) Hybrid-Float6 (E4M1) with QAT



e) Hybrid-Logarithmic 6-bit (E5M0) with QAT



f) MSE and MAE improvement

