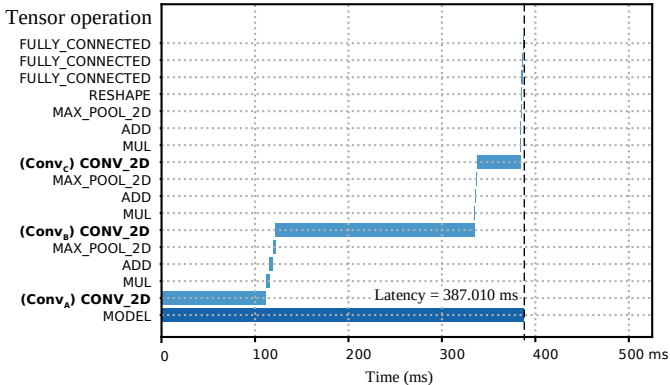
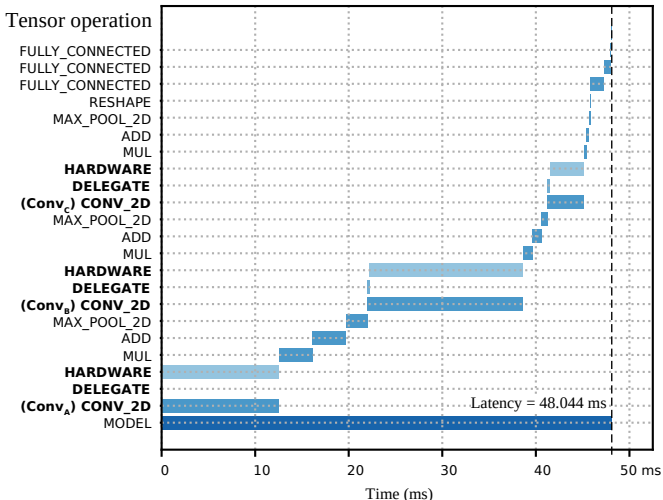


a) CPU (ARM Cortex-A9)



b) CPU + TP (Floating-Point Xilinx LogiCORE IP)



c) CPU + TP (Hybrid-Float6)

