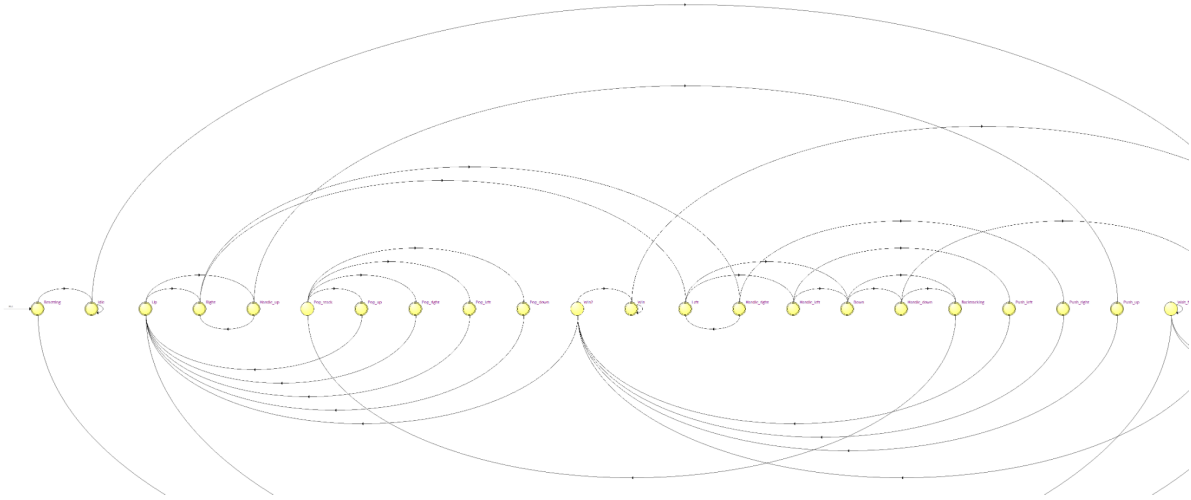
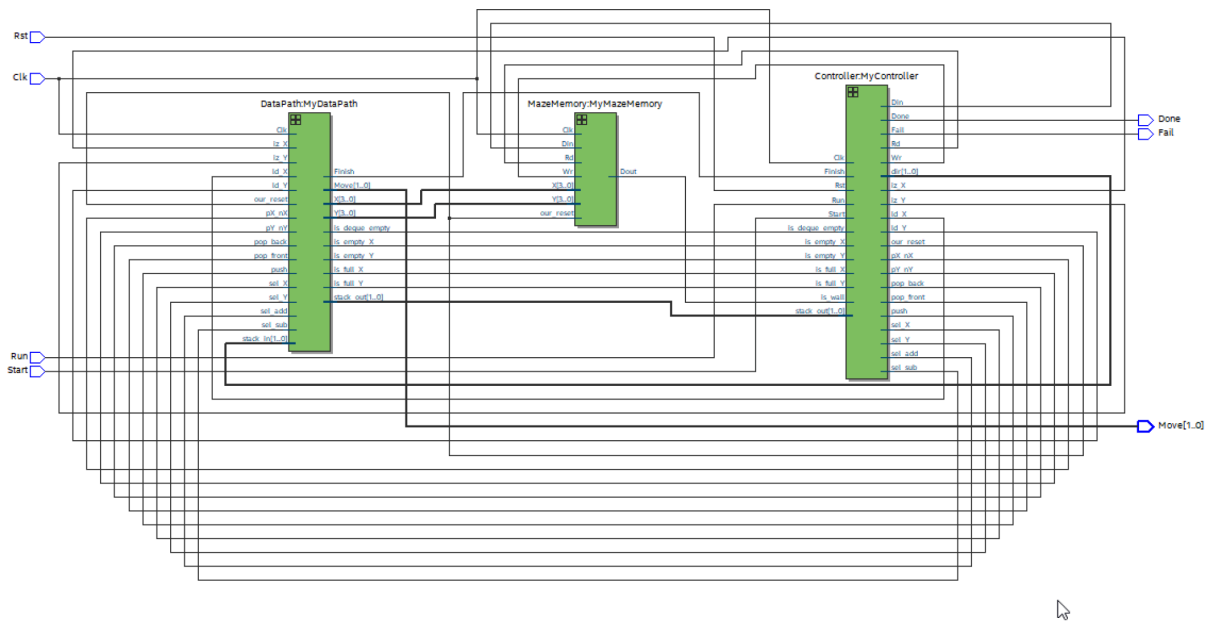


Quartus Report

- State machine viewer



- RTL viewer



- Summary

Project Navigator

Files

all.sv

Tasks

Compilation

Task

Compile Design

Analysis & Synthesis

Fitter (Place & Route)

Assembler (Generate programmin

Timing Analysis

EDA Netlist Writer

Edit Settings

Program Device (Open Programmer)

all.sv

Compilation Report - IntelligentRat

Resource Optimization Advisor

Pin Advisor

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Flow Status

Successful - Sat Apr 05 02:21:04 2025

Quartus Prime Version

20.1.0 Build 711 06/05/2020 SJ Lite Edition

Revision Name

IntelligentRat

Top-level Entity Name

IntelligentRat

Family

Cyclone V

Device

5CGXFC7C7F23C8

Timing Models

Final

Logic utilization (in ALMs)

860 / 56,480 (2 %)

Total registers

901

Total pins

8 / 268 (3 %)

Total virtual pins

0

Total block memory bits

0 / 7,024,640 (0 %)

Total DSP Blocks

0 / 156 (0 %)

Total HSSI RX PCSs

0 / 6 (0 %)

Total HSSI PMA RX Deserializers

0 / 6 (0 %)

Total HSSI TX PCSs

0 / 6 (0 %)

Total HSSI PMA TX Serializers

0 / 6 (0 %)

Total PLLs

0 / 13 (0 %)

Total DLLs

0 / 4 (0 %)