

Tablo I: Bilgisayar komutlarının tamamının çalışmasını tanımlayan tüm mikroişlemler

Fetch	R'T ₀ :	AR ← PC
	R'T ₁ :	IR ← M[AR], PC ← PC + 1
Decode	R'T ₂ :	D0, ..., D7 ← Decode IR(12 ~ 14), AR ← IR(0 ~ 11), I ← IR(15)
Indirect Interrupt	D ₇ 'IT ₃ :	AR ← M[AR]
	T ₀ 'T ₁ 'T ₂ '(IEN)(FGI + FGO):	R ← 1
	RT ₀ :	AR ← 0, TR ← PC
	RT ₁ :	M[AR] ← TR, PC ← 0
	RT ₂ :	PC ← PC + 1, IEN ← 0, R ← 0, SC ← 0
Memory-Reference		
AND	D ₀ T ₄ :	DR ← M[AR]
	D ₀ T ₅ :	AC ← AC ∧ DR, SC ← 0
ADD	D ₁ T ₄ :	DR ← M[AR]
	D ₁ T ₅ :	AC ← AC + DR, E ← C _{out} , SC ← 0
LDA	D ₂ T ₄ :	DR ← M[AR]
	D ₂ T ₅ :	AC ← DR, SC ← 0
STA	D ₃ T ₄ :	M[AR] ← AC, SC ← 0
BUN	D ₄ T ₄ :	PC ← AR, SC ← 0
BSA	D ₅ T ₄ :	M[AR] ← PC, AR ← AR + 1
	D ₅ T ₅ :	PC ← AR, SC ← 0
ISZ	D ₆ T ₄ :	DR ← M[AR]
	D ₆ T ₅ :	DR ← DR + 1
	D ₆ T ₆ :	M[AR] ← DR, if(DR=0) then (PC ← PC + 1), SC ← 0
Register-Reference		
	D ₇ I'T ₃ = r	(Common to all register-reference instr)
	IR(i) = B _i	(i = 0,1,2, ..., 11)
	r:	SC ← 0
CLA	rB ₁₁ :	AC ← 0
CLE	rB ₁₀ :	E ← 0
CMA	rB ₉ :	AC ← AC'
CME	rB ₈ :	E ← E'
CIR	rB ₇ :	AC ← shr AC, AC(15) ← E, E ← AC(0)
CIL	rB ₆ :	AC ← shl AC, AC(0) ← E, E ← AC(15)
INC	rB ₅ :	AC ← AC + 1
SPA	rB ₄ :	If(AC(15)=0) then (PC ← PC + 1)
SNA	rB ₃ :	If(AC(15)=1) then (PC ← PC + 1)
SZA	rB ₂ :	If(AC = 0) then (PC ← PC + 1)
SZE	rB ₁ :	If(E=0) then (PC ← PC + 1)
HLT	rB ₀ :	S ← 0
Input-Output		
	D ₇ I'T ₃ = p	(Common to all input-output instructions)
	IR(i) = B _i	(i = 6,7,8,9,10,11)
	p:	SC ← 0
INP	pB ₁₁ :	AC(0-7) ← INPR, FGI ← 0
OUT	pB ₁₀ :	OUTR ← AC(0-7), FGO ← 0
SKI	pB ₉ :	If(FGI=1) then (PC ← PC + 1)
SKO	pB ₈ :	If(FGO=1) then (PC ← PC + 1)
ION	pB ₇ :	IEN ← 1
IOF	pB ₆ :	IEN ← 0