Subject :

Date : 09/10/2022

YASEMIN AKW
22101782
CS223 - SECTION 6

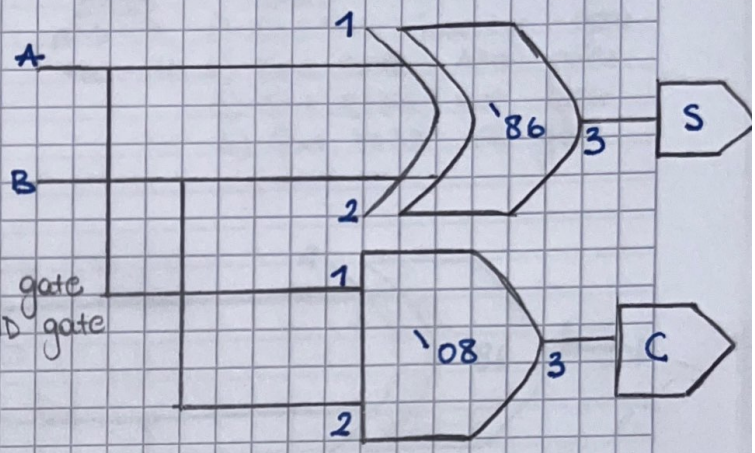
PRELIMINARY WORK - LAB 1

HALF ADDER CS

7486	7408
GND - 7	GND - 7
Vcc (+5V) - 14	Vcc (+5V) - 14

ICs

1) One 7486 quad 2-input XOR gate
2) One 7408 quad 2-input AND gate

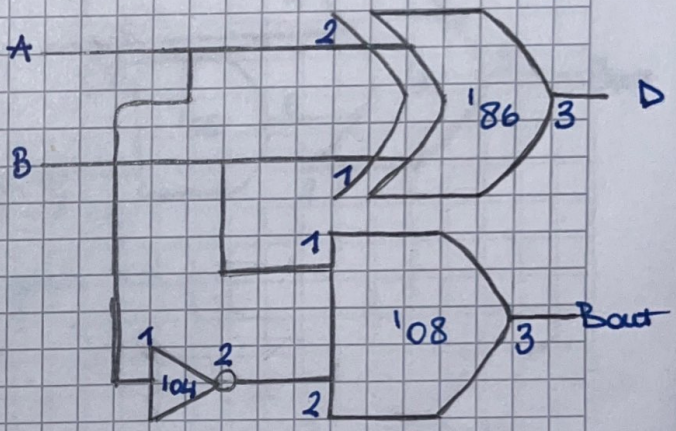


HALF SUBTRACTOR CS

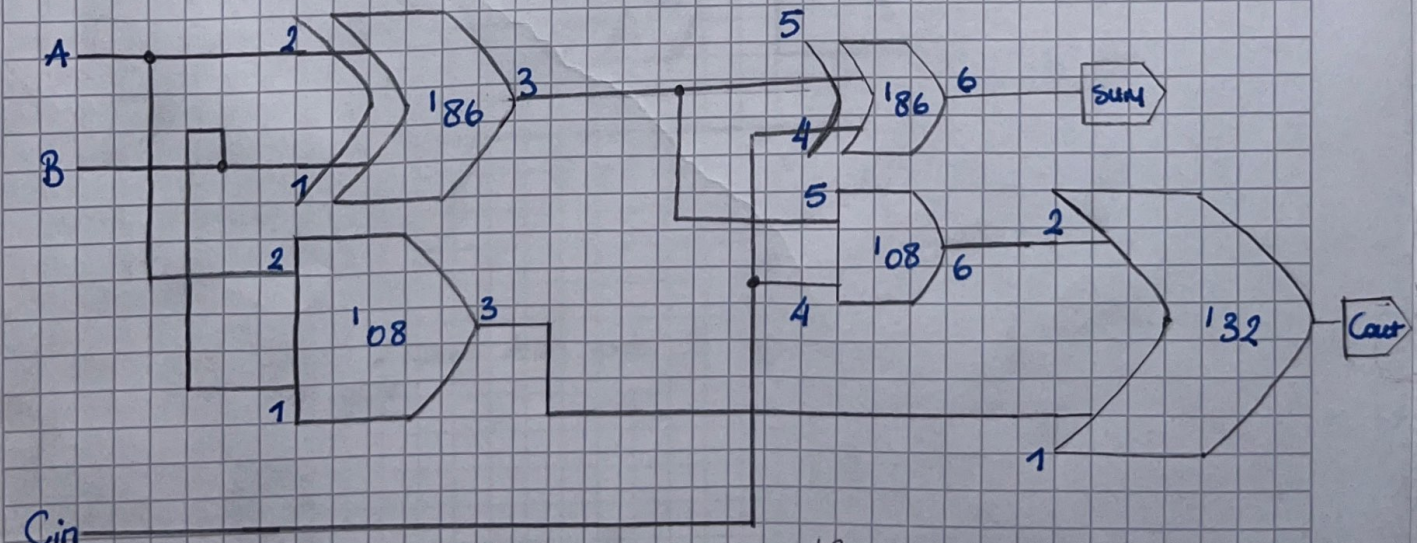
7486	7408	7404
GND (0V) - 7	GND - 7	GND - 7
Vcc (+5V) - 14	+5V - 14	Vcc - 14

ICs

1) One 7486 quad 2-input XOR gate
2) One 7408 quad 2-input AND gate
3) One 7404 invertor gate



FULL ADDER CS



7408	7486	7432
GND - 7	GND - 7	GND - 7
Vcc - 14	Vcc - 14	Vcc - 14

ICs

1) One 7486 XOR gate
2) One 7408 AND gate
3) One 7432 OR gate

Subject :

Date :...../...../.....

Yasemin Akın
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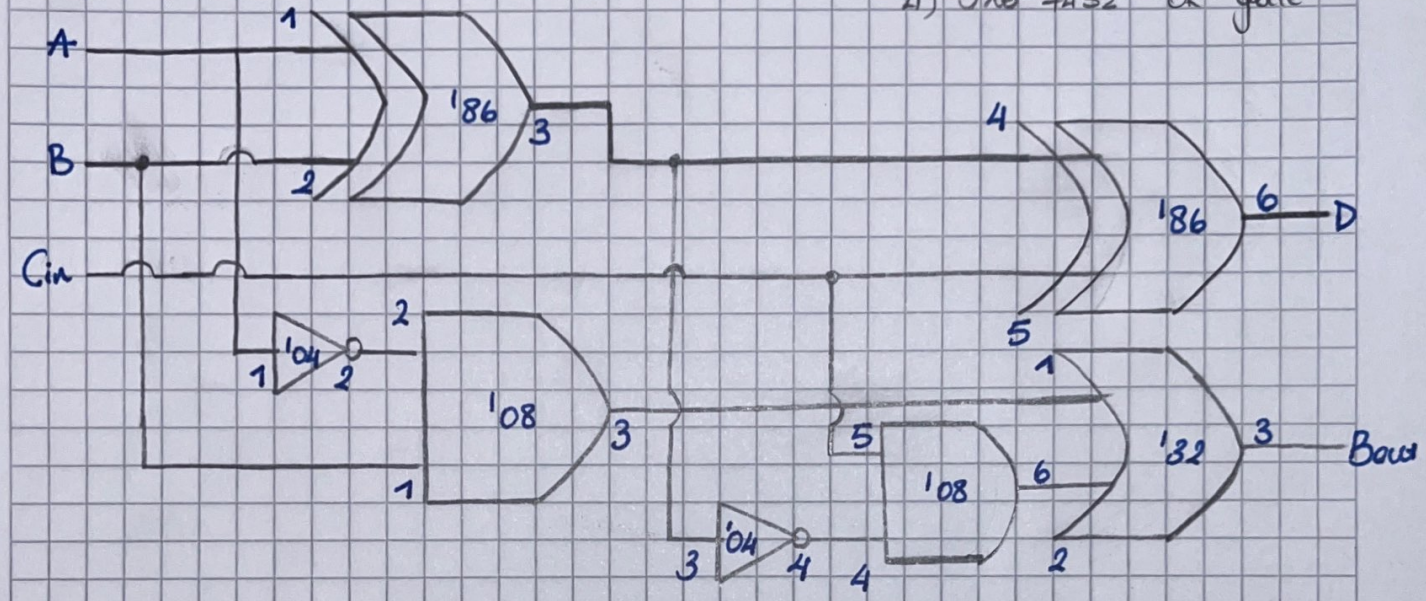
FULL SUBSTRACTOR CS

7404	7408	7486	7432
GND-7	GND-7	GND-7	GND-7
Vcc(+5V)-14	+5V-14	+5V-14	Vcc-14

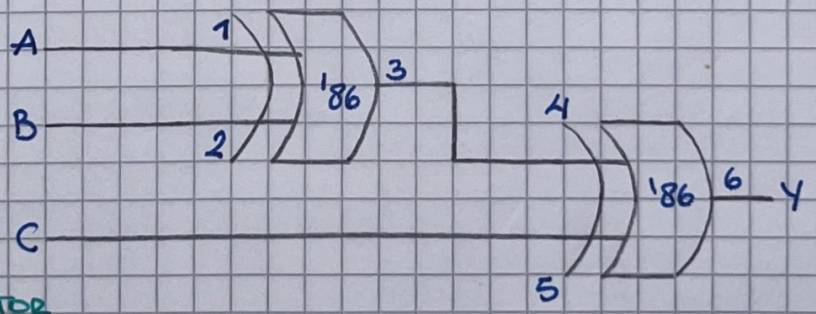
ICs
1) One 7404 inverter gate
2) One 7408 AND gate
3) One 7486 XOR gate
4) One 7432 OR gate



1- 3 input XOR gates using 2 input XOR gates CS

7486
GND-7
+5V-14 (Vcc)

IC → One 7486 XOR gate



LAB CALCULATOR

ICs used

1) One 7408 quad 2 input AND gate
2) One 7486 XOR gate

7408	7486
GND-7	GND-7
Vcc-14	Vcc-14