



NAME-Yash Agarwal



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Education

- **Indraprastha Institute of Information Technology Delhi (IIITD)**
- B.Tech in Electronics and VLSI Engineering (2022 – Present)
- Current CGPA: [Insert your current CGPA here, e.g., 8.5/10]
- **Modern Convent School, Dwarka, CBSE**
- **Class XII (2021):**
- Subjects: English, Mathematics, Physics, Chemistry, Physical Education
- Total Percentage: 93.2%
- CGPA: 9.81
- **Class X (2019):**
- Subjects: English, Hindi, Mathematics, Science, Social Science
- Total Percentage: 86.6%
- CGPA: 9.12

Skills

Expertise Areas:

- **FPGA Architecture:** Knows the designs and simulation techniques for systems in FPGA-based environment.
- **Front-End Web Development:** Specializing in creating responsive and user-friendly interfaces.
- **Programming Languages:**
- Proficient: HTML, CSS, JavaScript, Verilog.
- Learning: Python, SQL.
- **Tools & Technologies:**
- Hardware Design: Vivado, LTSpice, Cadence Virtuoso.
- Web Development: Bootstrap, Laravel, ServiceNow.
- Database Management: MySQL (Learning).
- Technical Electives:
- Digital VLSI Design, VLSI Design Flow, Embedded Systems.

Projects

- **JustAirports**(<https://www.justairports.com/>)
- Developed an airport cab service provider platform using Laravel for the backend and HTML, CSS, and JavaScript for the front-end. Enhanced user experience with a seamless booking interface and integrated payment gateway.
(Mar, 2023 – Jun, 2023) Team Size – 3
- **CoolCareServices**(<https://www.coolcareservice.in/>)
- Built an online platform for product rentals using Laravel, HTML, CSS, and JavaScript. Streamlined the rental process with a user-friendly interface and comprehensive functionality.
(Jul, 2023 – Sep, 2023) Team Size – 4
- **Bhartiye Parampara**(<https://bhartiyparampara.com/>)
- Created a dynamic blogging platform to showcase and promote traditional Indian cultural practices using Laravel, HTML, CSS, and JavaScript. Delivered a robust backend and visually compelling front-end experience.
(Oct, 2023 – Dec, 2023) Team Size – 2

Positions of Responsibility

☒	<Position1>	(Mon,YY – Mon,YY)
☒	<Position2>	(Mon,YY – Mon,YY)
☒	<Position3>	(Mon,YY – Mon,YY)

Awards and Achievements

- **Secured admission to the prestigious** IIT Delhi for B.Tech in Electronics and VLSI Engineering.
- **Web Development:** Successfully developed and deployed interactive platforms catering to diverse user needs.
- **Hands-on Experience with EDA Tools:** Gained extensive practical experience using leading Electronic Design Automation (EDA) tools such as Cadence Virtuoso, LTSpice, and Vivado for circuit design, simulation, and analysis. Successfully applied these tools in various projects, demonstrating proficiency in VLSI design and FPGA-based system development.

Interests and Hobbies

- **National Security Policies:** Was interested and engaged in following policy impacts globally and regionally.
- **Gymming and Fitness:** Passionate about maintaining physical and mental well-being through regular workouts and fitness challenges.

Declaration: The above information is correct to the best of my knowledge.

Yash Agarwal

Date: January 16, 2025