# END TERM EXAMINATION

SIXTH SEMESTER [B.TECH] MAY - JUNE 2019

Time: 3 Hours Paper Code: IT-308

> Subject: Compiler Design Maximum Marks: 75

Note: Attempt five questions in all including Q no.1 which is compulsory.

Select one question from each unit.

QI Explain following in brief any five:-

(5×5=25)

- (a) Differentiate between LR(0) and LR(1) algorithm.
  (b) Discuss the various issues involved in designing lexical analyzer.
  (c) What are the advantages of intermediate code? Describe va What are the advantages of intermediate code? Describe various representation of Intermediate code.
- (d) Discuss the importance of symbol table in compiler. How is it manipulated in the different phases of compilation?
- (e) Compare syntax tree and parse tree.(f) What is a loader? What does the loader?
- What is a loader? What does the loading process do? What does the link editing does?

### UNIT-I

- Q2 Describe the general phases of a compiler. Trace the program segment A=C\*D-F+100 for all phases. (12.5)
- QЗ What are the compiler construction tools? Write note one each compiler construction tool. (12.5)

## UNIT-II

- Q4 (a) What is an ambiguous and unambiguous grammar? State whether the following grammar is ambiguous or not? Justify? Why unambiguous grammars are preferred?  $E \rightarrow E + E \mid E * E \mid (E) \mid id.$ (6.25)
- (b) Prepare the following grammar is LL(1) but not SLR(1) S → AaAb BbBa

(6.25)

 $A \rightarrow \epsilon$ 

 $B \rightarrow \epsilon$ 

- Q5 (a) Define YACC parser generator. List out the error recovery actions in YACC. (6.25) (6.25)
- (b) What is the main difference between YACC and Bison?

# UNIT-III

99 (a) Define inherited attributes and synthesised attributes. Give examples.

Define Syntax directed definition for the simple type declaration.(6.25)

(b) Differentiate between syntax and semantic errors.

Q7

What is an activation record? Consider the program fragment: Sum=0; (12.5)

<u>1</u>

for(i=1;i<=30;i++)

sum=sum+a[i]-b[i];

and generate the three-address code word for it. There are four bytes per

### UNIT-IV

- 8 (a) Give the code generation process for t=a-b, u=a-c, v=t+u Generate instruction for the statement, en arithmetic operation. (6.25)
- (b) How code optimization is ensured during compilation? Discuss. (6.25)

(4+4+4.5=12.5)

Answer following in brief:-

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- (a) How can we generate code from a DAG(b) What are the issues in design of a code generator?(c) Write short notes: Assembler and Interpreter.