END TERM EXAMINATION

SIXTH SEMESTER [B.TECH./M.TECH.] MAY-JUNE-2015
Subject: Microprocessors

por	Code: IT3	02 Subject	Taximum Marks :60
ipe.	3 Hours		uximum marks .oc
nte: A	ttempt any	five questions including Q.no.1 which is comp is indicated.	uisory. Mærnat enotes
	a) What is to microproof b) Explain to LEA, LES If c) What is to the control of t	he difference between general purpose registers se cessor? he instructions:	egment registers in 8086 (2x10=20) tegorized.
	(i) Interiace (j) Explain	the following directives for Intel 8086 microprocessor	or ENM, EQU and PTR.
2	(a) Compar (b) Explain	e the 8085 and 8086 microprocessor. the 80286, Pentium Processors and microcontroller	s. (5)
3	activitie	ne architecture diagram of 8086. Explain the signals of the microprocessor. BIU and EU in 8086 microprocessor. OR	ls used to interrupt the (5) (5)
4	microp	re and explain Maximum Mode and Minimum Mocessor. re 8086 processor with 8088.	Node operation of 8086 (5) (5)
) 5	for 808 (b) (i) Wh (ii) Sho BL	the different addressing mode in their proper forma 66 microprocessor. (at is the difference between DAA and AAD instruction (b) we the content of flag registers and AL in the following (a) and following instructions executed (b) DAL (c) DAA	(5) ns? (2.5)
		OR	
)6	(i) Arr (ii) Fin	n assemble language program to- ange a given series of hexadecimal bytes in ascending d square root of two digit number. Assume that t are.	
	(b) Explain	n program development algorithm for Assembly language program development tools.	nguage program and its (5)
Q7	(b) Explair (i) Cor pro (ii) Dra (iii) Wh	n different types of 8086 interrupt. In the following:- Introl word and its different modes with example to grammable peripheral device. It was analog circuit connections for ADC 1208 12-bit Day 8087 is known as coprocessor?	/A convertor.
	(iv) Wh trai	at is the difference Single Handshake I/O and nsfer for parallel data transfer?	Double-Handshake data
Q8	five 7 1,2,3,4	ce 16 bit 8255 ports with 8086 at 80H as an I/O ac segment displays with 8255. Write a sequence o and 5 over five displays continuously as per their p B position.	f instructions to display
	(b) Explair	the Architecture and signal descriptions for 825 at operating modes of programmable timer device (82)	3 chip. Also, explain the

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