

Vivekananda Institute of Professional Studies- Technical Campus, VSE&T

TIME TABLE (Section: AIDS-B/ 3rd Semester)

Days	9:00-9:50	9:50-10:40	10:40-11:30	11:30-12:20	12:20- 1:10	1:10-2:00	2:00-2:50	2:50-3:40	3:40-4:30	4:30-5:20
	1	2	3	4	5	6	7	8	9	10
Monday	DLD Lab (G2), 104		UHV-II	PSLA	BREAK	DLD	CRST	LIB	FOD Lab (G2), 408	
Room No.	DS Lab (G1), 202		207	206		206	207			
Tuesday	Mentorship/Project/Societies		DLD Lab (G1), 104			FOD	CRST	SR		
Room No.			DS Lab (G2), 202			206	207	207		
Wednesday		DS	PAI	PSLA		DLD	FOD	Mentor-Mentee Meeting		
Room No.		206	206	208		207	206			
Thursday	FOD	DS	WP Lab (G1+G2), 203			PSLA	UHV-II	PAI	PAI Lab (G1+G2), 203	
Room No.	206	206				206	207	206		
Friday	FOD Lab (G1), 411		DLD	DS	UHV-II	PSLA	BREAK	PAI		
Room No.			206	206	207	206		206		
Subject Code	Subject			Faculty		Lab	Subject		Faculty	
DS	Data Structure (3)			Ms. Vidushi		DS Lab	Data Structure Lab (4)		Ms. Vidushi	
PAI	Principles of Artificial Intelligence (3)			Dr. Deepti Chopra		PAI Lab	Principles of Artificial		Dr. Deepti Chopra	
FOD	Foundation of Data Science (3)			Dr. Alpna Mishra		FOD Lab	Foundation of Data		Dr. Alpna Mishra	
DLD	Digital Logic Design (3)			Dr. Khushboo		DLD Lab	Digital Logic Design Lab		Dr. Bhavya	
PSLA	Probability, statistics and Linear			Dr. Tanisha Chauhan		WP Lab	Web Programming Lab (2)		Dr. Shivanka	
UHV	Universal Human Values-II (3)			Dr. Nikita Sharma						
CRST	Critical Reasoning and System			Ms. Swati Luthra						
SR	Selected Readings (1)			Dr. Dharmender						

Vivekananda Institute of Professional Studies- Technical Campus, VSE&T
Outer Ring Rd, AU Block, Ranikhet, Pitampura, New Delhi - 110034
3rd Semester/ Department of Artificial Intelligence & Machine Learning

TIME TABLE (Section: AIML -A/ 3rd Semester)

Days	9:00-9:50	9:50-10:40	10:40-11:30	11:30-12:20	12:20- 1:10	1:10-2:00	2:00-2:50	2:50-3:40	3:40-4:30	4:30-5:20
	1	2	3	4	5	6	7	8	9	10
Monday	LIB	DLD	WP Lab (G1+G2), 203		PAI	BREAK	FOD	LIB	Mentor-Mentee Meeting	
Room No.		210			210		210			
Tuesday	DLD Lab (G2), 104		PSLA	DS	FOD		UHV-II	PAI		
Room No.	DS Lab (G1), 202		210	210	210		210	210		
Wednesday	PAI Lab (G1), 408		PSLA	FOD	BREAK	UHV-II	Mentorship/Project/Societies			
Room No.	PAI Lab (G2), 411		210	210		210				
Thursday	DS	DLD	PSLA	BREAK	UHV-II	CRST	FOD Lab (G1), 408		SR	
Room No.	210	210	207		207	210	FOD Lab (G2), 411		210	
Friday	LIB	CRST	PAI	DLD	DLD Lab (G1), 104		BREAK	PSLA	DS	
Room No.		206	210	210	DS Lab (G2), 202			210	210	
Subject Code	Subject			Faculty		Lab	Subject		Faculty	
DS	Data Structure (3)			Ms. Nishi Jain		DS Lab	Data Structure Lab (4)		Ms. Nishi Jain	
PAI	Principles of Artificial Intelligence (3)			Dr. Dimple Tiwari		PAI Lab	Principles of Artificial		Dr. Adeel Hashmi	
FOD	Foundation of Data Science (3)			Dr. Adeel Hashmi		FOD Lab	Foundation of Data		Dr. Adeel Hashmi	
DLD	Digital Logic Design (3)			Dr. Safina Shokeen		DLD Lab	Digital Logic Design Lab		Dr. Safina	
PSLA	Probability, Statistics and Linear			Dr. Deepika Kumari		WP Lab	Web Programming Lab (2		Ms. Anshula Gupta	
UHV	Universal Human Values-II (3)			Dr. Mudita Nagpal						
CRST	Critical Reasoning and System			Dr. Soumya Sharma						
SR	Selected Readings (1)			Dr. Nikita Sharma						

Vivekananda Institute of Professional Studies- Technical Campus, VSE&T
Outer Ring Rd, AU Block, Ranikhet, Pitampura, New Delhi - 110034
3rd Semester/ Department of Artificial Intelligence & Machine Learning

TIME TABLE (Section: AIML-B/ 3rd Semester)

Days	9:00-9:50	9:50-10:40	10:40-11:30	11:30-12:20	12:20- 1:10	1:10-2:00	2:00-2:50	2:50-3:40	3:40-4:30	4:30-5:20	
	1	2	3	4	5	6	7	8	9	10	
Monday	LIB	PSLA	FOD Lab (G1), 408		DLD	FOD	BREAK	SR	PAI		
Room No.		207	FOD Lab (G2), 411		207	210		210	214		
Tuesday		PSLA	UHV-II	DLD	WP Lab (G1+G2), 203			PAI	Mentor-Mentee Meeting		
Room No.		210	207	207				211			
Wednesday		UHV-II	DLD Lab (G2), 104		BREAK	PSLA	DS	FOD	PAI		
Room No.		210	DS Lab (G1), 202			214	210	210	211		
Thursday		PAI Lab (G1), 408		DS		FOD	PSLA	DLD	CRST		
Room No.		PAI Lab (G2), 411		210		210	212	210	210		
Friday		DS	UHV-II	DLD Lab (G1), 104		CRST	Mentorship/Project/Societies				
Room No.		210	210	DS Lab (G2), 202		210					
Subject Code	Subject			Faculty		Lab	Subject		Faculty		
DS	Data Structure (3)			Ms. Nishi Jain		DS Lab	Data Structure Lab (4)		Ms. Nishi Jain		
PAI	Principles of Artificial Intelligence (3)			Dr. Deepti Chopra		PAI Lab	Principles of Artificial		Dr. Adeel Hashmi		
FOD	Foundation of Data Science (3)			Dr. Adeel Hashmi		FOD Lab	Foundation of Data		Dr. Adeel Hashmi		
DLD	Digital Logic Design (3)			Dr. Safina Shokeen		DLD Lab	Digital Logic Design Lab		Dr. Gopal (G1)/ Dr. Safina		
PSLA	Probability, statistics and Linear			Dr. Deepika Kumari		WP Lab	Web Programming Lab (2		Ms. Anshula Gupta		
UHV	Universal Human Values-II (3)			Dr. Shubhangi							
CRST	Critical Reasoning and System			Mr. Kumar Ankur							
SR	Selected Readings (1)			Dr. Nikita Sharma							

**Vivekananda Institute of Professional Studies- Technical Campus, VSE&T
Outer Ring Rd, AU Block, Ranikhet, Pitampura, New Delhi - 110034
3rd Semester/ Department of Industrial Internet of Things**

TIME TABLE (Section: HoT/ 3rd Semester)

Days	9:00-9:50	9:50-10:40	10:40-11:30	11:30-12:20	12:20- 1:10	1:10-2:00	2:00-2:50	2:50-3:40	3:40-4:30	4:30-5:20
	1	2	3	4	5	6	7	8	9	10
Monday	LIB	DS	DLD	PSLA	DLD Lab (G2), 104		BREAK	FOD	SR	
Room No.		211	210	210	DS Lab (G1), 202			211	211	
Tuesday	PAI	CRST	PAI Lab (G2), 408		BREAK	DLD	DS	Mentor-Mentee Meeting		
Room No.	211	211	FOD Lab (G1), 411			211	211			
Wednesday	DS Lab (G2), 202		FOD	BREAK	PSLA	UHV-II				
Room No.	DLD Lab (G1), 104		211		211	211				
Thursday	PAI	CRST	UHV-II		WP Lab (G1+G2), 203		PSLA	DS	Mentorship/Project/Societies	
Room No.	208	211	211				211	211		
Friday	LIB	UHV-II	PSLA		PAI Lab (G1), 408		DLD	PAI	FOD	
Room No.		211	211		FOD Lab (G2), 411		211	211	211	
Subject Code	Subject			Faculty		Lab	Subject		Faculty	
DS	Data Structure (3)			Ms. Nishi Jain		DS Lab	Data Structure Lab (4)		Ms. Nishi (G1)/ Dr. Nivedita (G2)	
PAI	Principles of Artificial Intelligence (3)			Dr. Dimple Tiwari		PAI Lab	Principles of Artificial		Dr. Deepti Chopra	
FOD	Foundation of Data Science (3)			Dr. Alpna Mishra		FOD Lab	Foundation of Data		Dr. Adeel Hashmi	
DLD	Digital Logic Design (3)			Dr. Anand Kumar		DLD Lab	Digital Logic Design Lab		Dr. Anand Kumar	
PSLA	Probability, statistics and Linear			Dr. Deepika Kumari		WP Lab	Web Programming Lab (2		Dr. Shivanka	
UHV	Universal Human Values-II (3)			Dr. Komal Aggarwal						
CRST	Critical Reasoning and System			Dr. Chaitali Bhowmik						
SR	Selected Readings (1)			Dr. Sonal Khurana						

Vivekananda Institute of Professional Studies- Technical Campus, VSE&T
Outer Ring Rd, AU Block, Ranikhet, Pitampura, New Delhi - 110034
3rd Semester/ Department of Computer Science & Engineering

TIME TABLE (Section: CSE- A/ 3rd Semester)

Days	9:00-9:50	9:50-10:40	10:40-11:30	11:30-12:20	12:20- 1:10	1:10-2:00	2:00-2:50	2:50-3:40	3:40-4:30	4:30-5:20
	1	2	3	4	5	6	7	8	9	10
Monday	DS	DM	DS Lab (G2), 202		OOPS	BREAK	CM	DLCD	BS-181 (CSE A&B, VLSI)	LIB
Room No.	208	208	DLCD Lab (G1), 104		208		208	208	208	
Tuesday	OOPS Lab (G1), 408		DM	OOPS	DLCD		CM	IKS	DS	BS-183 (CSE A&B, VLSI)
Room No.	CM Lab (G2), 411		208	208	208		208	208	210	207
Wednesday	Mentorship/Project/Societies			DLCD	CM		OOPS Lab (G2), 408		BS-183 (CSE A&B, VLSI)	BS-183 (CSE A&B, VLSI)
Room No.				215	208		CM Lab (G1), 411		207	207
Thursday	DS Lab (G1), 202		DM	IKS	DS		OOPS	Mentor-Mentee Meeting		
Room No.	DLCD Lab (G2), 104		208	208	208		208			
Friday	BS-181 (CSE A&B, VLSI)	OOPS	DM	CM	BREAK	DLCD	DS	LIB	Bridge Course BS181 (CSE A&B)	
Room No.	206	208	208	208		208	208		208	206
Subject Code	Subject			Faculty		Lab	Subject		Faculty	
OOPS	Object Oriented Programming using			Dr. Deepika Bhatia		OOPS Lab	Object Oriented		Dr. Deepika Bhatia	

CM	Computational Methods (4)	Dr. Bharti Seth	CM Lab	Computational Methods	Dr. Bharti Seth
DS	Data Structures (4)	Dr. Nivedita Palia	DS Lab	Data Structures Lab (4)	Dr. Nivedita Palia
DLCD	Digital Logic and Computer Design (4)	Dr. Gopal	DLCD Lab	Digital Logic and	Dr. Gopal
IKS	Indian Knowledge System (2)	Dr. Vanita Bhardwaj			
DM	Discrete Mathematics (4)	Dr. Tanisha Chauhan			
BS-181	Bridge Course in Maths	Dr. Deepika Kumari			
BS-183	Bridge Course in Programming in C	Dr. Surabhi Rastogi			

Vivekananda Institute of Professional Studies- Technical Campus, VSE&T											
TIME TABLE (Section: CSE- B/ 3rd Semester)											
Days	9:00-9:50	9:50-10:40	10:40-11:30	11:30-12:20	12:20- 1:10	1:10-2:00	2:00-2:50	2:50-3:40	3:40-4:30	4:30-5:20	
	1	2	3	4	5	6	7	8	9	10	
Monday	CM Lab (G2), 411		IKS	CM	BREAK	DM	DS	LIB	BS-181 (CSE A&B, VLSI)		
Room No.			208	208		208	211		208		
Tuesday	LIB	DLCD	DS	BREAK	CM	DM	DS Lab (G2), 202		OOPS	BS-183 (CSE A&B, VLSI)	
Room No.		208	211		207	208	DLCD Lab (G1), 104		208	207	
Wednesday	DLCD	OOPS	DS		DS Lab (G1), 202		DM	IKS	BS-183 (CSE A&B, VLSI)	BS-183 (CSE A&B, VLSI)	
Room No.	208	211	207		DLCD Lab (G2), 104		208	208	207	207	
Thursday	Mentor-Mentee Meeting		OOPS Lab (G1), 415		CM	OOPS	BREAK	DM	DLCD	LIB	
Room No.					206	208		208	208		
Friday	BS-181 (CSE A&B, VLSI)	DLCD	DS	OOPS	CM	BREAK	OOPS Lab (G2), 408		BS-181 (CSE A&B, VLSI)		
Room No.	206	207	212	207	208		CM Lab (G1), 411		206		

Subject Code	Subject	Faculty	Lab	Subject	Faculty
OOPS	Object Oriented Programming using	Dr. Deepika Bhatia	OOPS Lab	Object Oriented	Dr. Deepika Bhatia
CM	Computational Methods (4)	Dr. Bharti Seth	CM Lab	Computational Methods	Dr. Bharti Seth
DS	Data Structures (4)	Dr. Nivedita Palia	DS Lab	Data Structures Lab (4)	Dr. Nivedita Palia
DLCD	Digital Logic and Computer Design (4)	Dr. Gopal	DLCD Lab	Digital Logic and	Dr. Gopal
IKS	Indian Knowledge System (2)	Dr. Swati Sharma			
DM	Discrete Mathematics (4)	Dr. Tanisha Chauhan			
BS-181	Bridge Course in Maths	Dr. Deepika Kumari			
BS-183	Bridge Course in Programming in C	Dr. Surabhi Rastogi			

Vivekananda Institute of Professional Studies- Technical Campus, VSE&T										
TIME TABLE (Section: VLSI/ 3rd Semester)										
Days	9:00-9:50	9:50-10:40	10:40-11:30	11:30-12:20	12:20- 1:10	1:10-2:00	2:00-2:50	2:50-3:40	3:40-4:30	4:30-5:20
	1	2	3	4	5	6	7	8	9	10
Monday	LIB	CM	S&S	A.Comm.	BREAK	AE-I	CM Lab (G1), 411		BS-181 (CSE A&B, VLSI)	
Room No.		215	211	211		211	A.Comm. Lab (G2), 105		208	
Tuesday	Mentor-Mentee Meeting			A.Comm.	CM Lab (G2), 411		S&S	BREAK	DLCD	BS-183 (CSE A&B, VLSI)
Room No.				206	A.Comm. Lab (G1), 105		206		207	207
Wednesday	IKS	CM	AE-I	S&S	BREAK	DLCD	DLCD Lab (G2), 104		BS-183 (CSE A&B, VLSI)	BS-183 (CSE A&B, VLSI)
Room No.	207	208	212	211		208	AE-I Lab (G1), 105		207	207
Thursday		CM	S&S Lab (G2), 202			A.Comm.	AE-I	DLCD	IKS	
Room No.		208	DLCD Lab (G1), 104			211	212	207	206	

Friday	BS-181 (CSE A&B, VLSI)	DLCD	BREAK	A.Comm.	CM	AE-I	S&S Lab (G1), 202	BS-181 (CSE A&B, VLSI)	Mentorship/Project/Societies
Room No.	206	212		211	215	211	AE-I Lab (G2), 105	206	
Subject Code	Subject			Faculty		Lab	Subject	Faculty	
CM	Computational Methods (4)			Dr. K.M Lipi		CM Lab	Computational Methods	Dr. K.M Lipi	
AComm	Analog Communications (4)			Dr. Nisha Chugh		AComm Lab	Analog Communications Lab (4)	Dr. Nisha Chugh	
AE-I	Analog Electronics-I (4)			Dr. Nishtha Kansal		AE-I Lab	Analog Electronics-I Lab	Dr. Nishtha Kansal	
S&S	Signal & Systems (3)			Dr. Khushboo		S&S Lab	Signal & Systems Lab (4)	Dr. Khushboo	
DLCD	Digital Logic and Computer Design (4)			Dr. Anand Kumar		DLCD Lab	Digital Logic and	Dr. Anand Kumar	
IKS	Indian Knowledge System (2)			Dr. Soumya					
BS-181	Bridge Course in Maths			Dr. Deepika Kumari					
BS-183	Bridge Course in Programming in C			Dr. Surabhi Rastogi					