MPCA Lab
Week 9

Name : Yashaswini Ippili

SRN : PES1UG21CS732

Section : L

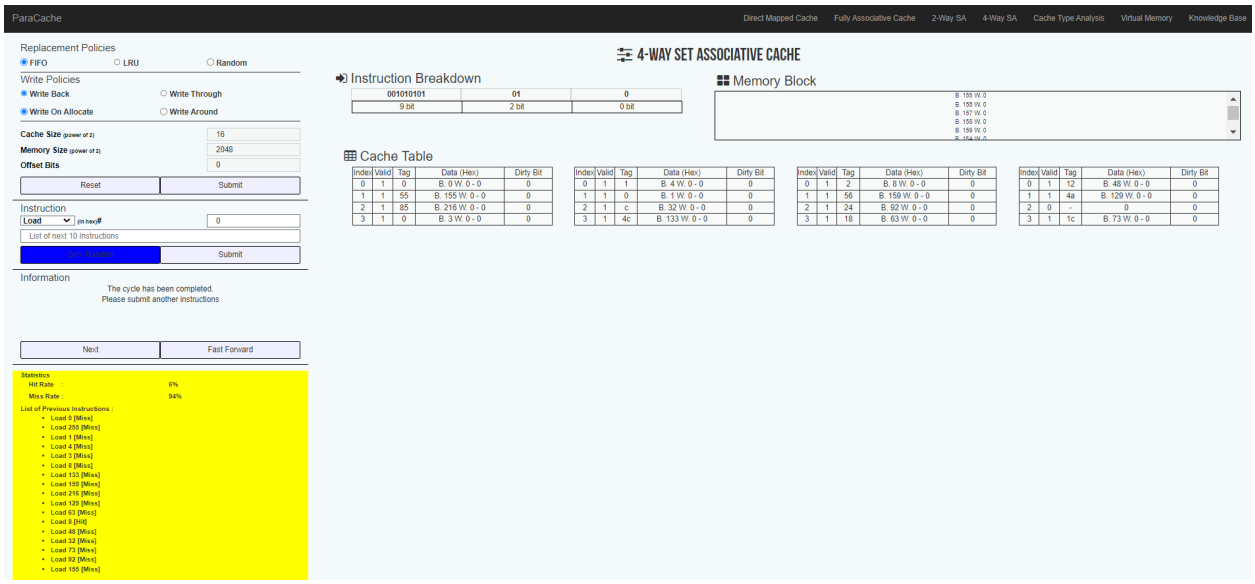
Sem : 4

Roll : 42

1. Consider a 4-way set associative mapping with 16 cache blocks and offset bit to 0. The memory block requests are in the order-0, 255, 1, 4, 3, 8, 133, 159, 216, 129, 63, 8, 48, 32, 73, 92, 155. If LRU and FIFO replacement policy is used, which cache block will not be present in the cache? Also, calculate the hit ratio and miss ratio.

Output :

Using FIFO :

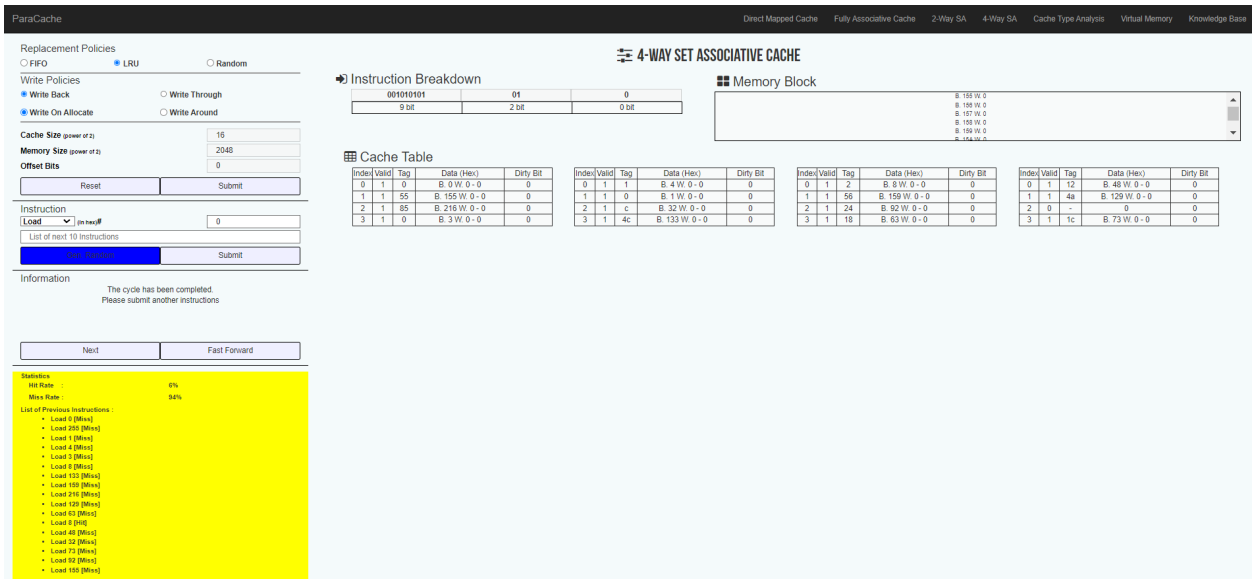


Missing Cache Block : 255

Hit Rate - 6%

Miss Rate - 94%

Using LRU :



Missing Cache Block : 255

Hit Rate - 6%

Miss Rate - 94%