Digital Logic Design

(EE1005)

Date: Pelmany 25th, 2025

Course Instructor(s)

Dr. Rabia Labassum

Dr. Adeel

Mr. Abuzar

Mr. Ishtlaq.

Mr. Javiad Queeshi,

Mr. Kashif.

Mr. Rahim.

Mr. Usmin.

Mr. Zulflgar All

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Sessional-I Exam

Total Time: 1 flour Total Marks: 30

Total Questions: 02

Semester: SP-2025

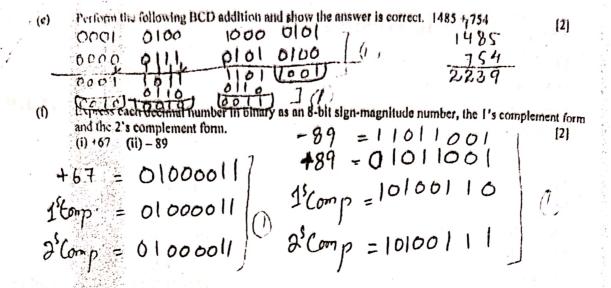
Campus: Karachi

Dept: School of Computing

Student Name Roll No Section Student Signature

		N 5,12 N 10.5	and a	Esthuated Time-20mins	
	CLOHI	Number systems and le	NK.	. 4. 4-1	[10]
	Ol: Answ	er the following:	1	14 bits	n ref in
			birary		[1]
	(a)	that is the largest himsey mumber that can be expressed that		14 000	- Dr
		\$ 4 16	, 3 -8 3 (a.		
		30 T	to the night of 2 and to these places.		[1]
	(b)	Calculate the binary equi-	valent of 2 out to three places.	1	1
		17: 6-66 x 2	. 1.33	(0.101) <u>.</u>
		017,23 x 1	= 0.66 6		
		6.666 X 3	- 1.33		į.
		discount the Rabit stoned a	addition of (using 2's Complement)	ave i i ili	
	(e)	(1) 1(291+(-49))	(11) [(-29)+ (+49)]	-29 111 +49 00 +20 1000	1 1121
		32 Aug 1	- 11111111111	-29	000()
1	Ocellia	129	000111011	4 44 DO	110001 6
١	0011000	Me - 119	11001111	7 7	19
•		19 15	111111111111111111111111111111111111111	+20 1000	10100
	11100011				. · · · · · · · · · · · · · · · · · · ·
	11001411	+20	00010100		1
			7.	/ . >	
		Served Authority	lowing hexadeolmal numbers: (B5)	(E5)16	[2]
	(d)	Find Rith code for the tor	loking name		. 3
		表 1程 ·			Dec. 1 -C2
		ES	(i)		Page 1 of 3
		13 17			

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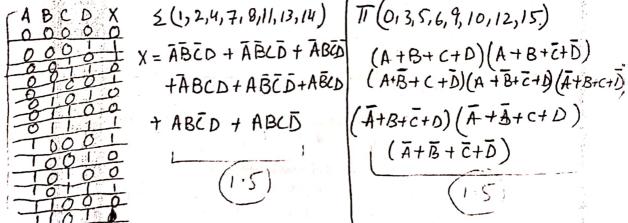


CLO # 2 Techniques to design logic circuits Q2: Answer the following:

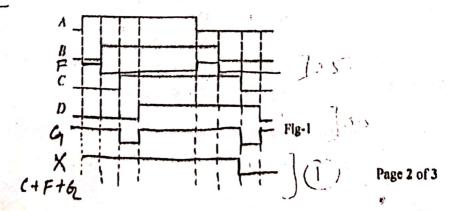
Estimated Time=40mins

[20]

(a) Design a combinational circuit with four inputs (A, B, C,D) and one output (F) such that: F=1 when the number of 1's in the input is odd. Develop the truth table(Input variables are A,B,C,D and output F) and write down the SOP and POS expressions for the logic circuit. [5]



(b) For the waveforms given in Fig-1 below, A and B are XORed to get an output F, then C and D are XNORed to get an output G. Finally C, F and G are ORed. Draw the net output waveform. [2]



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