

## CAHM. Notes

Overtion Difference between RAM & ROM ROM RAM It is a Temporary storage It is a fermanent storage

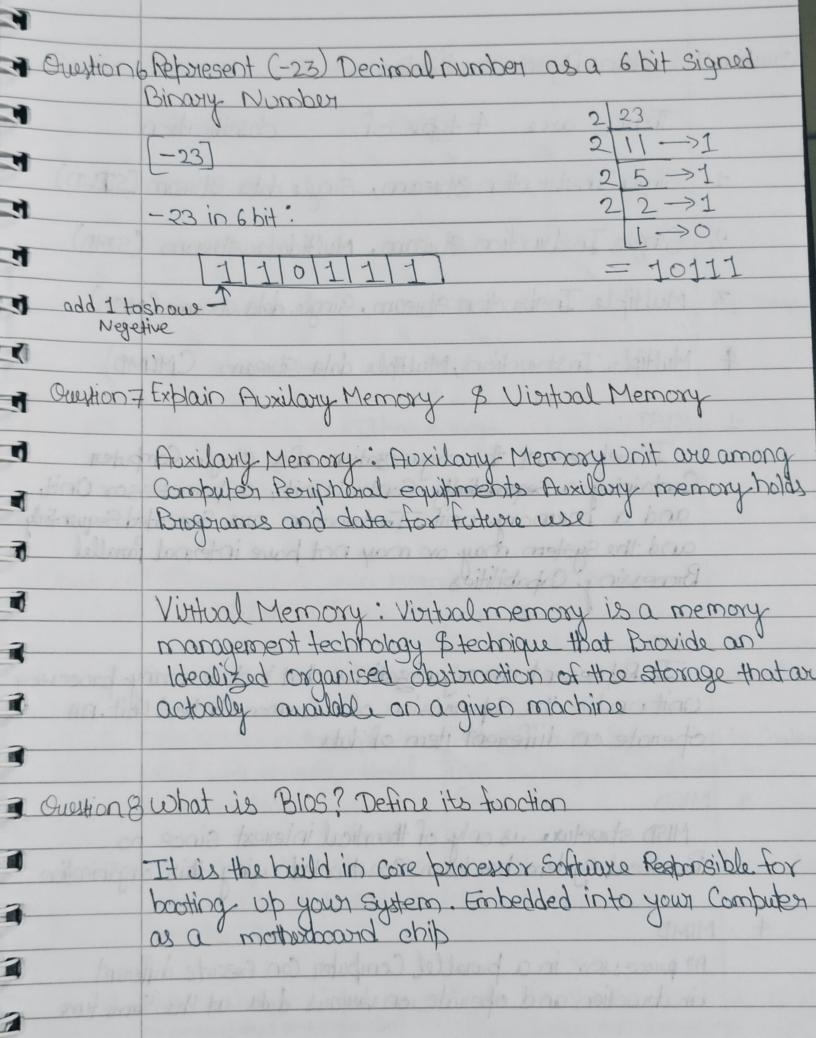
Store data in MBs It stores in GrB

Its Valatile It's a Non Volatile

y used in normal operation used for startup process of Comput

burithing data is fast whithing data is down T Questions Find 2's Complement of (101011)? 101011 - for 21s Complement IF Î 1's Complement I first, invert digits 10/011 -> 010/00 1 2's Complement [by adding 1] TO 10 Answer - 010101 are the 2's Complement of 10 11

Overtion 3 Define MMU The Run time Mapping between vivitual addressand Physical address is done by bardware device knowns Question 4 what is Multiprocessor Multiprocessor is a Computer System with twoormore Central Brocessing Orit CCPU) shares full access to a Common RAM the main objective of using a multiprocessor is to boost the System's executing Speed. Ourstion 5 Define disk access Time & Average Disk Access Time? Disk Acess Time & Average Calculated As: Disk Access Time = Seektime + Rotational delay + transfer time + Contraller Overhead + queing delay Average Disk Access Time: 110101 Average disk Accesstime = Average Stektime + Average Rotational
delay + transfer time + Controlly Overhead +
quing delay



noifeen	9 Exblain flynn classification
	There are 4 type of classification
1	. Single Instruction Stream, Single data Stream (SIILD)
9	Single Instruction Stream, Multidata stream (SIMD)
7	Multiple Instruction Stream, Single data stream (MISD)
4	Multiple Instruction, Multiple data Stream CMIMD)
1	SISD:  It represent the organization of a single Computer T
1	Containing a Sequentially Control Unit, a processor of and a memory unit. Instruction are Executed Sequentially and the System may or may not have internal Parallel Brocessing Capabilities
2	SIMP A All a nordest & updardest has a some of
s todt s	It Represent an organizations that include many processing Unit under the Supervision of a Common Control Unit. All operate on different item of data
3	MISD AND AND AND AND AND AND AND AND AND AN
7 10	MISD structure is only of theortical interest since no Bractical System has been constructed using this organisation
1 1 700	Back Cal System has been constituted as in the system
4	MIMD AND AND AND AND AND AND AND AND AND AN
	All processor in a borallel Computor can Execute different
	instruction and operate on various data at the Same time

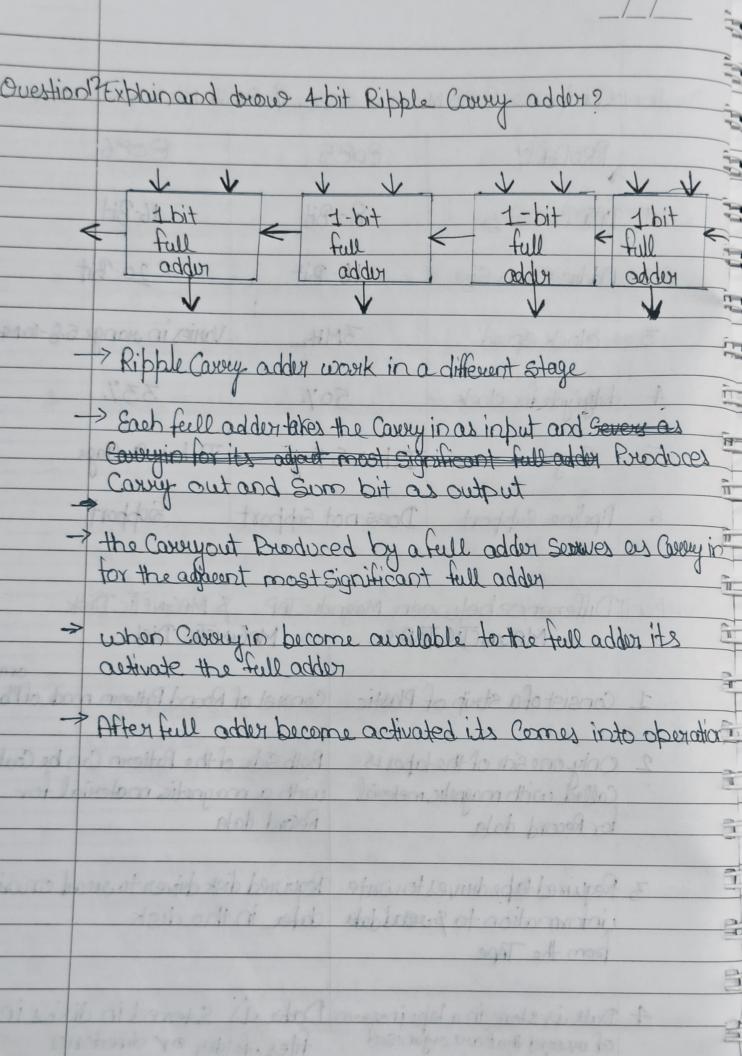
\* Question 10 Compare 8085 Microphocessor & 8086 Microphocessor PROPERTY 8085 8086 DATA BUS Size 8-Bit 16-Bit Address bus Size 20-Bit 16-Bit clock spood Varies in range 5.8-10 MHz 3MHz duty Cycle for clock 33% 50% Flage It has 9 flags Ithos 5 flags T Pipeline Support Does not Support Support Question! Difference between Magnetic TAP & MAGNETIC Disk MAGNETICTARES MAGINETIC DISK Consist of a strip of Plastic Consist of Round Pattern made of Plastic Only one side of the taber is Both Side of the Pattern Can be Coated Called with magnetic meterial with a magnetic material for for Record data Record data Required disk driver to read or write 3 Required tabe drives to write date in the disk information to sucod data from the Tape 4 Data is store in a tabe in your Data is Stored in disks in of vice ord that are organised

tiles, folder or directories

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Daytion 1.	Explain all the inter Connection network
Ingertin	There are 5 type of interConnection network
1	T 1 1 1 2
1	lime-shared Common bus
31	Multiport Memory Chossbar Switch
5]	Multistage Switching network Hyperclub System
do-0 1	beautiful and add add a continuous and the continuo
held	Time-shared Common bus: In this Multiprocessor System
10700 P91	Consist of number of Brocessors Connected through a
	Common Path to a memory Unit. Atime shared Connection
	common bus for five Publessor in Shown
	Memory Opit
	Individual Control
	CPOI CPU2 CPU3 [OP1 IOP2]
	Multiport Memory: A multiport Memory System employs Saprate shown in fig
	employs Saprate shown in tig
	MM1 MM2 MM3
	CPU1
	CPU2
	CPU3

CROSSbar Switch: - the Crossbar Switch organization Consut between Processor buses and memory module Paths Multistage Suitching Network: - the basic Component of a multistage Processor Network is a two Input two-output interdange Switch HyperCube InterConnection: the hyperCube or binary n-cube multiprocessor structure is a loosely Coupled System Composed of N=20 princessor interconnect ed in an n-dimentional binary cube percent tradition a image storage withing Ett di acrope stordes differe

Oustion 14	Draw & Explain 8085 Microprocessor Architecture
	0-05'
	8085 is an 8bit Microprocessor as its operates on 8bit at a time and is created with N-Mos Technology Basically, 8085 was the first Commercially soons the
	Basically, 8085 was the first Commercially Successful
	Basically, 8085 was the first Commercially Successful microphocessor by Intel As an Abichitechture drowback associated with 8080 was also Estimated by 8085
	associated with 8080 was also Estimated by 8085
	Operation Performed:
1	Operations on and stores 8 bit data
2	It Erecute Arithmetic & logic operation
3	8085 also sequence the instruction to be Execute
4	Stores DATA Temporarily