



CERTIFICATE OF COMPLETION

This is to certify	that Mr./Ms. Yashwanth Pra	asad S - 22BEE0207	has successfully
completed	VLSI Physical Design Internship	from 16-05-2025	to 04-07-2025
During the inte	ernship program with us, he/she worked on	PD Implementation of SPI u	sing Qflow project.

Date: 14 Jul 2025

Place: Bengaluru

MSUID: MS/B2BPDI/2025-26-267

