

**Group A2**

- a. Design a 4-Bit CLA (Carry Look Ahead) Adder Circuit.
- b. Design a Magnitude Comparator for 4-Bit .
- c. Design an 8 to 3-Line Priority Encoder Where the Priority is-

$$I_0 > I_1 > I_2 > I_3 > I_4 > I_5 > I_6 > I_7$$