Yasith Imalka

Undergraduate, Computer Science and Engineering, University of Moratuwa

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🔗 yasixportfolio.netlify.app   in Yasith Imalka   ⭘ Yasith Imalka   Ⓜ Yasith Imalka

Profile

Motivated and adaptable individual with a strong drive for excellence in both technical and interpersonal domains. Actively seeking a software engineering internship to apply my skills, contribute meaningfully to projects, and continue evolving as a professional in the field.

Education

University of Moratuwa - Faculty of Engineering   *Katubedda, Moratuwa, Sri Lanka*
Bachelor of Science Honors in Computer Science and Engineering   *2023 March - 2027 March*
CGPA: 3.37/4.00

Rahula College   *Matara, Sri Lanka*
GCE A/L 2021 (First Attempt) - Physical Science   *2019 – 2022*
Island Rank: 360
Z-Score: 2.3206

Projects

MoraClick – Smart Canteen System   *2024 Dec – 2025 Mar*
                    GitHub ☑
                    Live Site ☑
Contribution: Developed a web app to streamline canteen experiences at the University of Moratuwa. Features include login, food availability, ratings, reviews, real-time notifications, and menu management for staff.
Technologies used: React.js, Node.js, Express.js, MongoDB, Firebase, Cloudinary.

FastX – Logistics Management Platform   *2024 Aug – 2024 Nov*
                    See more ☑
Contribution: Developed a logistics management system for a university project, including role-based access, workforce tracking, order routing, and automated scheduling. Used MVC architecture and implemented advanced SQL features.
Technologies used: React.js, Tailwind CSS, Node.js, MySQL (Aiven.io).

RPAL Compiler Components – CS3513 Project   *2025 May – 2025 June*
                    GitHub ☑
Contribution: Designed and implemented a compiler frontend for the RPAL language as a university programming languages project. Developed a custom lexical analyzer and recursive-descent parser from scratch without lex/yacc. Built an Abstract Syntax Tree (AST), transformed it into a Standardized Tree (ST), and implemented a working CSE (Control Stack Environment) machine for interpretation. Enabled command-line flag-based outputs like '-ast' and ensured compatibility with reference implementations.
Technologies used: C++, STL, Recursive Descent Parsing, Custom Tree Structures, CLI.

Nano Processor Design - CS1050 Project   *2024 Jan – 2024 May*
                    See more ☑
Contribution: Designed and simulated a 4-bit nano processor with arithmetic logic, control unit, and register bank. Implemented using VHDL and deployed on BASYS 3 FPGA.
Technologies used: VHDL, BASYS 3 FPGA, Xilinx.

Yasix Real State Website   *2024 Mar – 2024 May*
                    Live Site ☑
Contribution: Created a responsive real estate website showcasing services and

listings. Focused on frontend UI/UX and responsive design.
Technologies used: React.js, HTML, CSS.

GitHub↗

Yasix Gym Website   *2024 Jan – 2024 Mar*
Contribution: Designed and deployed a gym website featuring service highlights,   Live Site↗
offers, and contact form. Focused on engaging frontend components.   GitHub↗
Technologies used: React.js, HTML, CSS.

Technologies

Programming Languages: C++, Java, Python, SQL, JavaScript, TypeScript
Technical Fields: Full Stack Development, Computer Networks, DevOps, Web Hosting
Database Systems: MySQL, MongoDB, PostgreSQL
Tools & Services: Git, Linux, Docker, Kubernetes , VScode
Frameworks: React, Node.js, Next.js, Vite

Languages

Sinhala: Native proficiency
English: Proficient in speaking, reading, and writing

Volunteering Experience

IESL Robo Games 2024 - Web Committee Member   *2024 Dec – 2025 Feb*
IESL, Dept. of Computer Science and Engineering
Contribution: Program committee member of IESL Robo Games 2024.

SLIOT Challenge 2025 - Web Committee Member   *2025 Jan – 2025 Mar*
Dept. of Computer Science and Engineering
Contribution: Program committee member of SLIOT Challenge 2025.

Mora Spirit - Photography Piller Member   *2024 Feb – 2025 Feb*
Mora Spirit - University of Moratuwa
Contribution: Photography Piller Member

References

Prof. Dulani Meedaniya   *Professor, Department of Computer*
                                                          Science and Engineering
                                                          University of Moratuwa
                                                          dulanim@cse.mrt.ac.lk

Dr. Adeesha Wijayasiri   *Senior Lecturer, Department of*
                                                       Computer Science and Engineering
                                                          University of Moratuwa
                                                         adeeshaw@cse.mrt.ac.lk