

An-Najah National University
Faculty of Engineering and Information
Technology Computer Engineering Department



Vlsi HW #2

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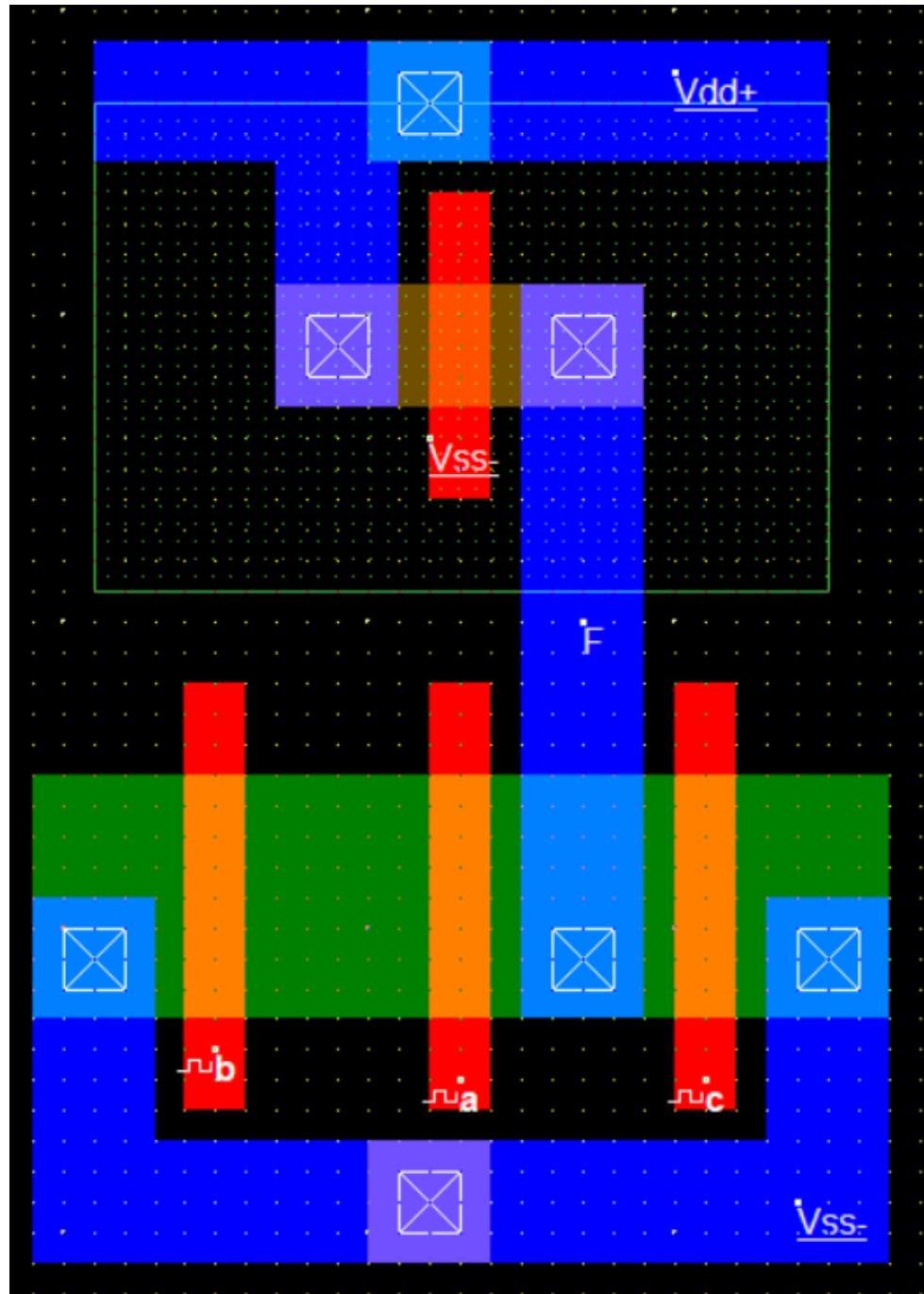
We have this function: $F = (AB + C)'$

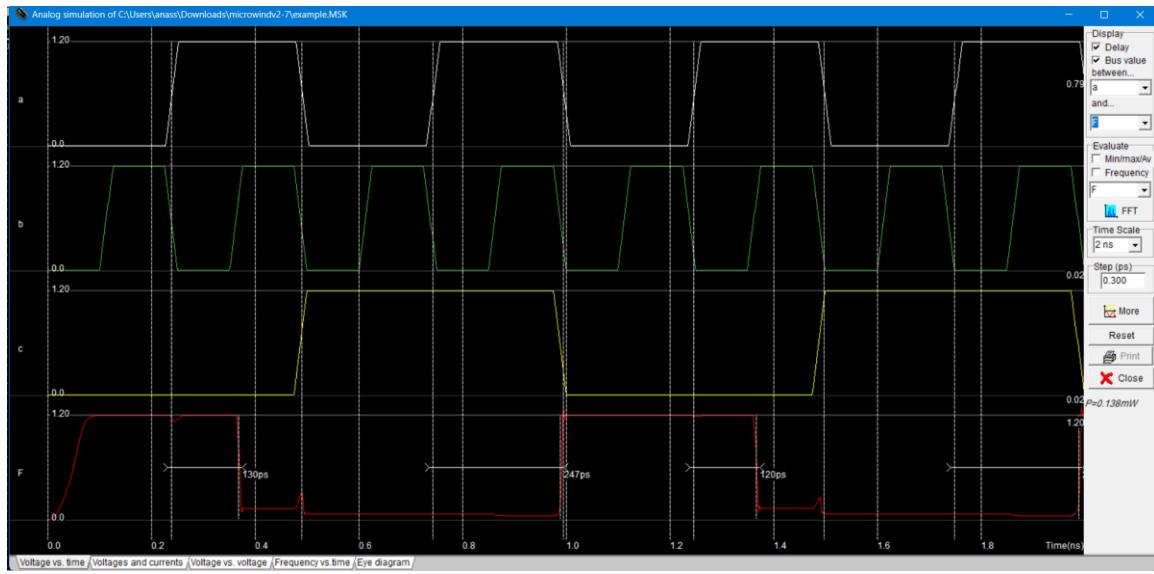
- Since in these technologies there is one P transistor. Make its size 1

⊕ In our simulation we assign to ϕ by (in) label

a. Pseudo-nMOS

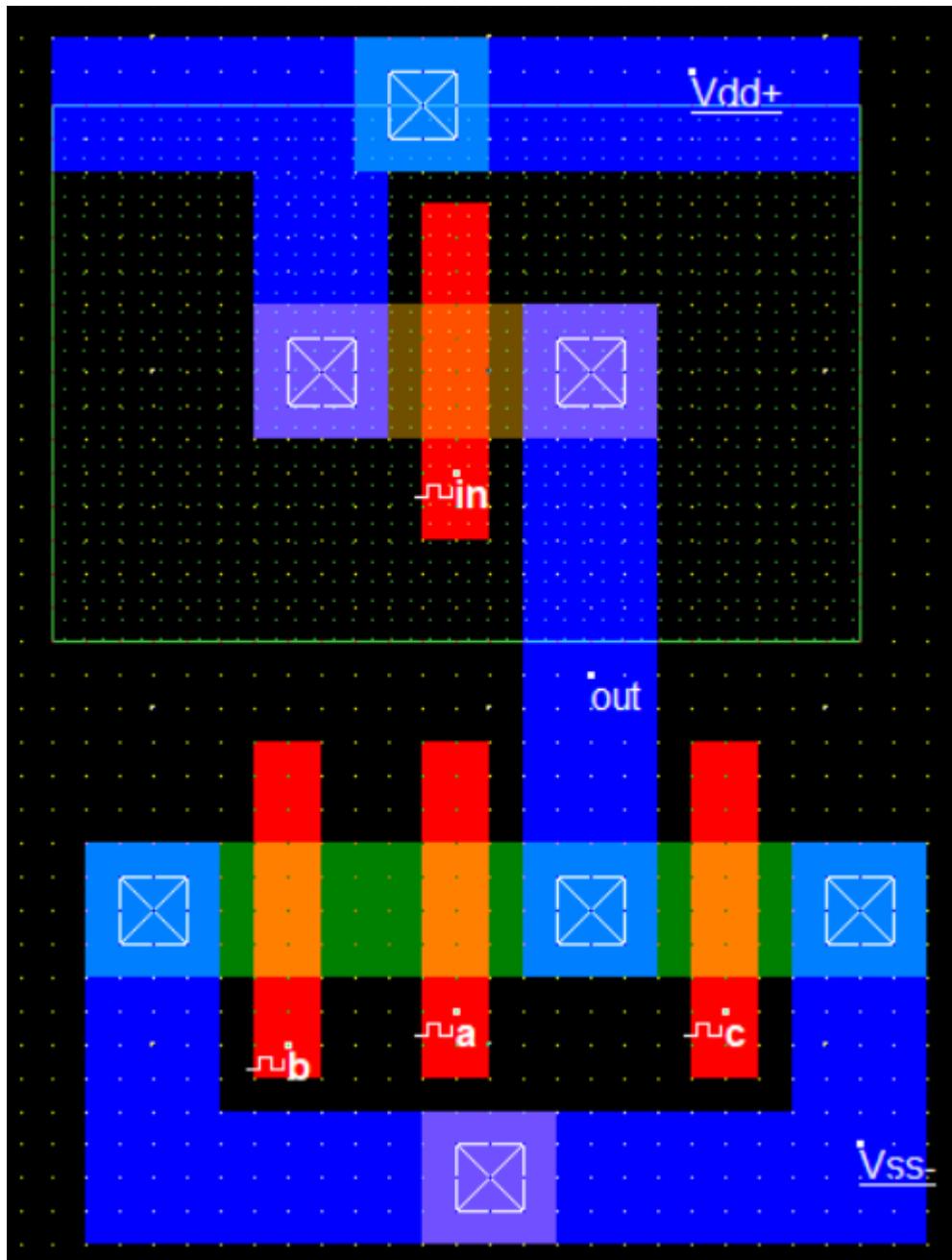
Peq:Neq = 1:2

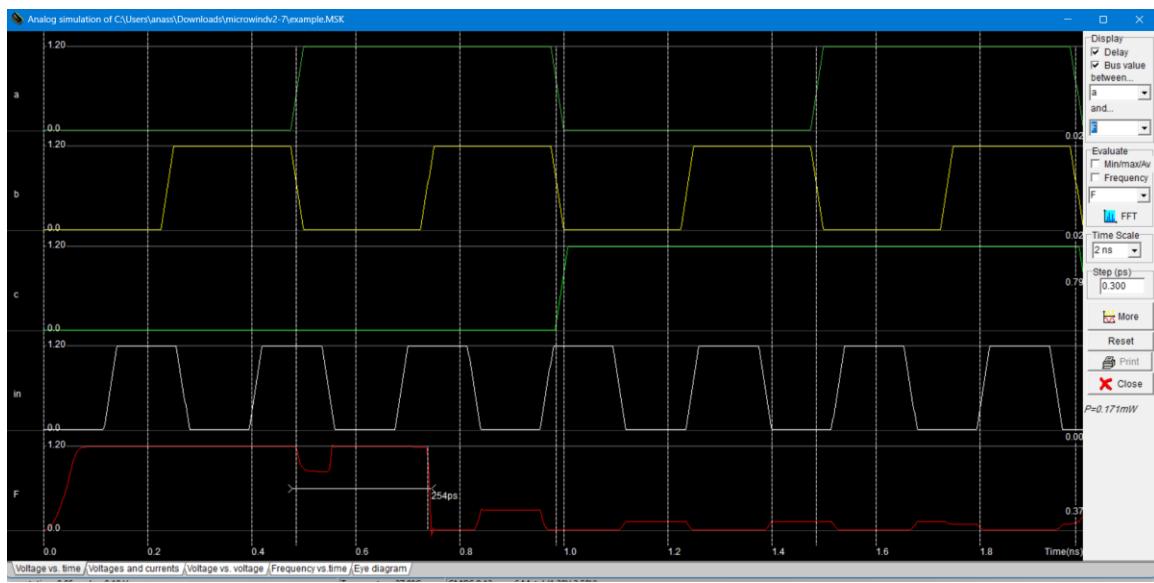




b. Unfooted Dynamic Gate

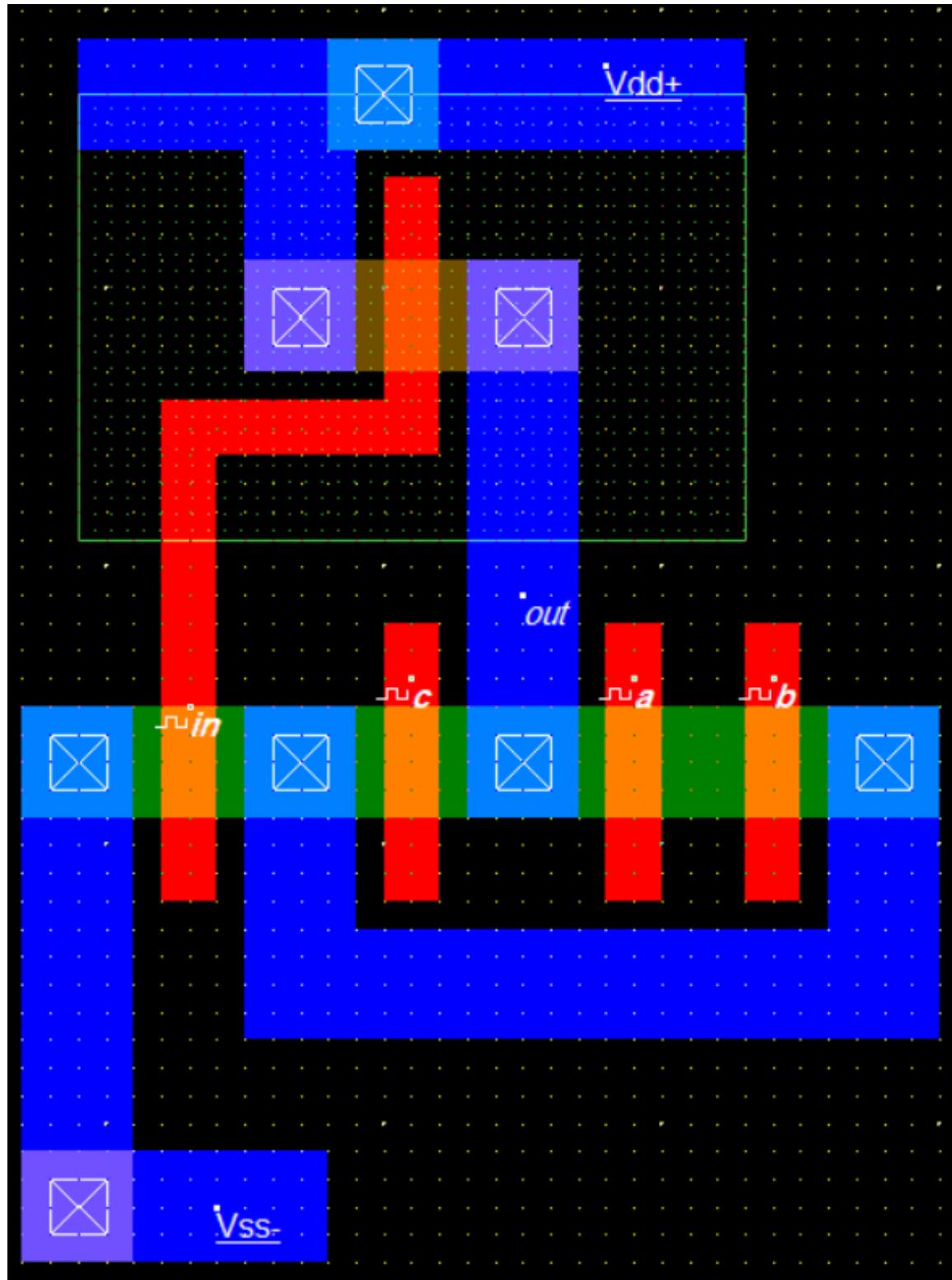
Peq:Neq = 1:1

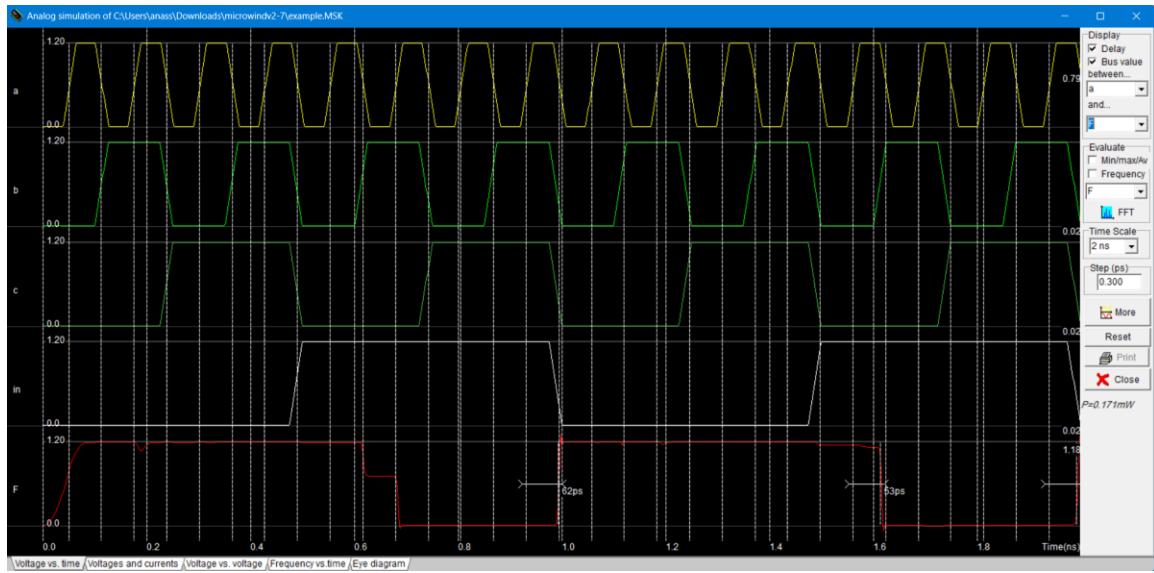




c. Footed dynamic gate

Peq:Neq = 1:1





d. Domino Gate which is F implemented in C followed by Static inverter(ratioed 2:1 regular transistor). In this case you will be implementing $F' = AB+C$

$P_{eq}:N_{eq} = 1:1$

