# PROJECT 2 Implementing EDF Scheduler

Criteria 4: Verifying the System Implementation

Note: The execution time for all tasks is calculated using GPIOs and the logic analyzer, check the last page.

## 1. Using analytical methods calculate the following for the given set of tasks:

#### - Calculate the system hyperperiod:

Hyperperiod is the lowest common multiple of the periodicity of all tasks. Since the periodicity of all tasks is given, we can find hyperperiod as follows:

Hyperperiod = LCM 
$$(50, 50, 100, 20, 10, 100) = 100$$

#### - Calculate the CPU load:

The CPU is the busy time over the total time. Let's consider the CPU for one hyperperiod, so total time = 100. It was found that tasks 1 to 4 share a similar execution time of around 14µs. Meanwhile, task 5 has 5ms execution time whereas task 6 has 12ms execution time. When it comes to the frequency of occurrence of each task within a single hyperperiod, we find that tasks 3 and 6 occur only once, tasks 1 and 2 occur twice, task 4 occurs 5 times, and task 5 occurs 10 times. In total that gives:

CPU load = 
$$(0.014*(2+2+1+5)+5*10+12*1)/100=0.6214=62.14\%$$

- Check system schedulability using URM and time demand analysis techniques (Assuming the given set of tasks are scheduled using a fixed priority rate -monotonic scheduler)

The schedulability test used for a rate monotonic scheduler is as shown below, where n is the number of tasks.

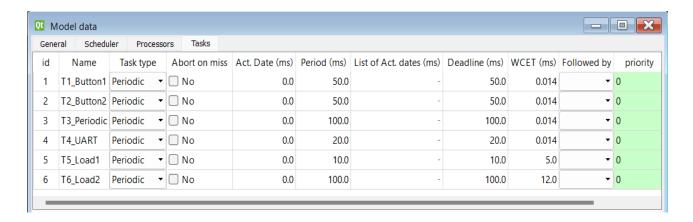
$$U \leq U_{RM} = n(2^{\frac{1}{n}} - 1)$$

$$U_{RM} = 6(2^{\frac{1}{6}} - 1) = 0.7348$$

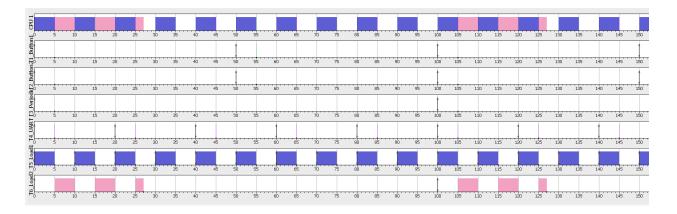
Since  $0.6214 \le 0.7348$ , the system is schedulable.

## 2. Using Simso (Offline Simulator)

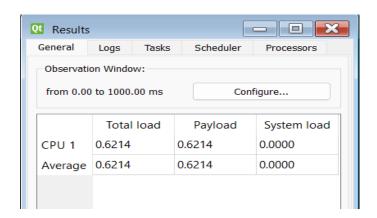
Shown below are the tasks with their respective period, execution time, and deadline.



With a fixed priority rate monotonic scheduler, the gantt chart is as shown below. The system repeats itself every 100ms, which is the hyperperiod as calculated above.



The resulting CPU load is shown below, also 62.14%.

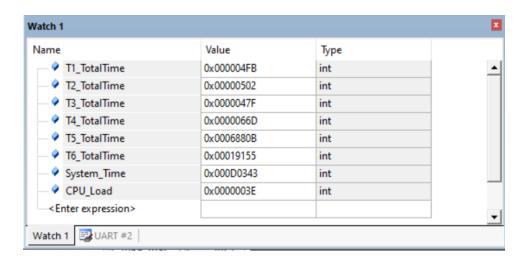


## 3. Using Keil simulator in run-time

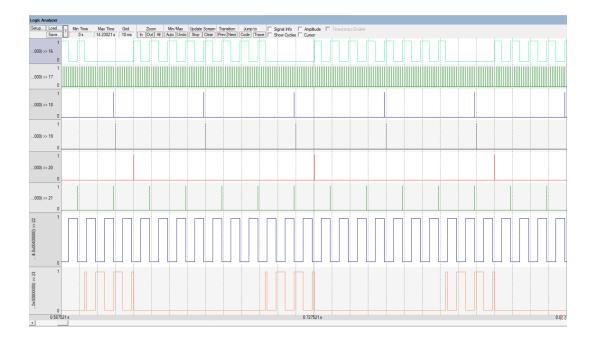
## - Calculate the CPU usage time using timer 1 and trace macros

Using the trace macros, traceTASK\_SWITCHED\_OUT and traceTASK\_SWITCHED\_IN, we can record the times of entry and the times of exit for each task, and add their difference to the total on duration of the task in the variable T#\_TotalTime. As for the system time, we can directly record the latest timer value. The CPU\_load updating code in the trace hook is as follows:

With that, when running the simulation, we can view the value of the CPU\_Load on the Watch window. Shown below is the CPU\_Load percentage settling at 0x3E, that is 62%.



Shown below is the logic analyzer with 8 graphs:



The 1st graph (pin16) is the idle task which sets the pin to 1, so whenever pin16 is high, the idle task is running, and whenever it is low, the other tasks are running (pin16 is set to low by all 6 tasks).

The 2nd graph (pin17) is the tick hook which is highly frequent, thus showing a denser graph.

The 3rd and 4th graph (pin18 and pin19) are button1 and button2 monitoring tasks, respectively. They both show up every 50ms.

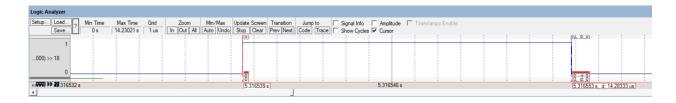
The 5th graph (pin20) is the periodic transmitter showing up every 100ms.

The 6th graph (pin21) is the UART receiver showing up every 20ms

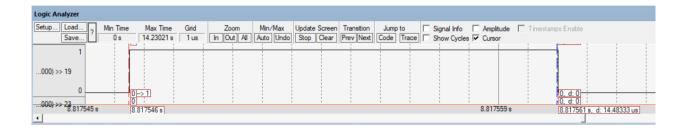
The 7th and 8th graph (pin22 and pin23) are load1 and load2 simulating tasks, respectively. Load1 shows up every 10ms, and load2 shows up every 12ms.

For execution times of tasks 1 to 4, a closer look at each task's graph is needed:

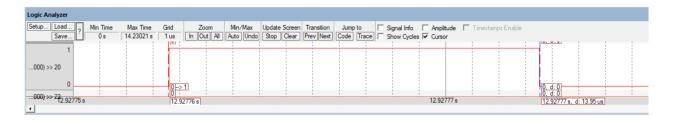
Task 1: 14.28µs



Task 2: 14.48µs



Task 3: 13.95µs



Task 4: 14µs

