### BOOT\_ChooseAndLoadSW\_CLSW

#### BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_FNOR

This function checks that the SIF header address is stored in FLASH PROGRAM.

Prototype:

void BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_FNOR(

const uint32\_t p\_SIFAddress,

const ts\_BOOT\_Data\* const p\_BootData,

uint8\_t\* p\_SIF\_HeaderBuffer,

boolean\_t\* p\_FlashReadFailed)

Parameters:

Function return : Not used

{p\_SIFAddress} (R) : SIF Header address

{p\_BootData } (R) : BOOT data

{p\_SIF\_HeaderBuffer}(R) : SIF Header Buffer

{p\_FlashReadFailed}(W): Read failure status

Calls:

LIBBSP\_FNOR\_Open

LIBBSP\_FNOR\_FastRead

LIBBSP\_FNOR\_Close

Preconditions:

None

##### Input Data

Data:

None

Preconditions:

None

##### Output Data

Data:

None

##### Requirements

REQ\_SDDD\_BOOT\_000xx-0x

*[COV.REQ\_SRD\_BOOT\_00169] [COV.REQ\_SRD\_BOOT\_00170]*

BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_FNOR

|  |  |  |  |
| --- | --- | --- | --- |
|  | *Causes* | | |
| Effects | **[**Open SPI link to access FLASH**]** | | |
| [The access to the FLASH is not successful] | [The access to the FLASH is successful] | |
| Set {p\_FlashReadFailed} to TRUE and exit function | **[**Retrieve the SIF header address from FLASH**]**  **[**Close The SPI link to the FLASH**]** | |
| [An error is detected] | [No error detected] |
| Set {p\_FlashReadFailed} to TRUE and exit function | No effect |

**[**Open SPI link to access FLASH**]**: corresponds to the following call:

**LIBBSP\_FNOR\_Open**

* **Function return:** {FLASH status}
* **IN:** {E\_LIBBSP\_SPI\_FLASH\_PROGRAM}

[The access to the FLASH is not successful]: {FLASH status} is not equal to {E\_LIBBSP\_FNOR\_OK}.

[The access to the FLASH is successful]: {FLASH status} is equal to {E\_LIBBSP\_FNOR\_OK}.

**[**Retrieve the SIF header address from FLASH**]**: corresponds to the following call:

**LIBBSP\_FNOR\_FastRead**

* **Function return:** {FLASH status}
* **IN:** {E\_LIBBSP\_SPI\_FLASH\_PROGRAM}
* **IN:** {p\_SIFAddress}
* **IN:** {p\_BootData->size}
* **OUT:** {p\_SIF\_HeaderBuffer}

**[**Close The SPI link to the FLASH**]**:corresponds to the following call:

**LIBBSP\_FNOR\_Close:**

* **Function return:** Not used

[An error is detected]: {FLASH status} is different from {E\_LIBBSP\_FNOR\_OK}.

[No error detected]: {FLASH status} is equal to {E\_LIBBSP\_FNOR\_OK}.

Traceability: Refined

Rationale:

Mean of verification: Test

[END\_REQ\_SDDD\_BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_FNOR\_000xx-0x]