### BOOT\_ChooseAndLoadSW\_CLSW

#### BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_RMM

This function retrieves the SIF Header address from the RMM.

**Prototype:**

void BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_RMM(

const uint32\_t p\_SIFAddress,

const ts\_BOOT\_Data\* const p\_BootData,

uint8\_t\* p\_SIF\_HeaderBuffer,

boolean\_t\* p\_FlashReadFailed)

**Parameters:**

Function return : Not used

{p\_SIFAddress} (R) : SIF header address

{p\_BootData} (R) : BOOT data

{p\_SIF\_HeaderBuffer} (R/W): SIF header Buffer

{p\_FlashReadFailed} (W) : Read failure status

**Calls:**

LIBBSP\_RMM\_Open

LIBBSP\_RMM\_Read

LIBBSP\_RMM\_Close

**Preconditions:**

None

##### Input Data

**Data:**

None

**Preconditions:**

None

##### Output Data

**Data:**

None

##### Requirements

REQ\_SDDD\_ BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_RMM\_000xx-0x

*[COV.* *REQ\_BOOT\_SRD-00169; COV.* *REQ\_BOOT\_SRD-00170]*

***BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_RMM***

|  |  |  |  |
| --- | --- | --- | --- |
|  | Causes | | |
| Effects | **[**Open I2C link to RMM**]** | | |
| [The I2C link to RMM is not successful] | [The I2C link to RMM is successful] | |
| Set {p\_FlashReadFailed} to TRUE and exit the function | **[**Retrieve the SIF Header address from RMM**]**  **[**Close The access to the device**]** | |
| [An error is detected] | [No error detected] |
| Set {p\_FlashReadFailed} to TRUE | No effect |

**[**Open I2C link to RMM**]:** corresponds to the following call:

**LIBBSP\_RMM\_Open**

* **Function return**: {access status}

[The I2C link to RMM is not successful]: {access status} is different from {E\_LIBBSP\_I2C\_OK}.

[The I2C link to RMM is successful]: {access status} is equal to {E\_LIBBSP\_I2C\_OK}.

**[**Retrieve the SIF Header address from RMM**]** corresponds to the following call:

**LIBBSP\_RMM\_Read**

* **Function return:** not used
* **IN:** {p\_SIFAddress} with (Bit 0=0)
* **IN:** {p\_BootData->size}
* **OUT:** {p\_SIF\_HeaderBuffer}
* **IN/OUT:** The address of {access status}

**[**Close The access to the device**]:** corresponds to the following call

**LIBBSP\_RMM\_Close**

* **Function return**: not used

[An error is detected]: {access status} is different from {E\_LIBBSP\_I2C\_OK}.

[No error detected]: {access status} is equal to {E\_LIBBSP\_I2C\_OK}.

Traceability: Refined

Rationale:

Mean of verification: Test

[END\_REQ\_SDDD\_ BOOT\_CLSW\_LoadSW\_Load\_LoadAndTestSIF\_Header\_RMM\_000xx-0x]