### BOOT\_TestAndConfigureAdditionalHW

#### BOOT\_TCAHW\_REG

This function initializes the PBIT failure status and the state of PBIT results.

Prototype:

void BOOT\_TCAHW\_REG (boolean\_t\* p\_PBIT\_Failed,

ts\_PBIT\_Results\* p\_PBIT\_Results)

Parameters:

Function return : Not used

p\_PBIT\_Failed (W) : PBIT failure status

p\_PBIT\_Results (W) : PBIT results

Calls:

None

Preconditions:

None

##### Input Data

Data:

None

Preconditions:

None

##### Output Data

Data:

None

##### Requirements

REQ\_SDDD\_BOOT\_000xx-0x

*[REQ\_BOOT\_SRD-00163]*

BOOT\_TCAHW\_REG

|  |  |  |
| --- | --- | --- |
|  | Causes | |
| Effects | [The masked value extracted from the PORPLLSR register is valid] | [The masked value extracted from the PORPLLSR register is not valid] |
| Set {p\_PBIT\_Failed} to TRUE  AND  {p\_PBIT\_Results->ConfRegPBIT\_Failed} to TRUE | Do nothing |

[The masked value extracted from the register is valid]: (C\_LIBBSP\_CCSR\_GUTS & C\_REG\_GUTS\_PORPLLSR\_VALID\_MASK) is different from (C\_REG\_GUTS\_PORPLLSR\_VALID\_VALUE)

[The masked value extracted from the register is not valid]: (C\_LIBBSP\_CCSR\_GUTS & C\_REG\_GUTS\_PORPLLSR\_VALID\_MASK) is equal to (C\_REG\_GUTS\_PORPLLSR\_VALID\_VALUE)

Traceability: Refined

Rationale:

Mean of verification: Test

[END\_REQ\_SDDD\_BOOT\_TCAHW\_REG\_00xxx-0x]