

# *Certificate of Completion*

*This is to certify that **Yassin Rostom** successfully  
completed 6 total hours of **FPGA Design Learning**  
**VHDL** online course on June 3, 2020*

*SURF VHDL*

SURF VHDL, Instructor

&



Certificate no: UC-deb22c47-cba8-4337-8de1-9d58d4163d37  
Certificate url: [ude.my/UC-deb22c47-cba8-4337-8de1-9d58d4163d37](https://ude.my/UC-deb22c47-cba8-4337-8de1-9d58d4163d37)

#BeAble