

NOAH HÜSSER

Nationality: Swiss
Date of Birth: 12th January 1991

CONTACT

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ACTIVITIES

Programming, Electronics,
Server Admin, Ju Jitsu,
Reading

SKILLS

PROFESSIONAL

Software Development
Embedded Systems
FPGA Programming
Electronical Prototyping
Project Management

LANGUAGES

German *mother tongue*
English *fluent*
French *experienced*

TOOLS

ENGINEERING

Altium/KiCad
Linux/Unix Administration
Jupyter, Scipy, Numpy, Pandas
MATLAB
Autodesk Inventor

PROGRAMMING

frequently used
Python, C, VHDL, TCL,
JavaScript, SQL, LaTeX

used in the past

Java, C#, PHP, C++, Bash

MISCELLANEOUS

Git/SVN
Microsoft Office
Photoshop, InDesign
Last updated 2017-04.

EDUCATION

FHNW BRUGG-WINDISCH

BsC in Electrical Engineering and Information Technology,
Feb 2016 to present in Windisch

ETH ZÜRICH

BsC in Electrical Engineering and Information Technology,
Sep 2013 to Feb 2016 in Zürich

ALTE KANTONSSCHULE AARAU

Matura, Graduated 2012 in Aarau

PRACTICAL EXPERIENCE

ABB MICAFIL

Intern Software Engineer, Jul 2016 to present in Altstetten

Development in VB.NET on a CAD tool to dimension bushings. Development of various tools in VB.NET, C# and JS to smoothen the process from the design of a bushing to it's production.

BASTLI

President, Feb 2016 to Oct 2016 in Zürich

Active Member, Sep 2013 to present in Zürich

Bastli is the student's electronics lab at ETH Zürich. With Bastli we realize creative engineering projects such as:

- BIRD: An interactive LED dancefloor that is lit up when stepping onto it.
- SWARM 2: A collection of autonomous LED badges that can communicate over IR, react to sound and have capacitive touch input.

Impressions on what we do at www.bastli.ch.

NEXUS TELECOM

Low Level C Programmer, Mai 2014 to Mai 2015 in Zürich

Diverse work on their main C library with focus on porting it from 32 to 64 bit.

AFC AG

Internship Software Engineer, Dec 2012 to Apr 2013 in Zürich

Creation of various tools in Python, C# and VB, working as a one man team.

WAPPLE.CH

Webdesigner and Co-Founder, Jan 2007 to present in Zürich

Webdesign for individuals and companies in cooperation with a friend.

PUBLICATIONS AND PROJECTS

FPGA BASED OSCILLOSCOPE

Group Thesis at ETH Zürich, Sep 2015 to Dec 2015 in Zürich

The tasks included but were not limited to the implementation of:

- Recursive trigger logic to detect special signal patterns (VHDL)
- Kernel module for data reading and processing on an ARM Core A9 (C)
- GUI to retrieve data over TCP/IP, filter and display it (C++, Python, Qt5)

230V SWITCH

Bluetooth Controlled Two Channel 230V Switch, Nov 2015 to present in Zürich

Design of a PCB and Software that feature dimming, master/slave control, timing and bluetooth control of ordinary power outlets. A proof of concept is completed.