

NOAH HÜSSER

Nationality: Swiss
Date of Birth: 12th January 1991

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ACTIVITIES

Programming, Electronics, Server Admin, Ju Jitsu

PROFESSIONAL SKILLS

Software Development Embedded Systems FPGA Programming Electronical Prototyping Project Management

LANGUAGES

German mother tongue English fluent French experienced

ENGINEERING TOOLS

Altium/KiCad Linux/Unix Administration Jupyter, Scipy, Numpy, Pandas MATLAB Autodesk Inventor

PROGRAMMING

frequently used Python, C, VHDL, TCL, JavaScript, SQL, LaTeX

used in the past Java, C#, PHP, C++, Bash, VB.NET

MISCELLANEOUS

Git/SVN Microsoft Office Photoshop, InDesign

REFERENCES

on request

Last updated 2017-08.

EDUCATION

FHNW BRUGG-WINDISCH

BsC in Electrical Engineering and Information Technology, Feb 2016 to present in Windisch

ETH ZÜRICH

BsC in Electrical Engineering and Information Technology, Sep 2013 to Feb 2016 in Zürich

ALTE KANTONSSCHULE AARAU

Matura, Graduated 2012 in Aarau

PRACTICAL EXPERIENCE

ABB MICAFIL

Software Engineer, Jul 2016 to present in Altstetten

Development of a CAD tool to dimension bushings in VB.NET. Development of a shopfloor application to monitor, analyze and control the production line of a bushing.

BASTLI, ETH ZURICH

President, Feb 2016 to Oct 2016 in Zürich

Active Member, Sep 2013 to present in Zürich

Bastli is the student's electronics lab at ETH Zürich. With Bastli we realize creative engineering projects such as:

- BIRD: An interactive LED dancefloor that is lit up when steping onto it.
- Swarm 2: A collection of autonomous LED badges that can communicate over IR, react to sound and have capacitive touch input.

Impressions on what we do at www.bastli.ch.

NEXUS TELECOM

Low Level C Programmer, Mai 2014 to Mai 2015 in Zürich

Diverse work on their main C library with focus on porting it from 32 to 64 bit.

AFC AG

Internship Software Engineer, Dec 2012 to Apr 2013 in Zürich

Engineering technology in the field of CFD simulations and clean room solutions. Development of various tools in Python, C# and TCL to enhance CFD simulations.

PUBLICATIONS AND PROJECTS

FPGA BASED SPECTRUM ANALYZER

Bachelor Thesis at FHNW, Mar 2017 to Aug 2017 in Windisch

The tasks included but were not limited to the implementation of:

- CIC/FIR-filter-chains for data decimation
- A server to transmit data via WebSockets on an embedded Linux
- A GUI to retrieve data over WebSockets, transform and display it (JavaScript)

FPGA BASED OSCILLOSCOPE

Group Thesis at ETH Zürich, Sep 2015 to Dec 2015 in Zürich

The tasks included but were not limited to the implementation of:

- Recursive trigger logic to detect special signal patterns (VHDL)
- Kernel module for data reading and processing on an ARM Core A9 (C)
- GUI to retrieve data over TCP/IP, filter and display it (C++, Python, Qt5)

BACKDOOR

NFC Auth System, Dec 2014 to Jun 2015 in Zürich

A system to manage access on devices such as Bastli's door to the backyard or our beverage vending machine.