

Data Conversion Chain — 20-02-2017

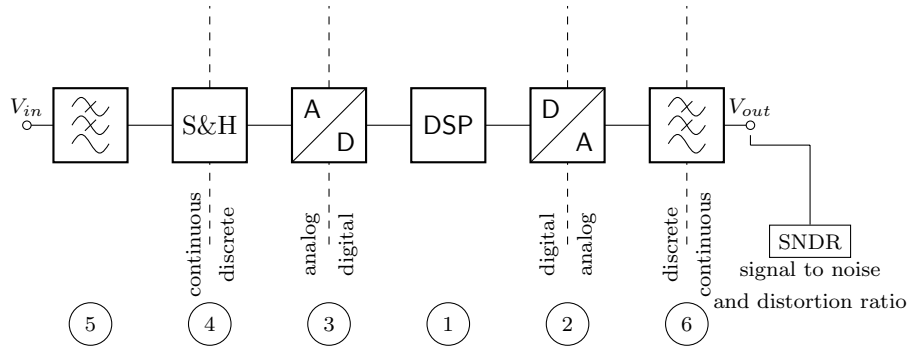


Figure 1: A typical signal chain

① DSP

The equivalent output voltage can be expressed with 1. Its maximum can be described with 2

$$V_{eq} = V_{ref} \left(\sum_{i=1}^N b_i 2^{-i} \right) \quad (1)$$

$$V_{eq} = V_{ref} (1 - 2^{-N}) \quad (2)$$

This is a representation in UINT. In most realworld implementations INT using 2's complement is required. Sometimes if there is peak currents, Gray-Code is to be used to minimize peak currents!

The quantizer-error is defined with equation 3.

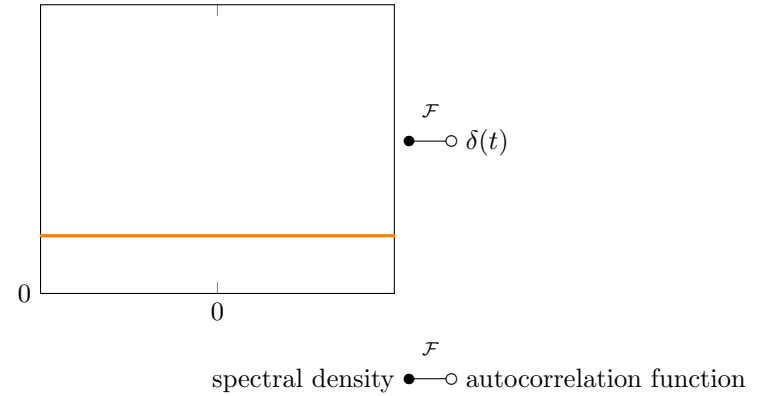
$$V_e = V_{in} - V_{eq} \quad (3)$$

It is assumed that the error behaves like white noise¹ because the digital signal is a sequence of pulses. If this is fourier transformed a constant spectral density is received.

¹White noise means that the noise has the same amplitude for every frequency.

²root mean square

³signal to noise ratio



Since the quantizer error has the probability density function of white noise, it can be depicted with the function seen in 2.

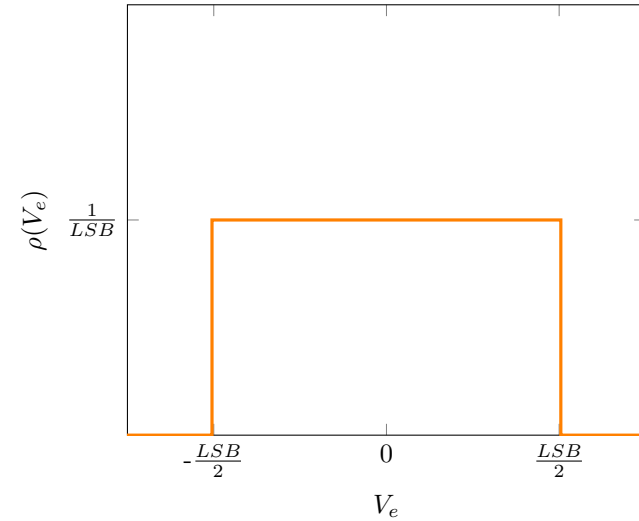


Figure 2: Probability density function of the quantizer error

With the knowledge of the fact that the variance equals the RMS² value, depicted in equation 4 and thus 8, we can now find the SNR³ value with solving equation 11

$$(rms)^2 = \sigma, \sigma \text{ is the standard deviation} \quad (4)$$

$$\sigma_e^2 = \int_{-\infty}^{+\infty} V_e^2 \rho(V_e) dV_e \quad (5)$$

$$= \int_{-\frac{LSB}{2}}^{+\frac{LSB}{2}} \frac{V_e^2}{LSB} dV_e \quad (6)$$

$$= \frac{1}{LSB} \frac{1}{3} V_e^3 \Big|_{-\frac{LSB}{2}}^{+\frac{LSB}{2}} \quad (7)$$

$$= \frac{LSB^2}{12} = \frac{1}{12} V_{ref}^2 2^{-2N} \quad (8)$$

$$SNR = \frac{V_{sig,rms}^2}{V_{n,rms}^2} \quad (9)$$

$$= \frac{\left(\frac{V_{ref}}{2} \frac{1}{\sqrt{2}} \right)^2}{\frac{1}{12} V_{ref}^2 2^{-2N}} \quad (10)$$

$$= N \cdot 6.02dB + 1.76dB \quad (11)$$

Using this equation a statement about the ENOB⁴ can be made, having a look at the SNR equation obtained. This is given in 12.

$$N_{eff} = ENOB = \frac{SNDR - 1.76dB}{6.02dB}$$

② D/A

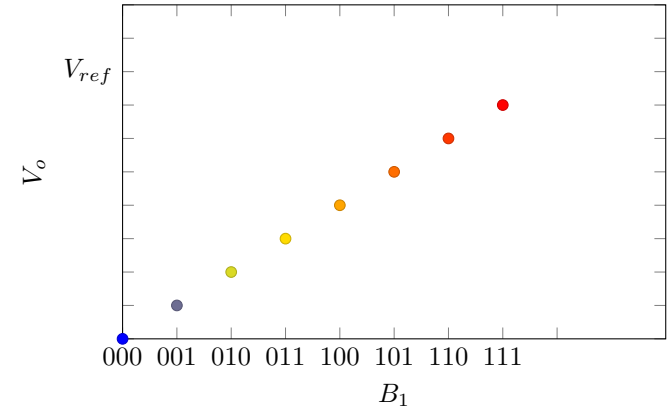


Figure 3: Output of a DAC and possible errors

③ A/D

④ S&H

⑤⑥ LP

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⁴effective number of bits