

NOAH HÜSSER

Nationality: Swiss

Date of Birth: 12th January 1991

CONTACT

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ACTIVITIES

Programming, Electronics, Server Admin, Ju Jitsu

PROFESSIONAL SKILLS

Software Development Embedded Systems FPGA Programming Electrical Prototyping Project Management

LANGUAGES

German mother tongue English fluent French experienced

ENGINEERING TOOLS

Altium/KiCad Linux/Unix Administration Jupyter, Scipy, Numpy, Pandas MATLAB Autodesk Inventor

PROGRAMMING

frequently used Python, C, VHDL, Tcl, JavaScript, SQL, LaTeX

used in the past Java, C#, PHP, C++, Bash, VB.NET

MISCELLANEOUS

Git/SVN Microsoft Office Photoshop, InDesign

REFERENCES

on request Last updated 2021-09.

PRACTICAL EXPERIENCE

TECHNOKRAT

Co-Founder, Software Engineer, May 2017 to October 2021 in Schlieren Founded a consulting company, all things electrical enginnering with 3 colleagues. Realizing Software from embedded devices to cloud services.

SIGLIS

Co-Founder, Software Engineer, August 2020 to August 2021 in SchlierenFounded a smart home company which develops a smart switch (zigfred.ch). Developing and manufacturing a product from start to finish. Responsibilities included mechanical design, mobile app and automated factory testing and provisioning.

ABB MICAFIL

Part Time Software Engineer, Jul 2016 to March 2018 in Altstetten

Development of a CAD tool to dimension bushings in VB.NET. Development of a shopfloor application to monitor, analyze and control the production line of a bushing.

NEXUS TELECOM

Low Level C Programmer, Mai 2014 to Mai 2015 in Zürich Porting the core C library from 32 to 64 bit.

VOLUNTEER EXPERIENCE

JU JITSU CLUB AARAU

Treasurer, Apr 2019 to present in Aarau

BASTLI. ETH ZURICH

President, Feb 2016 to Oct 2016 in Zürich Treasurer, Feb 2015 to Feb 2016 in Zürich

EDUCATION

FHNW BRUGG-WINDISCH

BsC in EE and IT, Feb 2016 to Jan 2017 in Windisch

ETH ZÜRICH

BsC in EE and IT, Sep 2013 to Feb 2016 in Zürich

PUBLICATIONS AND OPEN SOURCE

PROBE-RS EMBEDDED TOOLCHAIN

Project Lead, Mar 2019 to present in probe.rs

The toolkit allows to control embedded ARM and RISC-V MCUs to be controlled from a host. The project offers:

- a libray to control targets from code
- CLI tools to flash and log data from targets
- A VSCode debugger plugin

FPGA BASED SPECTRUM ANALYZER

Bachelor Thesis at FHNW, Mar 2017 to Aug 2017 in Windisch

The tasks included, but were not limited to, the implementation of:

- CIC/FIR-filter-chains for data decimation
- A server to transmit data via WebSockets on an embedded Linux
- A GUI to retrieve data over WebSockets, transform and display it (JavaScript)

FPGA BASED OSCILLOSCOPE

Group Thesis at ETH Zürich, Sep 2015 to Dec 2015 in Zürich

The tasks included, but were not limited to, the implementation of:

- Recursive trigger logic to detect special signal patterns (VHDL)
- Kernel module for data reading and processing on an ARM Core A9 (C)
- GUI to retrieve data over TCP/IP, filter and display it (C++, Python, Qt5)