

# Yazan Habash

Computer Engineer



✉ habashyazan@yahoo.com ☎ 0598641757

📍 Nablus, Palestine in Yazan Habash

🔗 Yazan-Habash114

## PROFILE

I am newly graduated from computer engineering department at An-Najah National University. I am interested in many fields such as Web development and Design Verification.

## EDUCATION

**Bachelor's degree of computer engineering,**

*An-Najah National University* 🔗

2018 – 2023 | Nablus, Palestine

GPA: 3.73/4.00 (excellent)

**Tawjihi, Al-Salahiah secondary school**

2018

95.6%

## SKILLS

- Skilled in HTML, CSS, JavaScript, Java and C/C++
- Good in design verification (VHDL, Verilog/SystemVerilog and testbenches)
- Good in Linux commands
- Familiar with Python & PHP
- Communication skills and team working
- Clean code
- Self-motivated
- Quick learner
- Hard worker

## LANGUAGES

**Arabic** ● ● ● ● ●  
Native

**English** ● ● ● ● ●  
Good

## WORK EXPERIENCE

**Design Verification Internship, An-Najah Company** 🔗

Jun 2023 – present

I am gaining experience in Verilog, SystemVerilog, designing, layered testbench, coverage and I am currently learning UVM.

**ReactJS internship, Gaza Sky Geeks** 🔗

Feb 2023 – May 2023

I have gained an experience in front-end web development, introduction to Express.js and other topics such as data structures and algorithms, system design, OOP principles and AWS.

## CERTIFICATIONS

**Arduino Practical Course (Trainer)**

An-Najah IEEE student branch, 25 hours

**English Language**

Amideast; Level 4

## COURSES

**Object Oriented Programming (OOP)**

**Data structures, Databases and Algorithms**

**VHDL and Verilog/SystemVerilog**

**Computer Networks and Information Security**

**Linux OS**

**Distributed Operating Systems**

Microservices, REST, virtualization, replication and fault tolerance principles

## PROJECTS

**iDrive** 🔗

Software graduation project, it is a React Native app that can recognize the driver's car problem & connect him to the closest garage on the map.

**UART Protocol, ASM machine implemented by Verilog** 🔗

UART is a hardware communication protocol that uses asynchronous serial communication with configurable speed. So, in this project I design the protocol and verify it in all of its possible configurations using direct testing.

**FIFO Memory** 🔗

FIFO stands for First In, First Out which is a method for organizing the manipulation of a data structure (often, specifically a data buffer) where the oldest (first) entry, is processed first. So, in this project I designed and verify the FIFO memory using direct testing.

**Bazar app** 🔗

Flask project, microservices with REST APIs distributed on virtual machines.

**Baymax, Dynamic Programming Problem** 🔗