1 Explain LE Ainm) me sone by morreto Brains and so The LEA instruction is all Load Perfective laddresse which is an indirected instruction, which means the table-ADDR points contour memory location and which the addition to load in found. Dota regment is a porction of an object like or the corresponding viretual additions page of a program trat contains intralized statice varciables ithat in global varoiables and ortate local variable. Data is a portion of an object addition space of a the ore the concresponding Program that contains initialized atalle variable wardables vardables. Our mospoon pas small the Breeze

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4. The memory address register (mar) by the PU register that either stories the memory admin from which data will be fetered to the OPU, OTT The addition to which data will be sent and obstorced in memory before register (MBR) also known at as memory adres register memory data register (MDR) holds inemorganitate that is transferenced to be

from the data has trop day more than the assembler

to assume director to the transmitter

to assume the day of the transmitter

to assume the day of the transmitter

to assume the director of the day of the distance of the day of the d to assume that a certain register contains the base of name of the code perment and the data regiment respectively, both the one and only of their met respective kind.

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