# XXX Design Spec

Date

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| --- | --- | --- | --- |
| Revision History | | | |
| Date | Author | Version | Description |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

# 项目背景

# 主要的feature

# 硬件设计细节

# Command设计

|  |  |  |
| --- | --- | --- |
| Cmd name | Length | Description |
|  |  |  |
|  |  |  |
|  |  |  |

# 接口信号定义

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 信号名称 | Width | I/O | P/L/D  (P/L/D：pulse，level，data) | Description | Clk domain |
| Data |  | Input |  | Data信号如果存在无效字节，需要标记低字节还是高字节无效 |  |
|  |  | Output |  |  |  |
|  |  |  |  |  |  |
|  |  |  |  |  |  |
| Error |  |  |  | 说明在什么时候有效，在整个数据周期都保持？ |  |
|  |  |  |  |  |  |

# 时钟及复位

# PPA估计