

PicoZed - Rev C 7/16/2015

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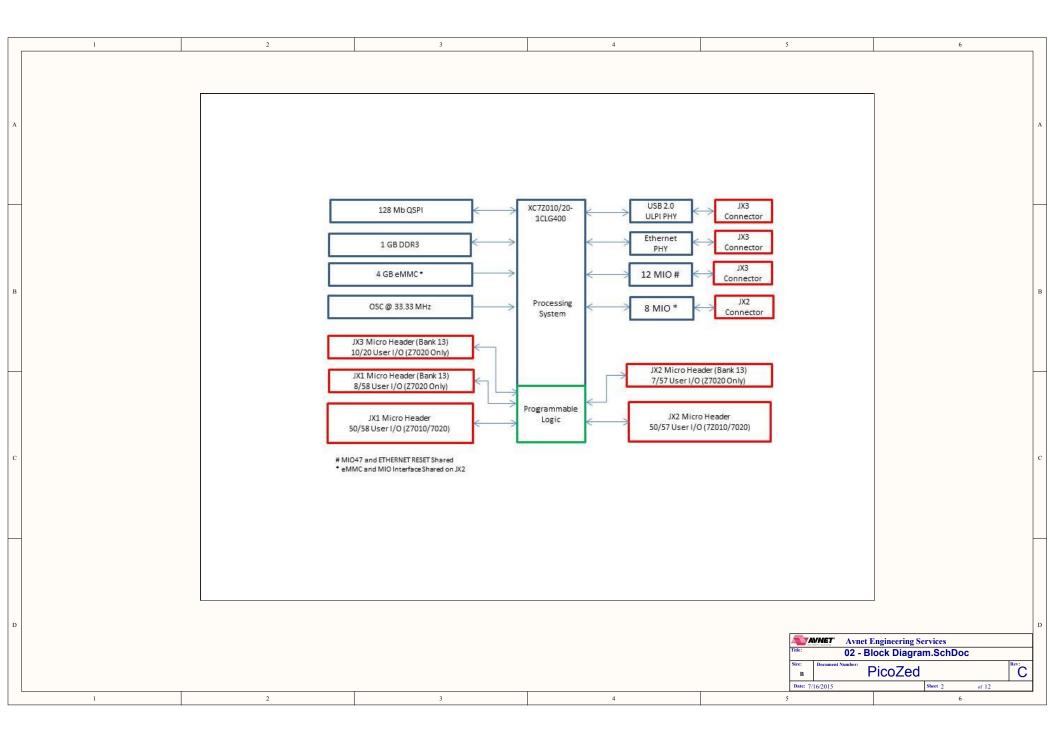
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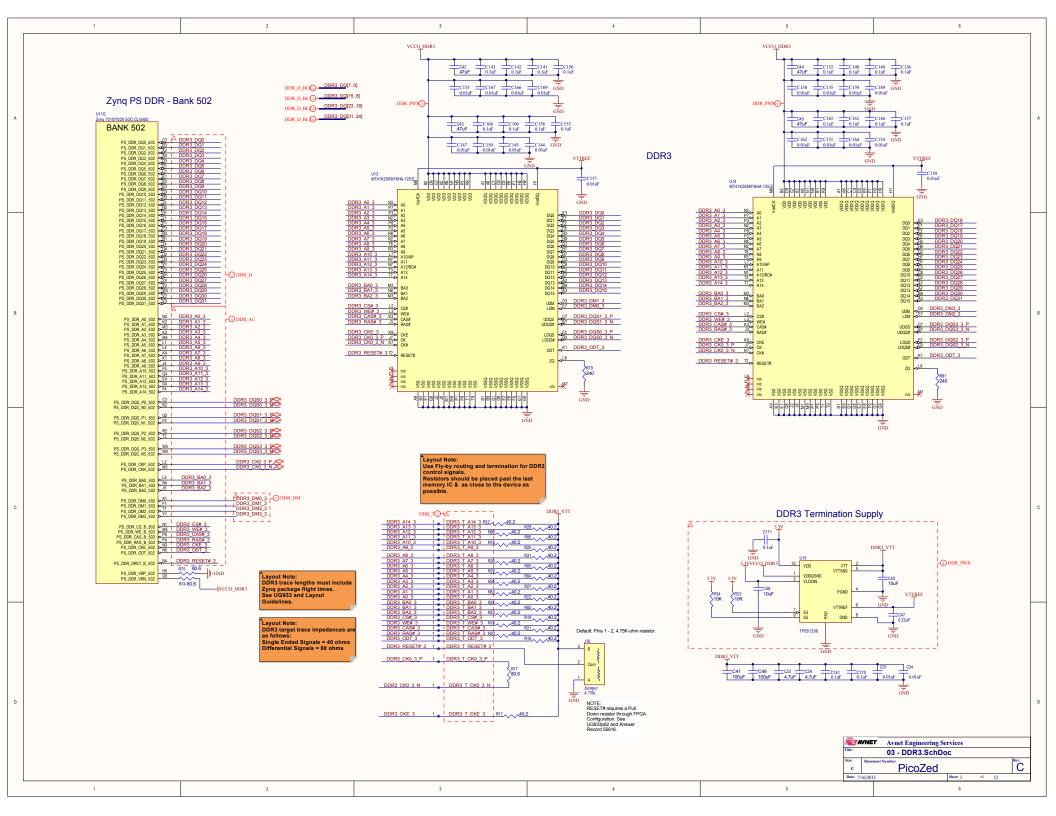
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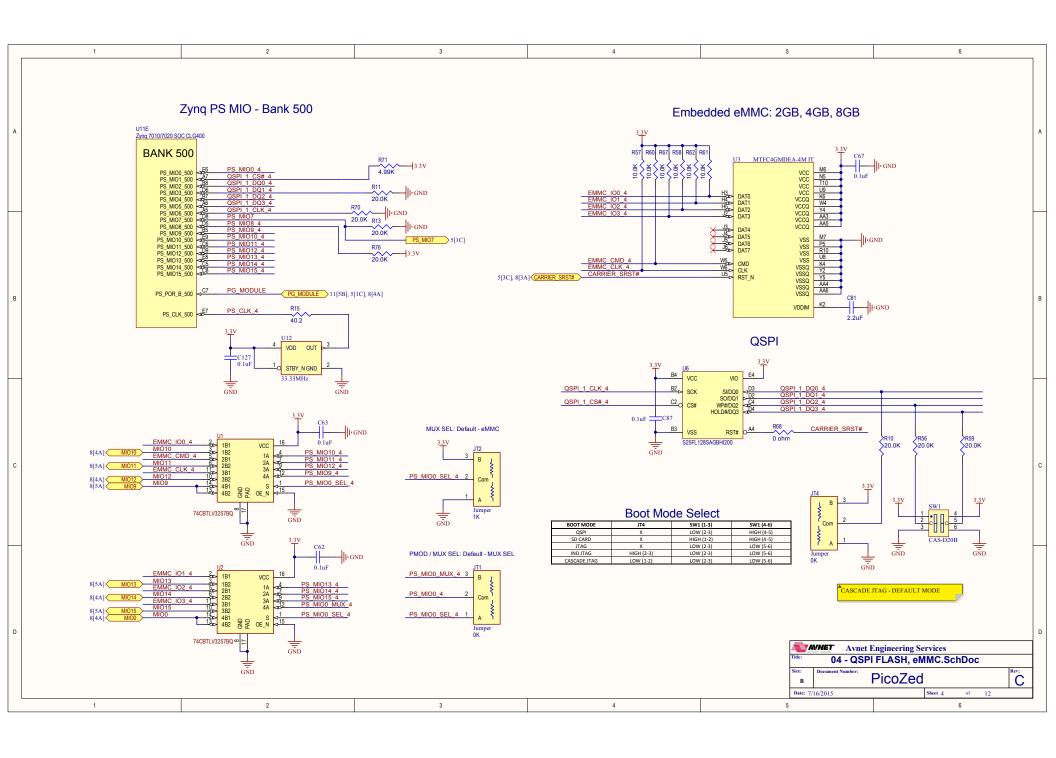
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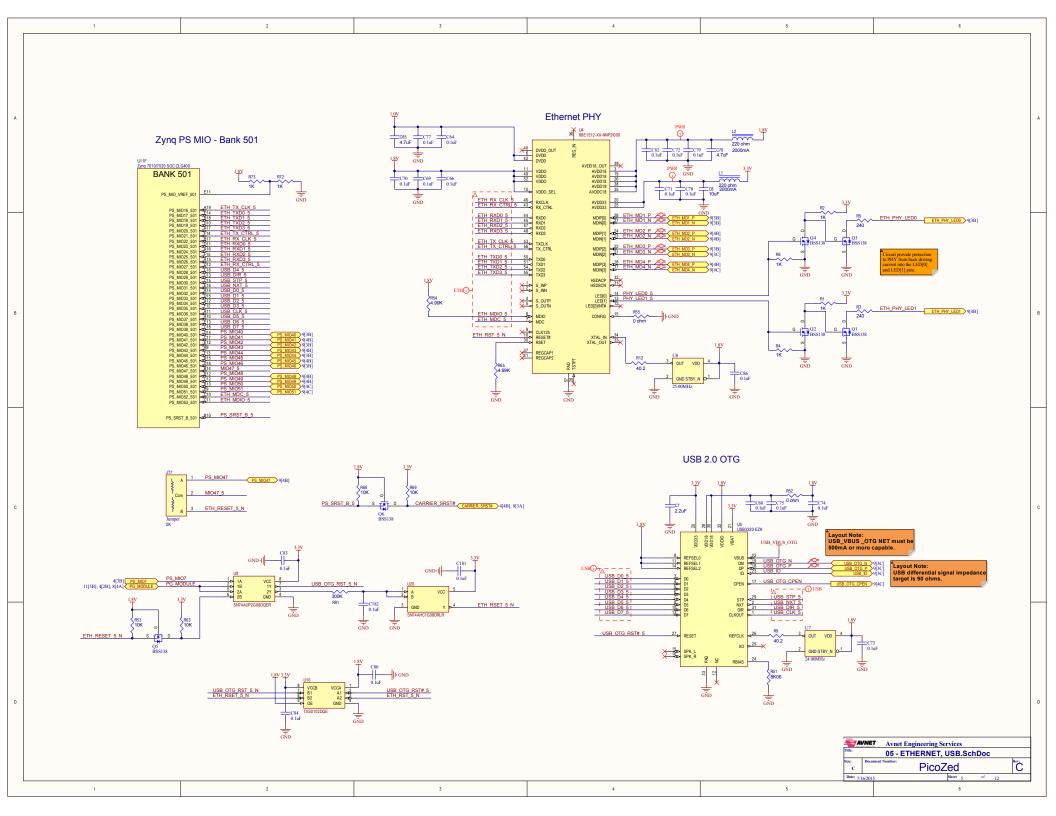
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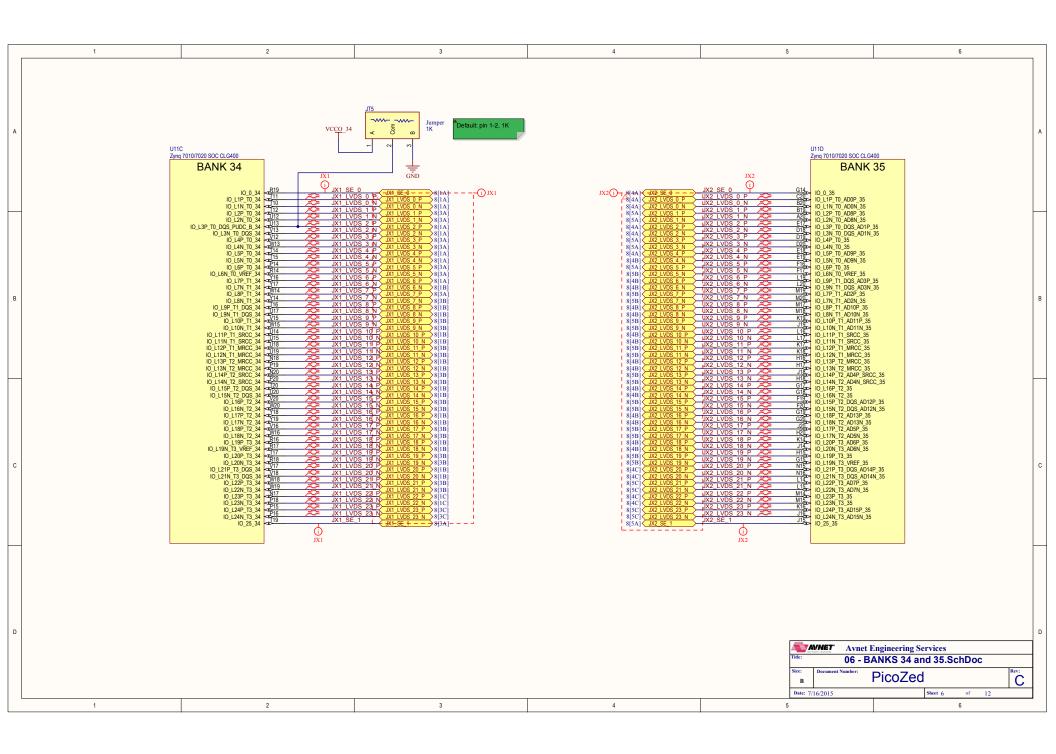
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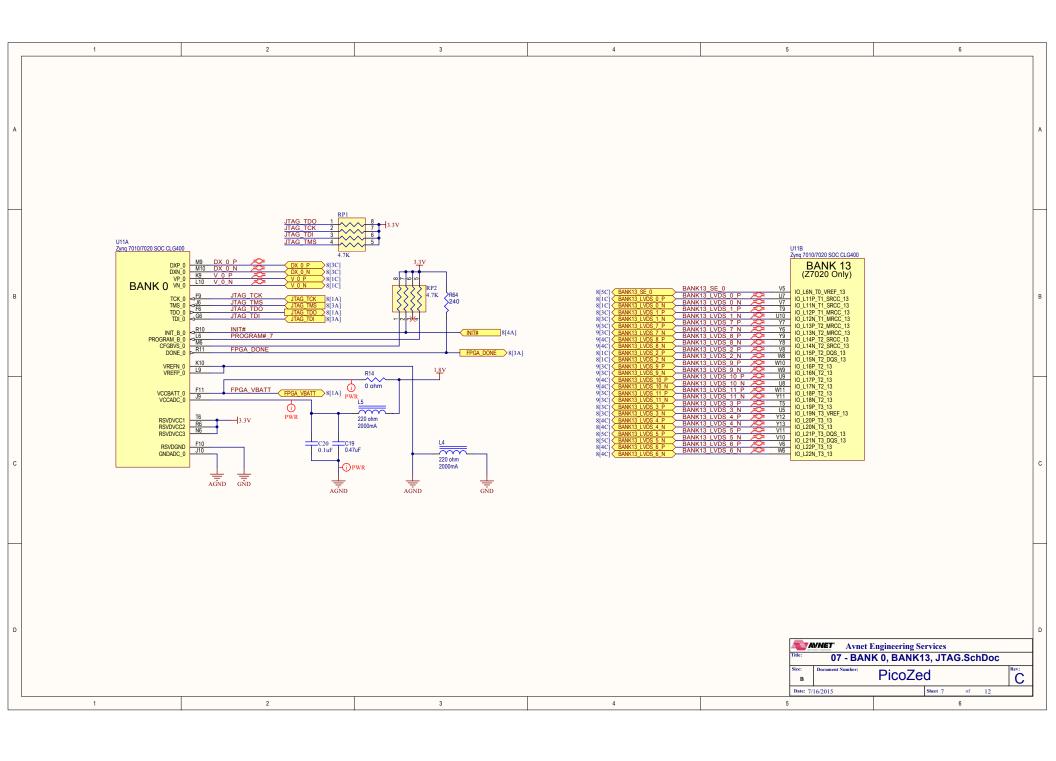


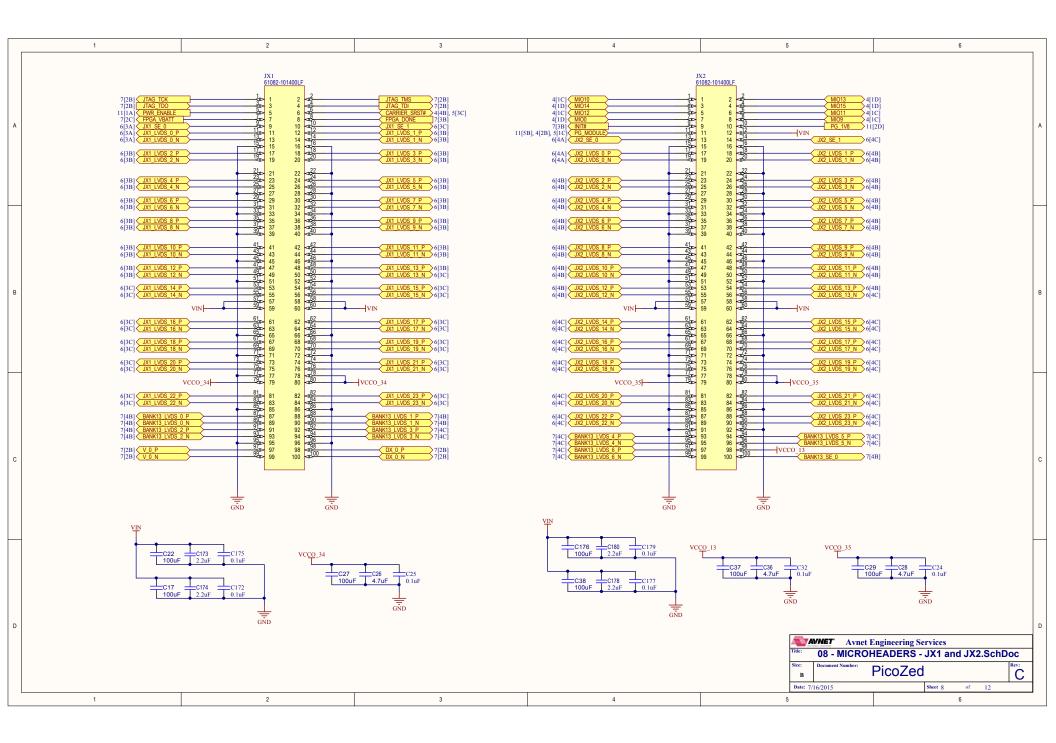


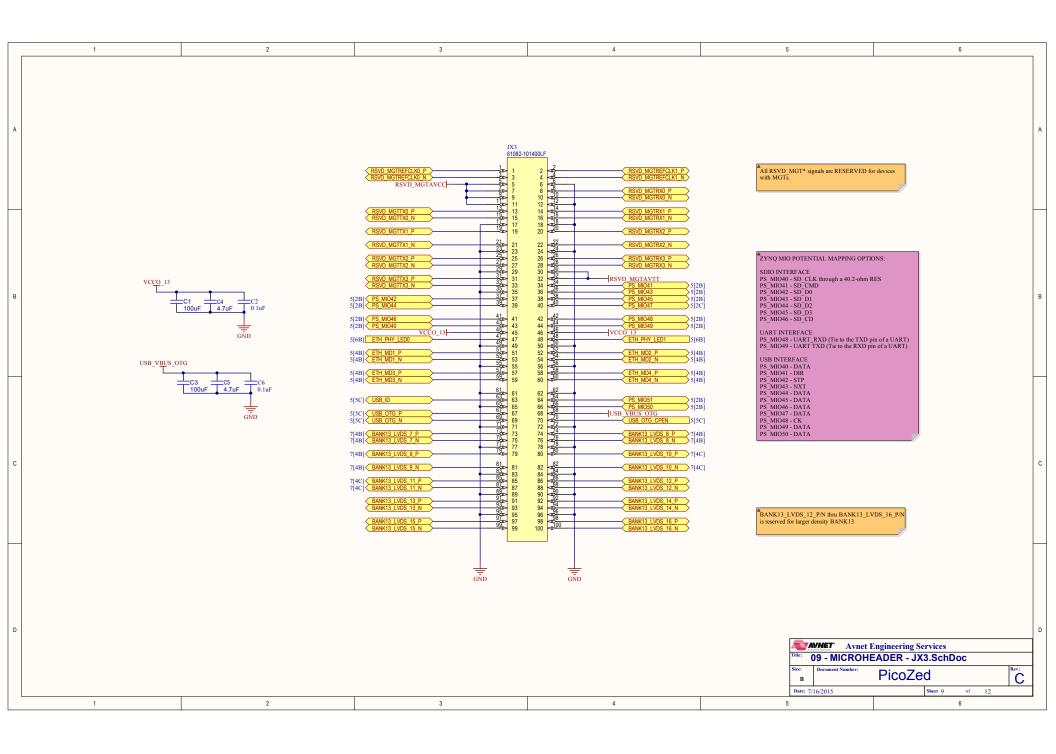


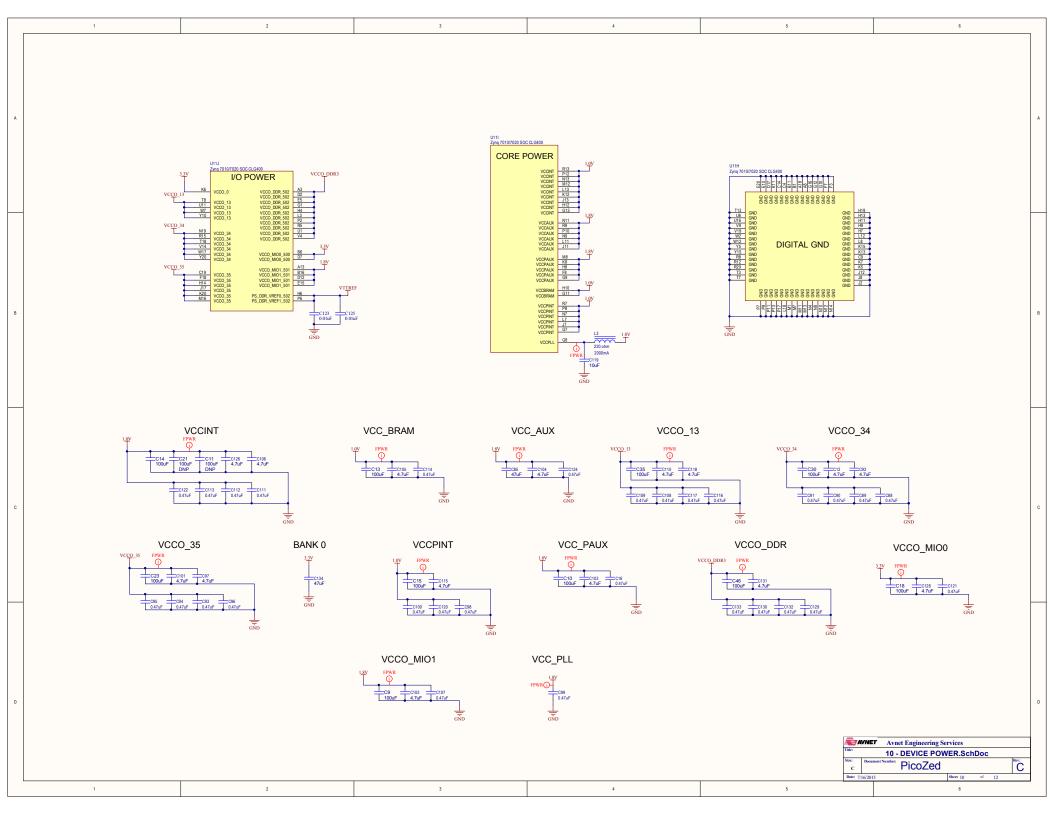


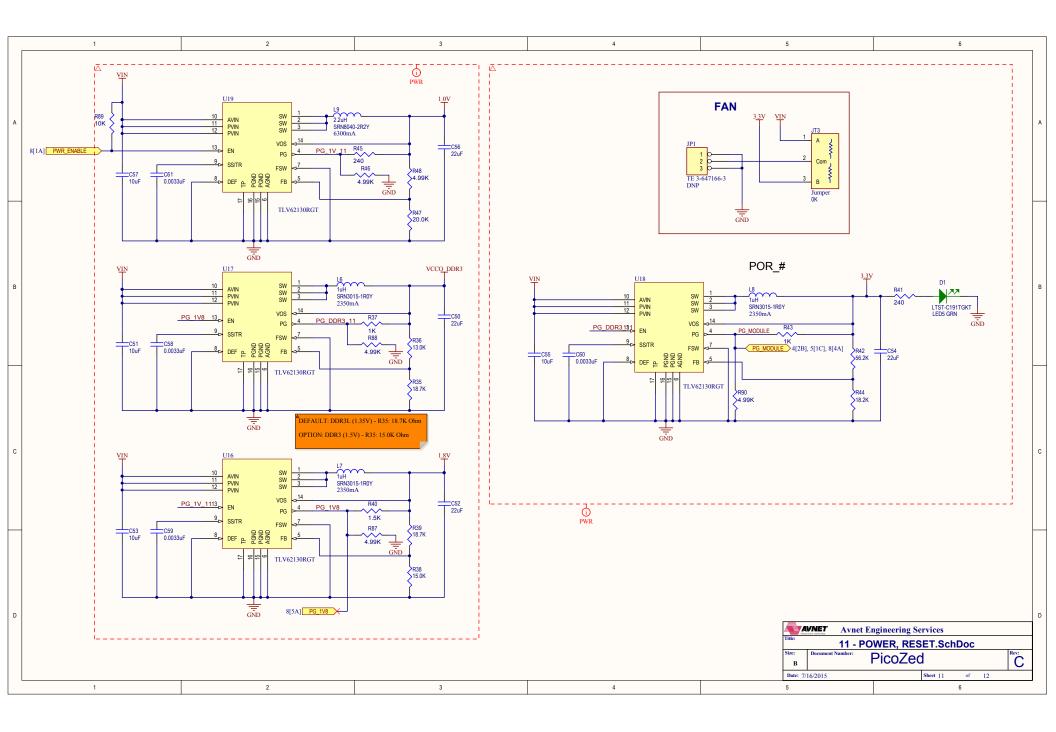












4 Revision Notes:
PicoZed Revision B1:
1) Multiplexed JX2 MIO signals with EMMC
2) Removed Push Button Footprint
3) Incorporated ETHERNET RESET circuit
4) Added bulk cap to AVDD18
5) Added GND Testpoints PicoZed Revision B2: 1) Updated BOOT MODE table PicoZed Revision C: 1) Updated Block Diagram
2) Added shared circuit MIO47 to JX3 and ETHERNET RESET
3) Added RC time constant to ETHERNET RESET Mechanicals: 4) DDR3L / DDR3 Option Added 5) Modified resistor divider value on PG_1V8 6) Changed DDR3 Termination Regulator VLDOIN from 1.8V to 3.3V PicoZed Revision C (Errata): 1) R71: Changed to 4.99K 2) R182: Changed to 0.1uF Z7PZ-Z70x0-PCB-C **PCB Mounting Holes** 0 ohm MOUNTING HOLE 125mil MOUNTING HOLE 125mil DNP MTG6_1²⁵⁰ 0 ohm DNP MOUNTING HOLE 125mil MOUNTING HOLE 125mil DNP **GND Test Points Fansink Mounting Holes** TEST POINT 50mil TEST POINT 50mil MTG4
MOUNTING HOLE MTG3
MOUNTING HOLE GND Avnet Engineering Services 12 - Back Page.SchDoc C PicoZed Date: 7/16/2015 Sheet 12